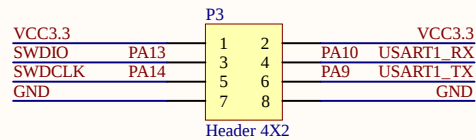


MCU_ABC

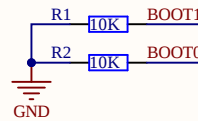
WKUP		PA0	23	U1A	PA0/USART2_CTS/UART4_TX/ETH_MII_CRD/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR/ADC123_IN0/WKUP
		PA1	24		PA1/USART2_RTS/UART4_RX/ETH_RMII_REF_CLK/ETH_MII_RX_CLK/TIM5_CH2/TIM2_CH2/ADC123_IN1
GBC_RX		PA2	25		PA2/USART2_TX/TIM5_CH3/TIM9_CH1/TIM2_CH3/ETH_MDIO/ADC123_IN2
GBC_TX		PA3	26		PA3/USART2_RX/TIM5_CH4/TIM9_CH2/TIM2_CH4/OTG_HS_ULPI_D0/ETH_MII_COL/ADC123_IN3
DCMI_HREF		PA4	29		PA4/SPI1_NSS/SPI3_NSS/USART2_CK/DCMI_HSYNC/OTG_HS_SOF/I2S3_WS/ADC12_IN4/DAC_OUT1
		PA5	30		PA5/SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM8_CH1N/ADC12_IN5/DAC_OUT2
DCMI_PCLK		PA6	31		PA6/SPI1_MISO/TIM8_BKIN/TIM13_CH1/DCMI_PIXCLK/TIM3_CH1/TIM1_BKIN/ADC12_IN6
DCMI_RESET		PA7	32		PA7/SPI1_MOSI/TIM8_CH1N/TIM14_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/ETH_RMII_CRD/ADC12_IN7
DCMI_XCLK		PA8	67		PA8/MCO1/USART1_CK/TIM1_CH1/I2C3_SCL/OTG_FS_SOF
USART1_TX		PA9	68		PA9/USART1_TX/TIM1_CH2/I2C3_SMBA/DCMI_D0/OTG_FS_VBUS
USART1_RX		PA10	69		PA10/USART1_RX/TIM1_CH3/OTG_FS_ID/DCMI_D1
USB_D-		PA11	70		PA11/USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM
USB_D+		PA12	71		PA12/USART1_RTS/CAN1_TX/TIM1_ETR/OTG_FS_DP
SWDIO		PA13	72		PA13/JTMS-SWDIO
SWDCLK		PA14	76		PA14/JTCK-SWCLK
KEY0		PA15	77		PA15/JTDI/SPI3_NSS/I2S3_WS/TIM2_CH1_ETR/SPI1_NSS
T_SCK		PB0	35		PB0/TIM3_CH3/TIM8_CH2N/OTG_HS_ULPI_D1/ETH_MII_RXD2/TIM1_CH2N/ADC12_IN8
T_PEN		PB1	36		PB1/TIM3_CH4/TIM8_CH3N/OTG_HS_ULPI_D2/ETH_MII_RXD3/TIM1_CH3N/ADC12_IN9
BOOT1		PB2	37		PB2/BOOT1
T_MOSI		PB3	89		PB3/JTDO/TRACESWO/SPI3_SCK/I2S3_CK/TIM2_CH2/SPI1_SCK
		PB4	90		PB4/NJTRST/SPI3_MISO/TIM3_CH1/SPI1_MISO/I2S3ext_SD
LCD_BL		PB5	91		PB5/I2C1_SMBA/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/TIM3_CH2/SPI1_MOSI/SPI3_MOSI/DCMI_D10/I2S3_SD
DCMI_D5		PB6	92		PB6/I2C1_SCL/TIM4_CH1/CAN2_TX/DCMI_D5/USART1_TX
DCMI_VSYNC		PB7	93		PB7/I2C1_SDA/FSMC_NL/DCMI_VSYNC/USART1_RX/TIM4_CH2
DCMI_D6		PB8	95		PB8/TIM4_CH3/SDIO_D4/TIM10_CH1/DCMI_D6/ETH_MII_TXD3/I2C1_SCL/CAN1_RX
DCMI_D7		PB9	96		PB9/SPI2_NSS/I2S2_WS/TIM4_CH4/TIM11_CH1/SDIO_D5/DCMI_D7/I2C1_SDA/CAN1_TX
DCMI_SCL		PB10	47		PB10/SPI2_SCK/I2S2_CK/I2C2_SCL/USART3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/TIM2_CH3
DCMI_SDA		PB11	48		PB11/I2C2_SDA/USART3_RX/OTG_HS_ULPI_D4/ETH_RMII_TX_EN/ETH_MII_TX_EN/TIM2_CH4
IIC_SCL		PB12	51		PB12/SPI2_NSS/I2S2_WS/I2C2_SMBA/USART3_CK/TIM1_BKIN/CAN2_RX/OTG_HS_ULPI_D5/ETH_RMII_TXD0/ETH_MII_TXD0/OTG_HS_ID
IIC_SDA		PB13	52		PB13/SPI2_SCK/I2S2_CK/USART3_CTS/TIM1_CH1N/CAN2_TX/OTG_HS_ULPI_D6/ETH_RMII_TXD1/ETH_MII_TXD1/OTG_HS_VBUS
SPI2_CS		PB14	53		PB14/SPI2_MISO/TIM1_CH2N/TIM12_CH1/OTG_HS_DM/USART3_RTS/TIM8_CH2N/I2S2ext_SD
SPI2_SCK		PB15	54		PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N/TIM8_CH3N/TIM12_CH2/OTG_HS_DP/RTC_REFIN
SPI2_MISO					
SPI2_MOSI					
		PC0	15		PC0/OTG_HS_ULPI_STP/ADC123_IN10
		PC1	16		PC1/ETH_MDC/ADC123_IN11
GBC_KEY		PC2	17		PC2/SPI2_MISO/OTG_HS_ULPI_DIR/ETH_MII_TXD2/I2S2ext_SD/ADC123_IN12
GBC_LED		PC3	18		PC3/SPI2_MOSI/I2S2_SD/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/ADC123_IN13
DCMI_PWDN		PC4	33		PC4/ETH_RMII_RX_D0/ETH_MII_RX_D0/ADC12_IN14
T_CS		PC5	34		PC5/ETH_RMII_RX_D1/ETH_MII_RX_D1/ADC12_IN15
DCMI_D0		PC6	63		PC6/I2S2_MCK/TIM8_CH1/SDIO_D6/USART6_TX/DCMI_D0/TIM3_CH1
DCMI_D1		PC7	64		PC7/I2S3_MCK/TIM8_CH2/SDIO_D7/USART6_RX/DCMI_D1/TIM3_CH2
DCMI_D2		PC8	65		PC8/TIM8_CH3/SDIO_D0/TIM3_CH3/USART6_CK/DCMI_D2
DCMI_D3		PC9	66		PC9/I2S3_CKIN/MCO2/TIM8_CH4/SDIO_D1/I2C3_SDA/DCMI_D3/TIM3_CH4
SDIO_D0		PC10	78		PC10/SPI3_SCK/I2S3_CK/UART4_TX/SDIO_D2/DCMI_D8/USART3_TX
SDIO_D1		PC11	79		PC11/UART4_RX/SPI3_MISO/SDIO_D3/DCMI_D4/USART3_RX/I2S3ext_SD
SDIO_D2		PC12	80		PC12/UART5_TX/SDIO_CK/DCMI_D9/SPI3_MOSI/I2S3_SD/USART3_CK
SDIO_D3		PC13	7		PC13/RTC_OUT/RTC_TAMP1/RTC_TS

STM32F103VET6

SWD & USART1

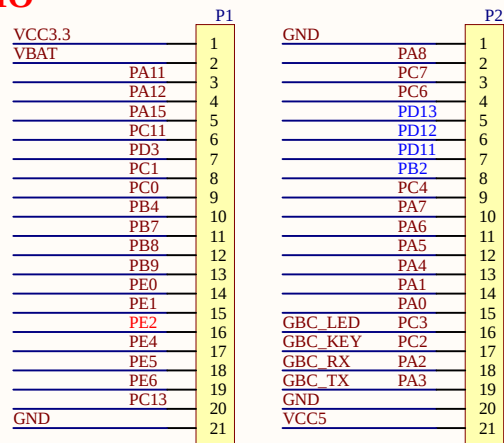


BOOT



Title: M100Z-M3&4.PrjPcb		
Author: ALIENTEK	Size: A4	
Date: 2023/6/26	File: M100Z-M3&4_CPU_ABC.SchDoc	
Revision: V1.1	Version:	

IO



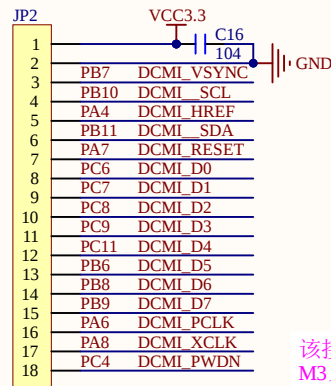
蓝色部分IO口
M7和M3/4存在差异!
无法做到完全兼容!

要兼容, 请用其他的IO口

不考虑兼容, 则所有IO都可以正常使用

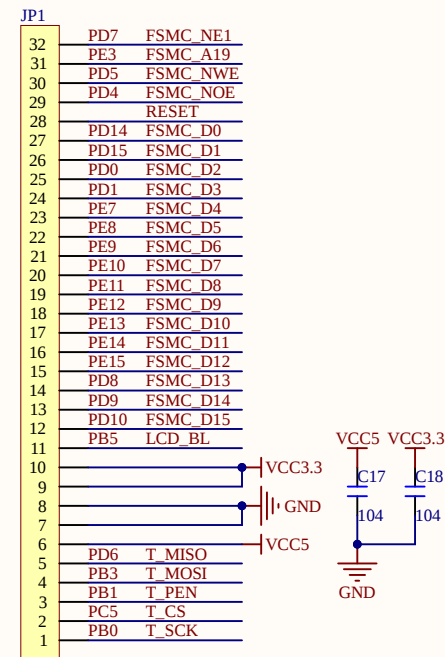
PE2在M7上用作QSPI数据线, 因此在M7上一般也不作IO使用。但是在M3/4上, 则可以当普通IO用

CAMERA

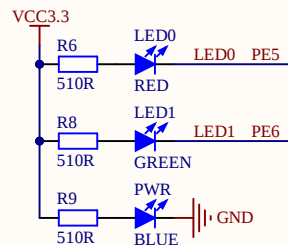


该接口仅对M4芯片有效
M3系列芯片不焊接此接口

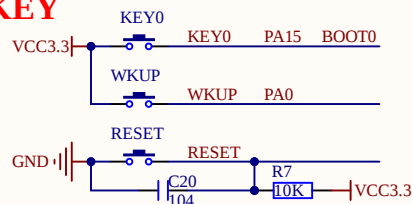
TFTLCD



LED

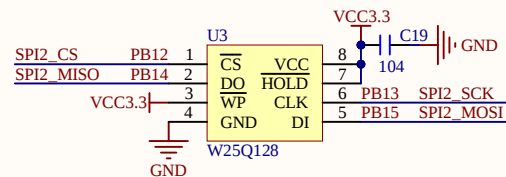


KEY

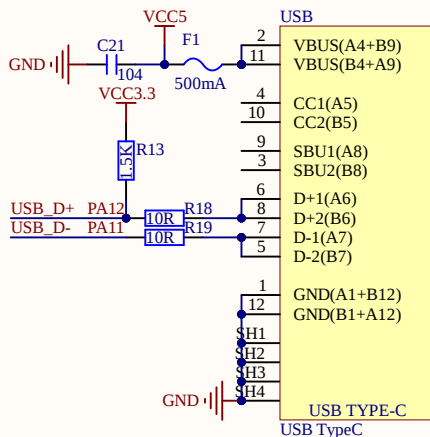


进入默认BootLoader模式方法:
先按住KEY0 (BOOT0=1) 并保持不放
然后再按板子的复位键, 再松开复位键
此时, MCU将进入Bootloader模式

FLASH

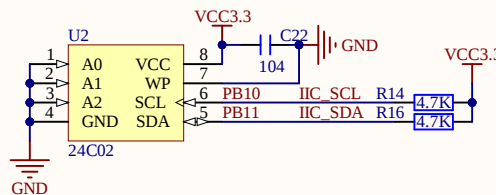


USB & POWER

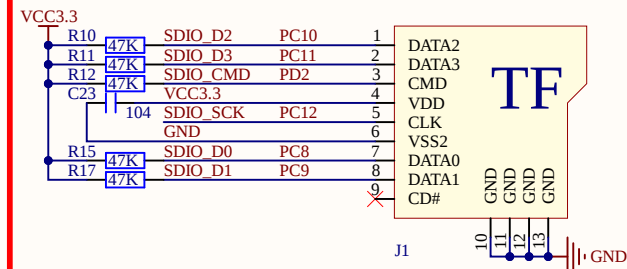


仅当使用F103/E103时
需要焊接R13(1.5K上拉电阻)

EEPROM



TF CARD



Title:
M100Z-M3&4.PrjPcb
Author:
ALIENTEK
Date:
2023/6/26
Revision:
V1.1

Size:
A4
File:
M100Z-M3&4_DEVICE.SchDoc
Version:



