




## MCU - GPIOA/B/C

	WK_UP	PA0	N3	PA0/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/USART2_CTS/UART4_TX/SAI2_SD_B/ETH_MII_CRS/ADC123_IN0/WKUP1
	RMII_REF_CLK	PA1	N2	PA1/TIM2_CH2/TIM5_CH2/USART2_RTS/UART4_RX/QUADSPI_BK1_IO3/SAI2_MCK_B/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/LCD_R2/ADC123_IN1
USART2_TX	ETH_MDIO	PA2	P2	PA2/TIM2_CH3/TIM5_CH3/TIM9_CH1/USART2_TX/SAI2_SCK_B/ETH_MDIO/LCD_R1/ADC123_IN2/WKUP2
USART2_RX	PWM_DAC	PA3	R2	PA3/TIM2_CH4/TIM5_CH4/TIM9_CH2/USART2_RX/OTG_HS_ULPI_D0/ETH_MII_COL/LCD_B5/ADC123_IN3
DCMI_HREF	STM_DAC	PA4	N4	PA4/SP11_NSS/I2S1_WS/SP13_NSS/I2S3_WS/USART2_CK/OTG_HS_SOF/DCMI_HSYNC/LCD_VSYNC/ADC12_IN4/DAC_OUT1
	STM_ADC	PA5	P4	PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SP11_SCK/I2S1_CK/OTG_HS_ULPI_CK/LCD_R4/ADC12_IN5/DAC_OUT2
DCMI_PCLK		PA6	P3	PA6/TIM1_BKIN/TIM3_CH1/TIM8_BKIN/SP11_MISO/TIM13_CH1/DCMI_PIXCLK/LCD_G2/ADC12_IN6
	RMII_CS_DV	PA7	R3	PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/SP11_MOSI/I2S1_SD/TIM14_CH1/ETH_MII_RX_DV/ETH_RMII_CRS_DV/FMC_SDNWE/ADC12_IN7
DCMI_XCLK	REMOTE_IN	PA8	F15	PA8/MCO1/TIM1_CH1/TIM8_BKIN/I2C3_SCL/USART1_CK/OTG_FS_SOF/LCD_R6
	USART1_TX	PA9	E15	PA9/TIM1_CH2/I2C3_SMB/SP12_SCK/I2S2_CK/USART1_TX/DCMI_D0/OTG_FS_VBUS
	USART1_RX	PA10	D15	PA10/TIM1_CH3/USART1_RX/OTG_FS_ID/DCMI_D1
	USB_D-	PA11	C15	PA11/TIM1_CH4/USART1_CTS/CAN1_RX/OTG_FS_DM/LCD_R4
	USB_D+	PA12	B15	PA12/TIM1_ETR/USART1_RTS/SAI2_FS_B/CAN1_TX/OTG_FS_DP/LCD_R5
	JTMS	PA13	A15	PA13/JTMS
	JTCK	PA14	A14	PA14/JTCK
NRF_CS	JTDI	PA15	A13	PA15/JTDI/TIM2_CH1/TIM2_ETR/HDMI_CEC/SP11_NSS/I2S1_WS/SP13_NSS/I2S3_WS/UART4_RTS
	LED1	PB0	R5	PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/UART4_CTS/LCD_R3/OTG_HS_ULPI_D1/ETH_MII_RXD2/ADC12_IN8
	LED0	PB1	R4	PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/LCD_R6/OTG_HS_ULPI_D2/ETH_MII_RXD3/ADC12_IN9
	QSPI_BK1_CLK	PB2	M5	PB2/SAI1_SD_A/SP13_MOSI/I2S3_SD/QUADSPI_CLK
DCMI_SDA	JTDO	PB3	A10	PB3/JTDO/TIM2_CH2/SP11_SCK/I2S1_CK/SP13_SCK/I2S3_CK
DCMI_SCL	JTRST	PB4	A9	PB4/NTRST/TIM3_CH1/SP11_MISO/SP13_MISO/SP12_NSS/I2S2_WS
	LCD_BL	PB5	A8	PB5/TIM3_CH2/I2C1_SMB/SP11_MOSI/I2S1_SD/SP13_MOSI/I2S3_SD/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10
	QSPI_BK1_NCS	PB6	B6	PB6/TIM4_CH1/HDMI_CEC/I2C1_SCL/USART1_TX/CAN2_TX/QUADSPI_BK1_NCS/FMC_SDNE1/DCMI_D5
DCMI_VSYNC		PB7	B5	PB7/TIM4_CH2/I2C1_SDA/USART1_RX/FMC_NL/DCMI_VSYNC
DCMI_D6		PB8	A7	PB8/TIM4_CH3/TIM10_CH1/I2C1_SCL/CAN1_RX/ETH_MII_TXD3/SDMMC1_D4/DCMI_D6/LCD_B6
DCMI_D7		PB9	B4	PB9/TIM4_CH4/TIM11_CH1/I2C1_SDA/SP12_NSS/I2S2_WS/CAN1_TX/SDMMC1_D5/DCMI_D7/LCD_B7
	USART3_TX	PB10	P12	PB10/TIM2_CH3/I2C2_SCL/SP12_SCK/I2S2_CK/USART3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/LCD_G4
	USART3_RX	PB11	R3	PB11/TIM2_CH4/I2C2_SDA/USART3_RX/OTG_HS_ULPI_D4/ETH_MII_TX_EN/ETH_RMII_TX_EN/LCD_G5
NRF_IRQ	GBC_LED	PB12	L13	PB12/TIM1_BKIN/I2C2_SMB/SP12_NSS/I2S2_WS/USART3_CK/CAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMII_TXD0/OTG_HS_ID
	SP12_SCK	PB13	K14	PB13/TIM1_CH1N/SP12_SCK/I2S2_CK/USART3_CTS/CAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMII_RXD1/OTG_HS_VBUS
	SP12_MISO	PB14	R14	PB14/TIM1_CH2N/TIM8_CH2N/SP12_MISO/USART3_RTS/TIM12_CH1/OTG_HS_DM
	SP12_MOSI	PB15	R15	PB15/RTC_REFIN/TIM8_CH3N/TIM8_CH3N/SP12_MOSI/I2S2_SD/TIM12_CH2/OTG_HS_DP
	FMC_SDNWE	PC0	M2	PC0/SAI2_FS_B/OTG_HS_ULPI_STP/FMC_SDNEW/LCD_R5/ADC123_IN10
	ETH_MDC	PC1	M3	PC1/SP12_MOSI/I2S2_SD/SAI1_SD_A/ETH_MDC/ADC123_IN11/RTC_RAMP3/WKUP3
	FMC_SDNE0	PC2	M4	PC2/SP12_MISO/OTG_HS_ULPI_DIR/ETH_MII_TXD2/FMC_SDNE0/ADC123_IN12
	FMC_SDCKE0	PC3	L4	PC3/SP12_MOSI/I2S2_SD/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/FMC_SDCKE0/ADC123_IN13
	RMII_RXD0	PC4	N5	PC4/I2S1_MCK/SPDIFRX_IN2/ETH_MII_RXD0/ETH_RMII_RXD0/FMC_SDNE0/ADC12_IN14
	RMII_RXD1	PC5	P5	PC5/SPDIFRX_IN3/ETH_MII_RXD1/ETH_RMII_RXD1/FMC_SDCKE0/ADC12_IN15
	DCMI_D0	PC6	H15	PC6/TIM3_CH1/TIM8_CH1/I2S2_MCK/USART6_TX/SDMMC1_D6/DCMI_D0/LCD_HSYNC
	DCMI_D1	PC7	G15	PC7/TIM3_CH2/TIM8_CH2/I2S3_MCK/USART6_RX/SDMMC1_D7/DCMI_D1/LCD_G6
SD1_D0	DCMI_D2	PC8	G14	PC8/TIM3_CH3/TIM8_CH3/UART5_RTS/USART6_CK/SDMMC1_D0/DCMI_D2
SD1_D1	DCMI_D3	PC9	F14	PC9/MCO2/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/UART5_CTS/QUADSPI_BK1_IO0/SDMMC1_D1/DCMI_D3
SD1_D2		PC10	B14	PC10/SP13_SCK/I2S3_CK/USART3_TX/UART4_TX/QUADSPI_BK1_IO1/SDMMC1_D2/DCMI_D8/LCD_R2
SD1_D3	DCMI_D4	PC11	B13	PC11/SP13_MISO/USART3_RX/UART4_RX/QUADSPI_BK2_NCS/SDMMC1_D3/DCMI_D4
SD1_CLK		PC12	A12	PC12/SP13_MOSI/I2S3_SD/USART3_CK/UART5_TX/SDMMC1_CK/DCMI_D9
	KEY2	PC13	D1	PC13/RTC_TAMP1/RTC_TS/RTC_OUT/WKUP4

STM32F750N8H6

Title:	ATK_STM32F750_CORE_BOARD	 <b>正点原子</b>
Author:	ALIENTEK	
Date:	2024/10/14	
Revision:	*	
Size:	A4	
File:	STM32F750N8_PIN_ABC_SchDoc	
Version:	V1.3	



MCU - GPIOD/E/F

U7C

FMC_D2	PD0	B12	PD0/CAN1_RX/FMC_D2
FMC_D3	PD1	C12	PD1/CAN1_TX/FMC_D3
SD1_CMD	PD2	D12	PD2/TIM3_ETR/UART5_RX/SDMMC1_CMD/DCMI_D11
DCMI_D5	PD3	C11	PD3/SPI2_SCK/I2S2_CK/USART2_CTS/FMC_CLK/DCMI_D5/LCD_G7
FMC_NOE	PD4	D11	PD4/USART2_RTS/FMC_NOE
FMC_NWE	PD5	C10	PD5/USART2_TX/FMC_NWE
FMC_NWAIT	PD6	B11	PD6/SPI3_MOSI/I2S3_SD/SAI1_SD_A/USART2_RX/FMC_NWAIT/DCMI_D10/LCD_B2
FMC_NE1	PD7	A11	PD7/USART2_CK/SPDIFRX_IN0/FMC_NE1
FMC_D13	PD8	L15	PD8/USART3_TX/SPDIFREX_IN11/FMC_D13
FMC_D14	PD9	L14	PD9/USART3_RX/FMC_D14
FMC_D15	PD10	K15	PD10/USART3_CK/FMC_D15/LCD_B3
FMC_A16_CLE	PD11	N10	PD11/I2C4_SMBA/USART3_CTS/QUADSPI_BK1_IO0/SAI2_SD_A/FMC_D16/FMC_CLE
FMC_A17_ALE	PD12	M10	PD12/TIM4_CH1/LPTIM1_IN1/I2C4_SCL/USART3_RTS/QUADSPI_BK1_IO1/SAI2_FS_A/FMC_A17/FMC_ALE
FMC_A18	PD13	M11	PD13/TIM4_CH2/LPTIM1_OUT/I2C4_SDA/QUADSPI_BK1_IO3/SAI2_SCK_A/FMC_A18
FMC_D0	PD14	L12	PD14/TIM4_CH3/UART8_CTS/FMC_D0
FMC_D1	PD15	K13	PD15/TIM4_CH4/UART8_RTS/FMC_D1
FMC_NBL0	PE0	A6	PE0/TIM4_ETR/LPTIM1_ETR/UART8_RX/SAI2_MCK_A/FMC_NBL0/DCMI_D2
FMC_NBL1	PE1	A5	PE1/LPTIM1_IN2/UART8_TX/FMC_NBL1/DCMI_D3
SAI1_MCLKA	PE2	A3	PE2/SPI4_SCK/SAI1_MCLK_A/QUADSPI_BK1_IO2/ETH_MII_TXD3/FMC_A23
SAI1_SDB	PE3	A2	PE3/SAI1_SD_B/FMC_A19
SAI1_FSA	PE4	A1	PE4/SPI4_NSS/SAI1_FS_A/FMC_A20/DCMI_D4/LCD_B0
SAI1_SCKA	PE5	B1	PE5/TIM9_CH1/SPI4_MISO/SAI1_SCK_A/FMC_A21/DCMI_D6/LCD_G0
SAI1_SDA	PE6	B2	PE6/TIM1_BKIN2/TIM9_CH2/SPI4_MOSI/SAI1_SD_A/SAI2_MCK_B/FMC_A22/DCMI_D7/LCD_G1
FMC_D4	PE7	R8	PE7/TIM1_ETR/UART7_RX/QUADSPI_BK2_IO0/FMC_D4
FMC_D5	PE8	N9	PE8/TIM1_CH1N/UART7_TX/QUADSPI_BK2_IO1/FMC_D5
FMC_D6	PE9	P9	PE9/TIM1_CH1/UART7_RTS/QUADSPI_BK2_IO2/FMC_D6
FMC_D7	PE10	R9	PE10/TIM1_CH2N/UART7_CTS/QUADSPI_BK2_IO3/FMC_D7
FMC_D8	PE11	P10	PE11/TIM1_CH2/SPI4_NSS/SAI2_SD_B/FMC_D8/LCD_G3
FMC_D9	PE12	R10	PE12/TIM1_CH3N/SPI4_SCK/SAI2_SCK_B/FMC_D9/LCD_B4
FMC_D10	PE13	R12	PE13/TIM1_CH3/SPI4_MISO/SAI2_FS_B/FMC_D10/LCD_DE
FMC_D11	PE14	P11	PE14/TIM1_CH4/SPI4_MOSI/SAI2_MCK_B/FMC_D11/LCD_CLK
FMC_D12	PE15	R11	PE15/TIM1_BKIN/FMC_D12/LCD_R7
FMC_A0	PF0	D2	PF0/I2C2_SDA/FMC_A0
FMC_A1	PF1	E2	PF1/I2C2_SCL/FMC_A1
FMC_A2	PF2	G2	PF2/I2C2_SMBA/FMC_A2
FMC_A3	PF3	H2	PF3/FMC_A3/ADC3_IN9
FMC_A4	PF4	J2	PF4/FMC_A4/ADC3_IN14
FMC_A5	PF5	K3	PF5/FMC_A5/ADC3_IN15
QSPI_BK1_IO3	PF6	K2	PF6/TIM10_CH1/SPI5_NSS/SAI1_SD_B/UART7_RX/QUADSPI_BK1_IO03/ADC3_IN4
QSPI_BK1_IO2	PF7	K1	PF7/TIM11_CH1/SPI5_SCK/SAI1_MCLK_B/UART7_TX/QUADSPI_BK1_IO02/ADC3_IN5
QSPI_BK1_IO0	PF8	L3	PF8/SPI5_MISO/SAI1_SCK_B/UART7_RTS/TIM13_CH1/QUADSPI_BK1_IO0/ADC_IN6
QSPI_BK1_IO1	PF9	L2	PF9/SPI5_MOSI/SAI1_FS_B/UART7_CTS/TIM14_CH1/QUADSPI_BK1_IO1/ADC3_IN7
F_CS	PF10	L1	PF10/DCMI_D11/LCD_DE/ADC3_IN8
FMC_SDNRAS	PF11	P8	PF11/SPI5_MOSI/SAI2_SD_B/FMC_SDNRAS/DCMI_D12
FMC_A6	PF12	M6	PF12/FMC_A6
FMC_A7	PF13	N6	PF13/I2C4_SMBA/FMC_A7
FMC_A8	PF14	P6	PF14/I2C4_SCL/FMC_A8
FMC_A9	PF15	M8	PF15/I2C4_SDA/FMC_A9

STM32F750N8H6

Title: ATK_STM32F750_CORE_BOARD	
Author: ALIENTEK	Size: A4
Date: 2024/10/14	File: STM32F750N8_PIN_DEF.SchDoc
Revision: *	Version: V1.3



MCU - GPIOG/H/I/J/K

U7D

FMC_A10	PG0	N7
FMC_A11	PG1	M7
FMC_A12	PG2	M13
T_MISO	PG3	M12
FMC_BA0	PG4	N12
FMC_BA1	PG5	N11
NRF_CE	PG6	J15
T_MOSI	PG7	J14
FMC_SDCLK	PG8	H14
FMC_NCE3	PG9	D9
I2C_INT	PG10	C8
RMII_TX_EN	PG11	B8
DCMI_RESET	PG12	C7
RMII_TXD0	PG13	B3
RMII_TXD1	PG14	A4
FMC_SDNCA	PG15	B7
KEY1	PH2	K4
KEY0	PH3	J4
IIC_SCL	PH4	H4
IIC_SDA	PH5	J3
T_SCK	PH6	P13
T_PEN	PH7	N13
FMC_D16	PH8	P14
FMC_D17	PH9	N14
FMC_D18	PH10	P15
FMC_D19	PH11	N15
FMC_D20	PH12	M15
FMC_D21	PH13	E12
FMC_D22	PH14	E13
FMC_D23	PH15	D13
FMC_D24	PI0	E14
FMC_D25	PI1	D14
FMC_D26	PI2	C14
FMC_D27	PI3	C13
FMC_NBL2	PI4	C3
FMC_NBL3	PI5	D3
FMC_D28	PI6	D6
FMC_D29	PI7	D4
T_CS	PI8	C2
FMC_D30	PI9	E4
FMC_D31	PI10	D5
GBC_KEY	PI11	F3
LCD_HSYNC	PI12	E3
LCD_VSYNC	PI13	G3
LCD_CLK	PI14	H3
LCD_R0	PI15	G4

PG0/FMC_A10
PG1/FMC_A11
PG2/FMC_A12
PG3/FMC_A13
PG4/FMC_A14/FMC_BA0
PG5/FMC_A15/FMC_BA1
PG6/DCMI_D12/LCD_R7
PG7/USART6_CK/FMC_INT/DCMI_D13/LCD_CLK
PG8/SPI6_NSS/SPIDIFRX_IN2/USART6_RTS/ETH_PPS_OUT/FMC_SDCLK
PG9/SPIDIFRX_IN3/USART6_RX/QUADSPI_BK2_IO2/SAI2_FS_B/FMC_NE2/FMC_NCE/DCMI_VSYNC
PG10/LCD_G3/SAI2_SD_B/FMC_NE3/DCMI_D2/LCD_B2
PG11/SPIDIFRX_IN0/ETH_MII_TX_EN/ETH_RMII_TX_EN/DCMI_D3/LCD_B3
PG12/LPTIM1_IN1/SPI6_MISO/SPIDIFRX_IN1/USART6_RTS/LCD_B4/FMC_NE4/LCD_B1
PG13/LPTIM1_OUT/SPI6_SCK/USART6_CTS/ETH_MII_TXD0/ETH_RMII_TXD0/FMC_A24/LCD_R0
PG14/LPTIM1_ETR/SPI6_MOSI/USART6_TX/QUADSPI_BK2_IO3/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_A25/LCD_B0
PG15/USART6_CTS/FMC_SDNCA/DCMI_D13
PH2/LPTIM1_IN2/QUADSPI_BK2_IO0/SAI2_SCK_B/ETH_MII_CRS/FMC_SDCKE0/LCD_R0
PH3/QUADSPI_BK2_IO1/SAI2_MCK_B/ETH_MII_COL/FMC_SDNE0/LCD_R1
PH4/I2C2_SCL/OTG_HS_ULPI_NXT
PH5/I2C2_SDA/SPI5_NSS/FMC_SDNWE
PH6/I2C2_SMB/SAI5_SCK/TIM12_CH1/ETH_MII_RXD2/FMC_SDNE1/DCMI_D8
PH7/I2C3_SCL/SPI5_MISO/ETH_MII_RXD3/FMC_SDCKE1/DCMI_D9
PH8/I2C3_SDA/FMC_D16/DCMI_HSYNC/LCD_R2
PH9/I2C3_SMB/TIM12_CH2/FMC_D17/DCMI_D0/LCD_R3
PH10/TIM5_CH1/I2C4_SMB/FMC_D18/DCMI_D1/LCD_R4
PH11/TIM5_CH2/I2C4_SCL/FMC_D19/DCMI_D2/LCD_R5
PH12/TIM5_CH3/I2C4_SDA/FMC_D20/DCMI_D3/LCD_R6
PH13/TIM8_CH1N/CAN1_TX/FMC_D21/LCD_G2
PH14/TIM8_CH2N/FMC_D22/DCMI_D4/LCD_G3
PH15/TIM8_CH3N/FMC_D23/DCMI_D11/LCD_G4
PI0/TIM5_CH4/SPI2_NSS/I2S2_WS/FMC_D24/DCMI_D13/LCD_G5
PI1/TIM8_BKIN2/SPI2_SCK/I2S2_CK/FMC_D25/DCMI_D8/LCD_G6
PI2/TIM8_CH4/SPI2_MISO/FMC_D26/DCMI_D9/LCD_G7
PI3/TIM8_ETR/SPI2_MOSI/I2S2_SD/FMC_D27/DCMI_D10
PI4/TIM8_BKIN/SAI2_MCK_A/FMC_NBL2/DCMI_D5/LCD_B4
PI5/TIM8_CH1/SAI2_SCK_A/FMC_NBL3/DCMI_VSYNC/LCD_B5
PI6/TIM8_CH2/SAI2_SD_A/FMC_D28/DCMI_D6/LCD_B6
PI7/TIM8_CH3/SAI2_FS_A/FMC_D29/DCMI_D7/LCD_B7
PI8/RTC_TAMP2/RTC_TS/WKUP5
PI9/CAN1_RX/FMC_D30/LCD_VSYNC
PI10/ETH_MII_RX_ER/FMC_D31/LCD_HSYNC
PI11/OTG_HS_ULPI_DIR/WKUP6
PI12/LCD_HSYNC
PI13/LCD_VSYNC
PI14/LCD_CLK
PI15/LCD_R0

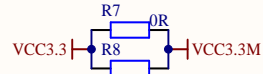
STM32F750N8H6

PJ0/LCD_R1	R6	PJ0	LCD_R1
PJ1/LCD_R2	R7	PJ1	LCD_R2
PJ2/LCD_R3	P7	PJ2	LCD_R3
PJ3/LCD_R4	N8	PJ3	LCD_R4
PJ4/LCD_R5	M9	PJ4	LCD_R5
PJ5/LCD_R6	M14	PJ5	LCD_R6
PJ6/LCD_R7	K12	PJ6	LCD_R7
PJ7/LCD_G0	J12	PJ7	LCD_G0
PJ8/LCD_G1	H12	PJ8	LCD_G1
PJ9/LCD_G2	J13	PJ9	LCD_G2
PJ10/LCD_G3	H13	PJ10	LCD_G3
PJ11/LCD_G4	G12	PJ11	LCD_G4
PJ12/LCD_B0	B10	PJ12	LCD_B0
PJ13/LCD_B1	B9	PJ13	LCD_B1
PJ14/LCD_B2	C9	PJ14	LCD_B2
PJ15/LCD_B3	D10	PJ15	LCD_B3
PK0/LCD_G5	G13	PK0	LCD_G5
PK1/LCD_G6	F12	PK1	LCD_G6
PK2/LCD_G7	F13	PK2	LCD_G7
PK3/LCD_B4	D8	PK3	LCD_B4
PK4/LCD_B5	D7	PK4	LCD_B5
PK5/LCD_B6	C6	PK5	LCD_B6
PK6/LCD_B7	C5	PK6	LCD_B7
PK7/LCD_DE	C4	PK7	LCD_DE

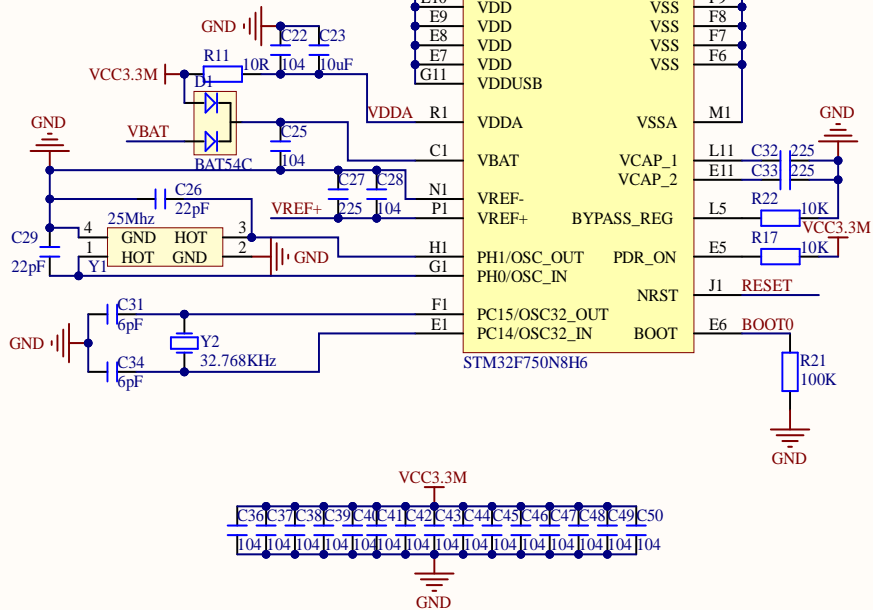
Title:	
ATK_STM32F750_CORE_BOARD	
Author:	Size:
ALIENTEK	A4
Date:	File:
2024/10/14	STM32F750N8_PIN_GHIJK_SchDoc
Revision:	Version:
*	V1.3



## MCU - POWER

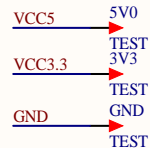


R8/R9 是为了维修方便而增加的2个电阻，大家实际设计电路板时候，可以不加这两个电阻。VCC3.3M 直接连接3.3 V即可

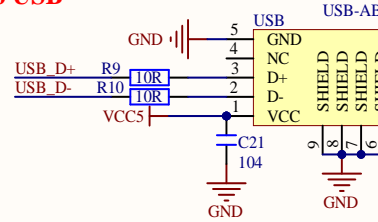


## TEST POINT

这是三个电源电压测试点。用来测试核心板的电源是否正常。也可以用来给核心板供电：焊接 GND 和5V，然后接外部5V电压即可。



## Micro USB

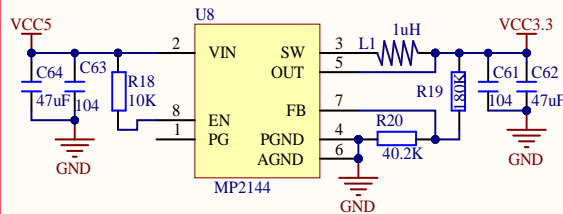


此MicroUSB 接口有如下功能：

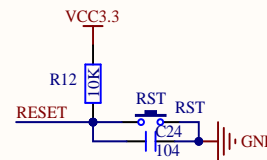
- 1, 单独使用核心板时，可给核心板供电。
- 2, 可做USB Slave 接口，连接电脑，同时也可以供电。
- 3, 可做USB Host 接口（需MicroUSB 转OTG线），接U盘等。

注意：通过主板上的 P5可以控制 VREF+。

## DC-DC POWER



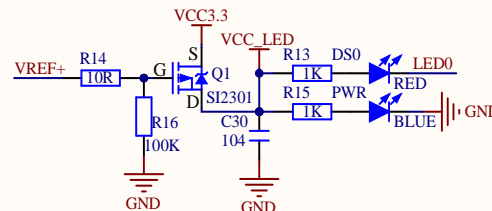
## RESET



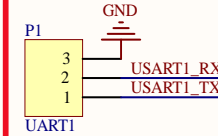
## KEY



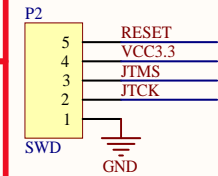
## LED



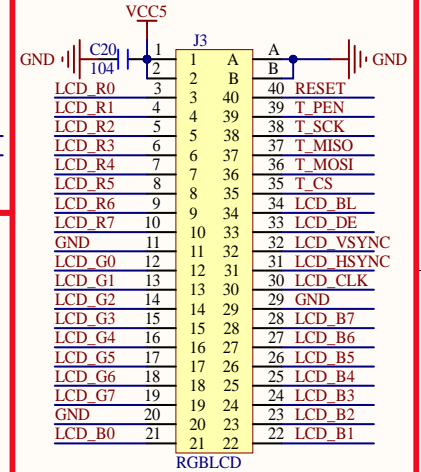
## USART1



## SWD



## RGBLCD



## MOTHER BOARD CON

PA6	J2	VREF+	VCC5	J1	GND
PA4	30	PD5	VCC5	30	GND
PA5	29	PD4	VBAT	29	PI15
PH4	28	PD7	PB8	28	PI0
PH5	27	PA3	PB9	27	PI1
PB1	26	PC1	PB7	26	PI2
PB0	25	PC4	PE2	25	PI3
PH3	24	PC5	PB3	24	PI4
PH2	23	PA2	PB4	23	PI5
PC13	22	PA7	PG10	22	PI6
PE3	21	PA1	PG14	21	PI7
PE4	20	PI11	PG13	20	PI8
PE6	19	PB13	PG11	19	PI9
PE5	18	PB14	PG12	18	PI10
PD14	17	PB12	BOOT0	17	PI11
PD15	16	PB15	PD3	16	PK0
PD0	15	PB11	PD2	15	PK1
PD1	14	PB10	PC8	14	PK2
PE7	13	PD13	PC12	13	PI12
PE8	12	PC7	PC9	12	PI13
PE9	11	PC6	PC10	11	PI14
PE10	10	PG6	PC11	10	PI15
PE11	9	PA8	PB5	9	PK3
PE12	8	PA12	PB8	8	PK4
PE13	7	PA11	PG7	7	PK5
PE14	6	PA10	PG3	6	PK6
PD8	5	PA9	PH6	5	PK7
PD9	4	PA13	PH7	4	PI14
PD10	3	PA14	PA0	3	PI13
	2	PA15	RESET	2	PI12
	1			1	

Title:		3710F060046G3FT01	
Author:		3710F060046G3FT01	
Date:		3710F060046G3FT01	
Revision:		3710F060046G3FT01	



52.00mm

42.00mm

