

MCU_A

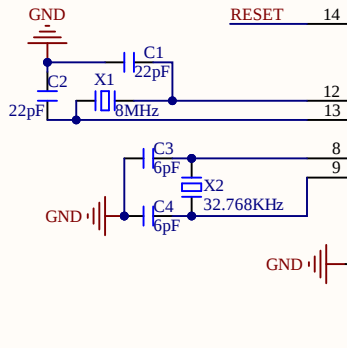
	WK_UP	PA0	23	U1A	
	KEY0	PA1	24	PA0/TIM2_CH1/TIM5_CH1/TIM8_ETR/TIM15_BKIN/SPI6_NSS/SPI3_RDY/USART2_CTS/USART2_NSS/UART4_TX/SAI2_SD_B/ETH_CRS/TIM2_ETR/ADC12_INP0/ADC12_INN1/TAMP_IN2/TAMP_OUT1/WKUP1	
	GBC_RX	PA2	25	PA1/TIM2_CH2/TIM15_CH2/TIM15_CH1N/LPTIM1_IN1/OCTOSPI1_DQS/USART2_RTS/UART4_RX/OCTOSPI1_IO3/SAI2_MCLK_B/ETH_REF_CLK/ETH_RX_CLK/ADC12_INP1/TAMP_IN5/TAMP_OUT4	
	GBC_TX	PA3	26	PA2/TIM2_CH3/TIM5_CH3/TIM15_CH1/LPTIM1_IN2/USART2_TX/SAI2_SCK_B/ETH_MDIO/ADC12_INP14/TAMP_IN4/TAMP_OUT3/WKUP2	
DCMI_HREF	STM_DAC	PA4	29	PA3/TIM2_CH4/TIM5_CH4/OCTOSPI1_CLK/TIM15_CH2/I2S2_WS/SPI2_NSS/SAI1_SD_B/USART2_RX/ETH_COL/ADC12_INP15	
	STM_ADC	PA5	30	PA4/TIM5_ETR/LPTIM2_CH1/I2S1_WS/SPI1_NSS/I2S3_WS/SPI3_NSS/USART2_CK/SPI6_NSS/DCMI_HSYNC/PSSI_DE/ADC12_INP18/DAC1_OUT1	
DCMI_PCLK		PA6	31	PA5/TIM2_CH1/TIM8_CH1N/I2S1_CK/SPI1_SCK/SPI6_SCK/ETH_TX_EN/PSSI_D14/TIM2_ETR/ADC12_INP19/ADC12_INN18/DAC1_OUT2	
	T_MOSI	PA7	32	PA6/TIM1_BKIN/TIM3_CH1/TIM8_BKIN/I2S1_SDI/SPI1_MISO/OCTOSPI1_IO3/USART11_TX/SPI6_MISO/TIM13_CH1/DCMI_PIXCLK/PSSI_PDCK/ADC12_INP3	
DCMI_XCLK	REMOTE_IN	PA8	67	PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/I2S1_SDO/SPI1_MOSI/USART11_RX/SPI6_MOSI/TIM14_CH1/OCTOSPI1_IO2/ETH_CRS_DV/ETH_RX_DV/FMC_SDNWE/FMC_NWE/ADC12_INP7/ADC12_INN3	
	USART1_TX	PA9	68	PA8/MCO_1/TIM1_CH1/TIM8_BKIN/I2C3_SCL/SPI1_RDY/USART1_CK/USB_SOF/UART7_RX/FMC_NOE/DCMI_D3/PSSI_D3	
	USART1_RX	PA10	69	PA9/TIM1_CH2/LPUART1_TX/I2C3_SMB_A/I2S2_CK/SPI2_SCK/USART1_TX/ETH_TX_ER/FMC_NWE/DCMI_D0/PSSI_D0/UCPD1_DB1	
	USB_DM	PA11	70	PA10/TIM1_CH3/LPUART1_RX/LPTIM2_IN2/UCPD1_FRSTX/USART1_RX/FDCAN2_TX/SDMMC1_D0/DCMI_D1/PSSI_D1	
	USB_DP	PA12	71	PA11/TIM1_CH4/LPUART1_CTS/I2S2_WS/SPI2_NSS/UART4_RX/USART1_CTS/USART1_NSS/FDCAN1_RX/USB_DM	
	SWD	PA13	72	PA12/TIM1_ETR/LPUART1_RTS/I2S2_CK/SPI2_SCK/UART4_TX/USART1_RTS/SAI2_FS_B/FDCAN1_TX/USB_DP	
	SWC	PA14	76	PA13/SWDIO	
	KEY1	PA15	77	PA14/SWCLK	
				PA15/TIM2_CH1/LPTIM3_IN2/CEC/I2S1_WS/SPI1_NSS/I2S3_WS/SPI3_NSS/SPI6_NSS/UART4_DE/UART4_RTS/UART7_TX/FMC_NBL1/DCMI_D11/PSSI_D11/TIM2_ETR	
	T_SCK	PB0	35		
	T_PEN	PB1	36	PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/OCTOSPI1_IO1/USART11_CK/UART4_CTS/ETH_RXD2/LPTIM3_CH1/ADC12_INP9/ADC12_INN5	
	QSPI_BK1_CLK	PB2	37	PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/OCTOSPI1_IO0/ETH_RXD3/LPTIM3_CH2/ADC12_INP5	
	IIC_SDA	PB3	89	PB2/RTC_OUT2/SAI1_D1/TIM8_CH4N/SPI1_RDY/LPTIM1_CH1/SAI1_SD_A/I2S3_SDO/SPI3_MOSI/OCTOSPI1_CLK/OCTOSPI1_DQS/SDMMC1_CMD/LPTIM5_ETR/LSCO	
	LED0	PB4	90	PB3/TIM2_CH2/I2C2_SDA/I2S1_CK/SPI1_SCK/I2S3_CK/SPI3_SCK/UART12_CTS/SPI6_SCK/CRS_SYNC/UART7_RX/LPTIM6_ETR	
	LED1	PB5	91	PB4/TIM16_BKIN/TIM3_CH1/OCTOSPI1_CLK/LPTIM1_CH2/I2S1_SDI/SPI1_MISO/I2S3_SDI/SPI3_MISO/I2S2_WS/SPI2_NSS/SPI6_MISO/UART7_TX/DCMI_D7/PSSI_D7	
	QSPI_BK1_NCS	PB6	92	PB5/TIM17_BKIN/TIM3_CH2/OCTOSPI1_NCLK/I2C1_SMB_A/I2S1_SDO/SPI1_MOSI/I2C4_SMB_A/I2S3_SDO/SPI3_MOSI/SPI6_MISO/FDCAN2_RX/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10/PSSI_D10/UART5_RX	
DCMI_VSYNC		PB7	93	PB6/TIM16_CH1N/TIM4_CH1/I3C1_SCL/I2C1_SCL/CEC/I2C4_SCL/USART1_TX/LPUART1_TX/FDCAN2_TX/OCTOSPI1_NCS/FMC_SDNE1/DCMI_D5/PSSI_D5/UART5_TX	
DCMI_D6		PB8	95	PB7/TIM17_CH1N/TIM4_CH2/I3C1_SDA/I2C1_SDA/I2C4_SDA/USART1_RX/LPUART1_RX/FDCAN1_TX/FMC_NL/DCMI_VSYNC/PSSI_RDY/WKUP5	
DCMI_D7		PB9	96	PB8/TIM16_CH1/TIM4_CH3/I3C1_SCL/I2C1_SCL/SPI4_RDY/I2C4_SCL/SDMMC1_CKIN/UART4_RX/FDCAN1_RX/ETH_TXD3/SDMMC1_D4/DCMI_D6/PSSI_D6	
	IIC_SCL	PB10	47	PB9/TIM17_CH1/TIM4_CH4/I3C1_SDA/I2C1_SDA/I2S2_WS/SPI2_NSS/I2C4_SDA/SDMMC1_CD1R/UART4_TX/FDCAN1_TX/SDMMC1_D5/DCMI_D7/PSSI_D7	
				PB10/TIM2_CH3/LPTIM3_CH1/LPTIM2_IN1/I2C2_SCL/I2S2_CK/SPI2_SCK/USART3_TX/OCTOSPI1_NCS/ETH_RX_ER	
	SPI2_CS	PB12	51		
	SPI2_SCK	PB13	52	PB12/TIM1_BKIN/OCTOSPI1_NCLK/I2C2_SDA/I2S2_WS/SPI2_NSS/UCPD1_FRSTX/USART3_CK/FDCAN2_RX/ETH_TXD0/UART5_RX	
	SPI2_MISO	PB14	53	PB13/TIM1_CH1N/LPTIM3_IN1/LPTIM2_CH1/I2C2_SMB_A/I2S2_CK/SPI2_SCK/USART3_CTS/USART3_NSS/FDCAN2_TX/ETH_TXD1/SDMMC1_D0/UART5_TX/UCPD1_CC1	
	SPI2_MOSI	PB15	54	PB14/TIM1_CH2N/TIM12_CH1/TIM8_CH2N/USART1_TX/I2S2_SDI/SPI2_MISO/USART3_RTS/UART4_DE/UART4_RTS/ETH_TX_EN/LPTIM3_ETR/UCPD1_CC2	
				PB15/RTC_REFIN/TIM1_CH3N/TIM12_CH2/TIM8_CH3N/USART1_RX/I2S2_SDO/SPI2_MOSI/USART11_CTS/USART11_NSS/UART4_CTS/OCTOSPI1_CLK/ETH_TXD1/DCMI_D2/PSSI_D2/UART5_RX/PVD_IN	
DCMI_PWDN	SAI1_MCLK_A	PC0	15	PC0/TIM16_BKIN/SAI1_MCLK_A/SPI2_RDY/SAI2_FS_B/FMC_A25/OCTOSPI1_IO7/FMC_SDNWE/ADC12_INP10	
DCMI_RESET	SAI1_SDA	PC1	16	PC1/SAI1_D1/I2S2_SDO/SPI2_MOSI/SAI1_SD_A/USART11_RTS/SAI2_SD_A/OCTOSPI1_IO4/ETH_MDC/ADC12_INP11/ADC12_INN10/TAMP_IN3/TAMP_OUT5/WKUP6	
	GBC_KEY	PC2	17	PC2/PWR_CSLEEP/TIM17_CH1/TIM4_CH4/I2S2_SDI/SPI2_MISO/OCTOSPI1_IO5/OCTOSPI1_IO2/ETH_TXD2/FMC_SDNE0/ADC12_INP12/ADC12_INN11	
	GBC_LED	PC3	18	PC3/PWR_CSTOP/SAI1_D3/LPTIM3_CH1/I2S2_SDO/SPI2_MOSI/OCTOSPI1_IO6/OCTOSPI1_IO0/ETH_TX_CLK/FMC_SDCKE0/ADC12_INP13/ADC12_INN12	
NRF_CE		PC4	33	PC4/TIM2_CH4/SAI1_CK1/LPTIM2_ETR/I2S1_MCK/USART3_RX/ETH_RXD0/FMC_SDNE0/ADC12_INP4	
	T_CS	PC5	34	PC5/TIM1_CH4N/SAI1_D3/PSSI_D15/SAI1_FS_A/UART12_RTS/OCTOSPI1_DQS/ETH_RXD1/FMC_SDCKE0/ADC12_INP8/ADC12_INN4	
DCMI_D0	SAI1_SCK_A	PC6	63	PC6/TIM3_CH1/TIM8_CH1/I2S2_MCK/SAI1_SCK_A/USART6_TX/SDMMC1_D0DIR/FMC_NWAIT/OCTOSPI1_IO5/SDMMC1_D6/DCMI_D0/PSSI_D0	
DCMI_D1		PC7	64	PC7/TIM3_CH2/TIM8_CH2/I2S3_MCK/USART6_RX/SDMMC1_D123DIR/FMC_NE1/OCTOSPI1_IO6/SDMMC1_D7/DCMI_D1/PSSI_D1	
DCMI_D2	SD1_D0	PC8	65	PC8/TIM3_CH3/TIM8_CH3/USART6_CK/UART5_DE/UART5_RTS/FMC_NCE/FMC_NE2/FMC_INT/FMC_ALE/SDMMC1_D0/DCMI_D2/PSSI_D2	
DCMI_D3	SD1_D1	PC9	66	PC9/MCO_2/TIM3_CH4/TIM8_CH4/I2C3_SDA/ADIOTCLK/UART5_CTS/OCTOSPI1_IO0/FMC_CLE/SDMMC1_D1/DCMI_D3/PSSI_D3/UCPD1_DB2	
	SD1_D2	PC10	78	PC10/LPTIM3_ETR/I2S3_CK/SPI3_SCK/USART3_TX/UART4_TX/OCTOSPI1_IO1/ETH_TXD0/SDMMC1_D2/DCMI_D8/PSSI_D8	
DCMI_D4	SD1_D3	PC11	79	PC11/LPTIM3_IN1/I2S3_SDI/SPI3_MISO/USART3_RX/UART4_RX/OCTOSPI1_NCS/SDMMC1_D3/DCMI_D4/PSSI_D4	
	SD1_CLK	PC12	80	PC12/TIM15_CH1/SPI6_SCK/I2S3_SDO/SPI3_MOSI/USART3_CK/UART5_TX/SDMMC1_CK/DCMI_D9/PSSI_D9	
NRF_IRQ	1WIRE_DQ	PC13	7	PC13/TAMP_IN1/TAMP_OUT2/TAMP_OUT3/RTC_OUT1/RTC_TS/WKUP4	
				STM32H562VIT6	

Title: DNH562M.PrjPCB		
Author: ALIENTEK	Size: A4	
Date: 2023/10/31	File: CORE_PIN_ABC.SchDoc	
Revision:	Version: V1.0	

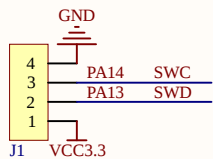
MCU_B

FMC_D2	PD0	81	U1B
FMC_D3	PD1	82	PD0/TIM8_CH4N/UART4_RX/FDCAN1_RX/UART9_CTS/FMC_D2/FMC_DA2
SD1_CMD	PD2	83	PD1/UART4_TX/FDCAN1_TX/FMC_D3/FMC_DA3
DCMI_D5	PD3	84	PD2/TIM3_ETR/TIM15_BKIN/UART5_RX/SDMMC1_CMD/DCMI_D11/PSSI_D11/LPTIM4_ETR/WKUP7
FMC_NOE	PD4	85	PD3/I2S2_CK/SPI2_SCK/USART2_CTS/USART2_NSS/FMC_CLK/DCMI_D5/PSSI_D5/WKUP8
FMC_NWE	PD5	86	PD4/USART2_RTS/OCTOSPI1_IO4/FMC_NOE
T_MISO	PD6	87	PD5/TIM1_CH4N/SPI2_RDY/USART2_TX/FDCAN1_TX/OCTOSPI1_IO5/FMC_NWE
FMC_NE1	PD7	88	PD6/SAI1_D1/I2S3_SDO/SPI3_MOSI/SAI1_SD_A/USART2_RX/OCTOSPI1_IO6/FMC_NWAIT/DCMI_D10/PSSI_D10
FMC_D13	PD8	55	PD7/I2S1_SDO/SPI1_MOSI/USART2_CK/OCTOSPI1_IO7/FMC_NCE/FMC_NE1/LPTIM4_OUT
FMC_D14	PD9	56	PD8/USART3_TX/FMC_D13/FMC_DA13
FMC_D15	PD10	57	PD9/USART3_RX/FDCAN2_RX/FMC_D14/FMC_DA14
QSPI_BK1_IO0	PD11	58	PD10/LPTIM2_CH2/USART3_CK/FMC_D15/FMC_DA15
QSPI_BK1_IO1	PD12	59	PD11/SAI1_CK1/LPTIM2_IN2/I2C4_SMB/USART3_CTS/USART3_NSS/UART4_RX/OCTOSPI1_IO0/SAI2_SD_A/FMC_A16/FMC_CLE
QSPI_BK1_IO3	PD13	60	PD12/LPTIM1_IN1/TIM4_CH1/LPTIM2_IN1/I2C4_SCL/I3C1_SCL/SAI1_D1/USART3_RTS/UART4_TX/OCTOSPI1_IO1/SAI2_FS_A/FMC_A17/FMC_ALE/DCMI_D12/PSSI_D12
FMC_D0	PD14	61	PD13/LPTIM1_CH1/TIM4_CH2/LPTIM2_CH1/I2C4_SDA/I3C1_SDA/OCTOSPI1_IO3/SAI2_SCK_A/UART9_RTS/FMC_A18/DCMI_D13/PSSI_D13/LPTIM4_IN1
FMC_D1	PD15	62	PD14/TIM4_CH3/UART8_CTS/UART9_RX/FMC_D0/FMC_DA0
			PD15/TIM4_CH4/UART8_RTS/UART9_TX/FMC_D1/FMC_DA1
BEEP	PE0	97	PE0/LPTIM1_ETR/TIM4_ETR/LPTIM2_CH2/LPTIM2_ETR/SPI3_RDY/UART8_RX/FDCAN1_RX/SAI2_MCLK_A/FMC_NBL0/DCMI_D2/PSSI_D2
QSPI_BK1_IO2	PE2	1	PE2/LPTIM1_IN2/SAI1_CK1/SPI4_SCK/SAI1_MCLK_A/USART10_RX/UART8_TX/OCTOSPI1_IO2/ETH_TXD3/FMC_A23/DCMI_D3/PSSI_D3
SAI1_SDB	PE3	2	PE3/TIM15_BKIN/SAI1_SD_B/USART10_TX/FMC_A19/TAMP_IN6/TAMP_OUT3
SAI1_FSA	PE4	3	PE4/SAI1_D2/TIM15_CH1N/SPI4_NSS/SAI1_FS_A/FMC_A20/DCMI_D4/PSSI_D4/TAMP_IN7/TAMP_OUT8
LCD_BL	PE5	4	PE5/SAI1_CK2/TIM15_CH1/SPI4_MISO/SAI1_SCK_A/FMC_A21/DCMI_D6/PSSI_D6/TAMP_IN8/TAMP_OUT7
FMC_A22	PE6	5	PE6/TIM1_BKIN2/SAI1_D1/TIM15_CH2/SPI4_MOSI/SAI1_SD_A/SAI2_MCLK_B/FMC_A22/DCMI_D7/PSSI_D7/TAMP_IN3/TAMP_OUT6
FMC_D4	PE7	38	PE7/TIM1_ETR/UART12_RTS/UART7_RX/OCTOSPI1_IO4/FMC_D4/FMC_DA4
FMC_D5	PE8	39	PE8/TIM1_CH1N/UART12_CTS/UART7_TX/OCTOSPI1_IO5/FMC_D5/FMC_DA5
FMC_D6	PE9	40	PE9/TIM1_CH1/UART12_RX/UART7_RTS/OCTOSPI1_IO6/FMC_D6/FMC_DA6
FMC_D7	PE10	41	PE10/TIM1_CH2N/UART12_TX/UART7_CTS/OCTOSPI1_IO7/FMC_D7/FMC_DA7
FMC_D8	PE11	42	PE11/TIM1_CH2/SPI1_RDY/SPI4_NSS/OCTOSPI1_NCS/SAI2_SD_B/FMC_D8/FMC_DA8
FMC_D9	PE12	43	PE12/TIM1_CH3N/SPI4_SCK/SAI2_SCK_B/FMC_D9/FMC_DA9
FMC_D10	PE13	44	PE13/TIM1_CH3/SPI4_MISO/SAI2_FS_B/FMC_D10/FMC_DA10
FMC_D11	PE14	45	PE14/TIM1_CH4/SPI4_MOSI/SAI2_MCLK_B/FMC_D11/FMC_DA11
FMC_D12	PE15	46	PE15/TIM1_BKIN/TIM1_CH4N/USART10_CK/FMC_D12/FMC_DA12

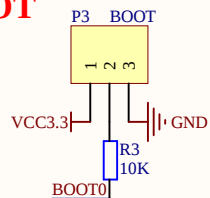
BOOT0 94
RESET 14



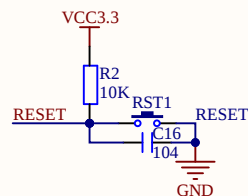
SWD



BOOT



RESET



IO

PD7	P1	1
PD5		2
PD4		3
PD14		4
PD15		5
PD0		6
PD1		7
PE7		8
PE8		9
PE9		10
PE10		11
PE11		12
PE12		13
PE13		14
PE14		15
PE15		16
PD8		17
PD9		18
PD10		19

Header 19

GND	P2	1	2	PB12
PB14		3	4	PB13
PB15		5	6	PE0
PA3		7	8	PA2
PC3		9	10	PC2
PC4		11	12	PA8
PA12		13	14	PA11
PA15		15	16	PC13
PB5		17	18	PB4
PA1		19	20	PA0
USART1_TX		21	22	USART1_RX
RXD		23	24	TXD

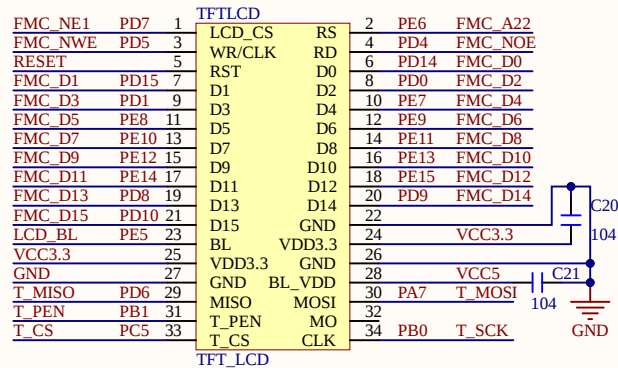
Header 12X2

Title:
DNH562M.PrjPCB
Author:
ALIENTEK
Date:
2023/10/31
Revision:

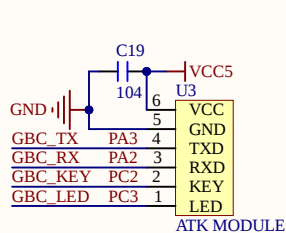
Size:
A4
File:
CORE_PIN_DEF.SchDoc
Version:
V1.0



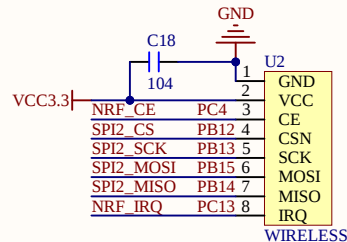
LCD



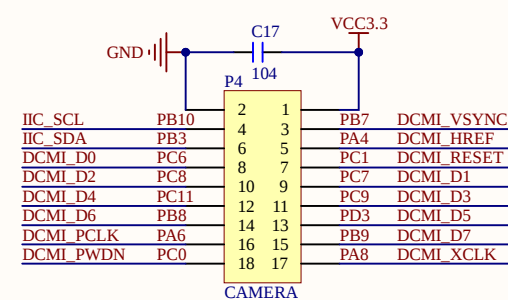
ATK MODULE



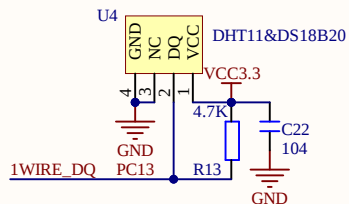
WIRELESS



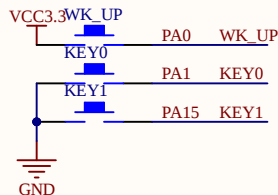
OLED&CAMERA



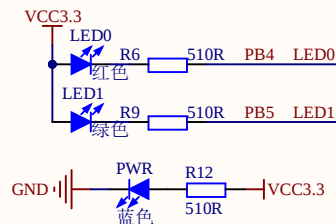
TEMP&HUMI SENSOR



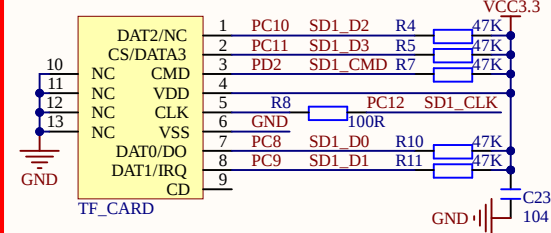
KEY



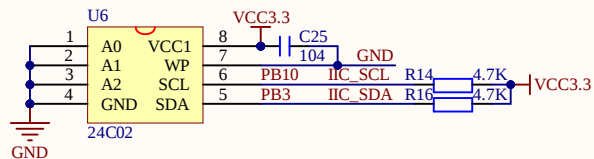
LED



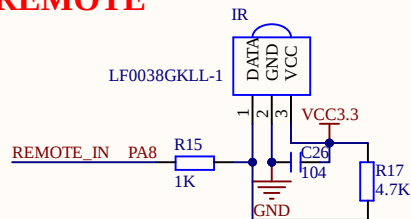
SD CARD



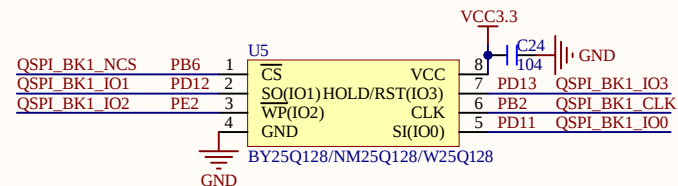
EEPROM



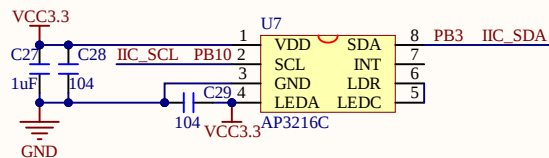
REMOTE



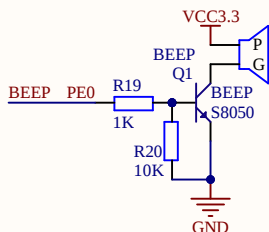
QSPI FLASH



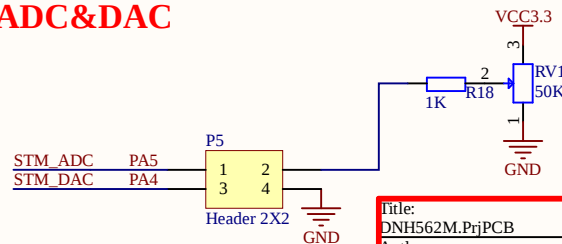
ALS&PS SENSOR



BEEP

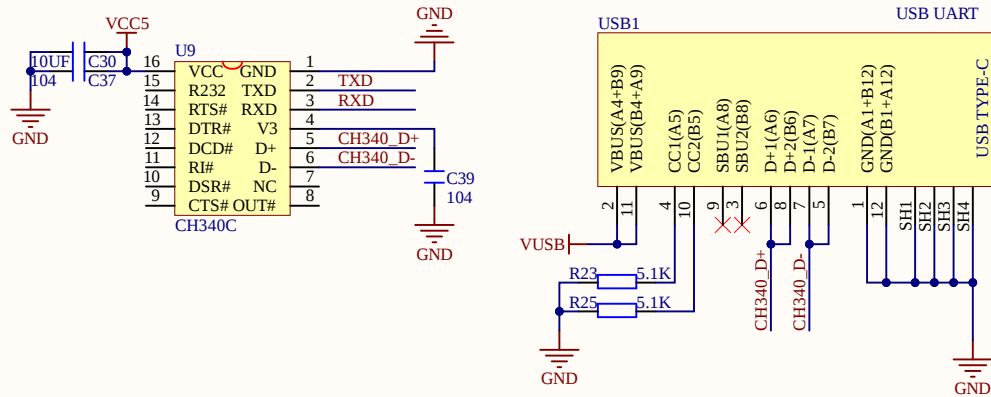


ADC&DAC

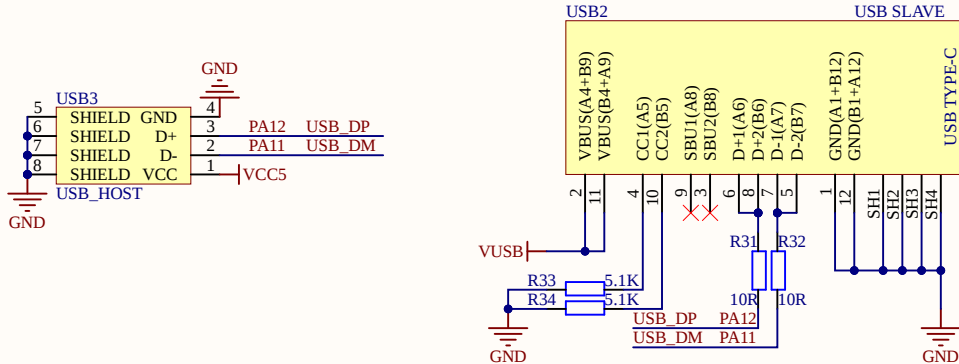


Title: DNH562M.PrjPCB		
Author: ALIENTEK	Size: A4	
Date: 2023/10/31	File: DEVICE.SchDoc	
Revision:	Version: V1.0	

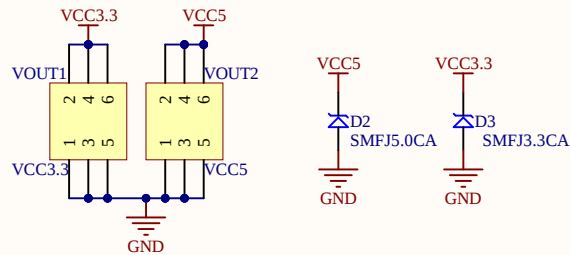
USB UART



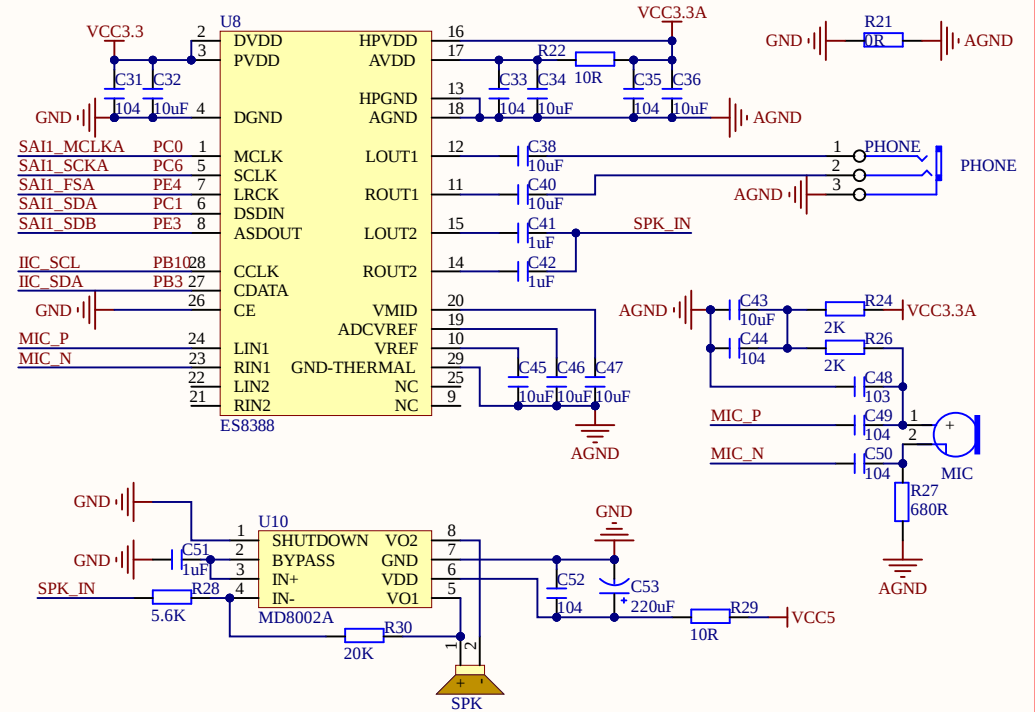
USB SLAVE & HOST



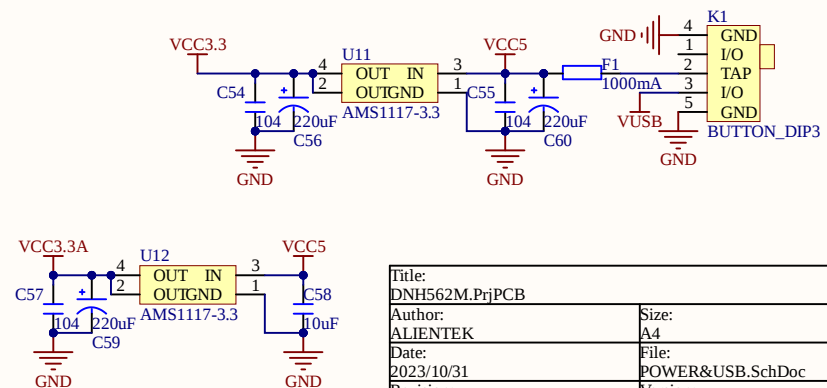
ON BOARD POWER SOURCE



AUDIO



LDO



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