

MCU_ABC

WKUP	PA0	22
	PA1	23
GBC_RX	PA2	24
GBC_TX	PA3	25
DCMI_HREF	PA4	28
	PA5	29
DCMI_PCLK	PA6	30
DCMI_RESET	PA7	31
DCMI_XCLK	PA8	67
USART1_TX	PA9	68
USART1_RX	PA10	69
USB_D-	PA11	70
USB_D+	PA12	71
SWDIO	PA13	72
SWDCLK	PA14	76
KEY0	PA15	77

	T_SCK	PB0	34
	T_PEN	PB1	35
	QSPI_BK1_CLK	PB2	36
	T_MOSI	PB3	89
		PB4	90
	LCD_BL	PB5	91
	QSPI_BK1_NCS	PB6	92
DCMI_VSYNC		PB7	93
DCMI_D6		PB8	95
DCMI_D7		PB9	96
DCMI_SCL	IIC_SCL	PB10	46
DCMI_SDA	IIC_SDA	PB11	47
	SPI2_CS	PB12	51
	SPI2_SCK	PB13	52
	SPI2_MISO	PB14	53
	SPI2_MOSI	PB15	54

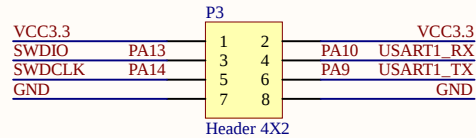
		PC0	15
		PC1	16
	GBC_KEY	PC2	17
	GBC_LED	PC3	18
DCMI_PWDN		PC4	32
	T_CS	PC5	33
DCMI_D0		PC6	63
DCMI_D1		PC7	64
DCMI_D2	SDIO_D0	PC8	65
DCMI_D3	SDIO_D1	PC9	66
	SDIO_D2	PC10	78
DCMI_D4	SDIO_D3	PC11	79
	SDIO_CLK	PC12	80
		PC13	7

U1A

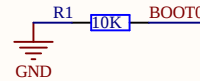
PA0/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/TIM15_BKIN/USART2_CTS/USART2_NSS/UART4_TX/SDMMC2_CMD/SAI2_SD_B/ETH_MII_CRS/ADC1_INP16/WKUP0
PA1/TIM2_CH2/TIM5_CH2/LPTIM3_OUT/TIM15_CH1N/USART2_RTS/USART2_DE/UART4_RX/QUADSPI_BK1_IO3/SAI2_MCLK_B/ETH_RMII_REF_CLK/LCD_R2/ADC1_INN16/ADC1_INP17
PA2/TIM2_CH3/TIM5_CH3/LPTIM4_OUT/TIM15_CH2/USART2_TX/SAI2_SCK_B/ETH_MDIO/MDIOS_MDIO/LCD_R1/ADC12_INP14/WKUP1
PA3/TIM2_CH4/TIM5_CH4/LPTIM5_OUT/TIM15_CH2/USART2_RX/LCD_B2/OTG_HS_ULPI_D0/ETH_MII_COL/LCD_B5/ADC12_INP15
PA4/TIM5_ETR/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/USART2_CK/SPI6_NSS/OTG_HS_SOF/DCMI_HSYNC/LCD_VSYNC/ADC12_INP18/DAC1_OUT1
PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SPI1_SCK/I2S1_CK/SPI6_SCK/OTG_HS_ULPI_CK/LCD_R4/ADC12_INN18/ADC12_INP19/DAC1_OUT2
PA6/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SPI1_SCK/I2S1_CK/SPI6_SCK/OTG_HS_ULPI_CK/LCD_R4/ADC12_INN18/ADC12_INP19/DAC1_OUT2
PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/SPI1_MOSI/I2S1_SDO/SPI6_MOSI/TIM14_CH1/ETH_MII_RX_DV/ETH_RMII_CRS_DV/FMC_SDNWE/ADC12_INN3/ADC12_INP7/OPAMP1_VINM
PA8/MCO1/TIM1_CH1/HRTIM_CHB2/TIM8_BKIN2/I2C3_SCL/USART1_CK/OTG_FS_SOF/UART7_RX/TIM8_BKIN2_COMP12/LCD_B3/LCD_R6
PA9/TIM1_CH2/HRTIM_CHC1/LPUART1_TX/I2C3_SMBA/SPI2_SCK/I2S2_CK/USART1_TX/FDCAN1_RXFD_MODE/DCMI_D0/LCD_R5/OTG_FS_VBUS
PA10/TIM1_CH3/HRTIM_CHC2/LPUART1_RX/USART1_RX/FDCAN1_TXFD_MODE/OTG_FS_ID/MDIOS_MDIO/LCD_B4/DCMI_D1/LCD_B1
PA11/TIM1_CH4/HRTIM_CHD1/LPUART1_CTS/SPI2_NSS/I2S2_WS/UART4_RX/USART1_CTS/USART1_NSS/FDCAN1_RX/OTG_FS_DM/LCD_R4
PA12/TIM1_ETR/HRTIM_CHD2/LPUART1_RTS/LPUART1_DE/SPI2_SCK/I2S2_CK/UART4_TX/USART1_RTS/USART1_DE/SAI2_FS_B/FDCAN1_TX/OTG_FS_DP/LCD_R5
PA13/JTMS-SWDIO
PA14/JTCK-SWCLK
PA15/JTDI/TIM2_CH1/TIM2_ETR/HRTIM_FLT1/CEC/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/SPI6_NSS/UART4_RTS/UART4_DE/UART7_TX
PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/DFSDM1_CKOUT/UART4_CTS/LCD_R3/OTG_HS_ULPI_D1/ETH_MII_RXD2/LCD_G1/ADC12_INN5/ADC12_INP9/OPAMP1_VINP/COMP1_INP
PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/DFSDM1_DATIN1/LCD_R6/OTG_HS_ULPI_D2/ETH_MII_RXD3/LCD_G0/ADC12_INP5/COMP1_INM
PB2/RTC_OUT/SAI1_D1/DFSDM1_CKIN1/SAI1_SD_A/SPI3_MOSI/I2S3_SDO/SAI4_SD_A/QUADSPI_CLK/SAI4_D1/COMP1_INP
PB3/JTDO/TRACESWO/TIM2_CH2/HRTIM_FLT4/SPI1_SCK/I2S1_CK/SPI3_SCK/I2S3_CK/SPI6_SCK/SDMMC2_D2/CRS_SYNC/UART7_RX
PB4/NJTRST/TIM16_BKIN/TIM3_CH1/HRTIM_EEV6/SPI1_MISO/I2S1_SDI/SPI3_MISO/I2S3_SDI/SPI2_NSS/I2S2_WS/SPI6_MISO/SDMMC2_D3/UART7_TX
PB5/TIM17_BKIN/TIM3_CH2/HRTIM_EEV7/I2C1_SMBA/SPI1_MOSI/I2S1_SDO/I2C4_SMBA/SPI3_MOSI/I2S3_SDO/SPI6_MOSI/FDCAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10/UART5_RX
PB6/TIM16_CH1N/TIM4_CH1/HRTIM_EEV8/I2C1_SCL/CEC/I2C4_SCL/USART1_TX/LPUART1_TX/FDCAN2_TX/QUADSPI_BK1_NCS/DFSDM1_DATIN5/FMC_SDNE1/DCMI_D5/UART5_TX
PB7/TIM17_CH1N/TIM4_CH2/HRTIM_EEV9/I2C1_SDA/I2C4_SDA/USART1_RX/LPUART1_RX/FDCAN2_TXFD_MODE/DFSDM1_CKIN5/FMC_NL/DCMI_VSYNC/PVD_IN
PB8/TIM16_CH1/TIM4_CH3/DFSDM1_CKIN7/I2C1_SCL/I2C4_SCL/SDMMC1_CKIN/UART4_RX/FDCAN1_RX/SDMMC2_D4/ETH_MII_TXD3/SDMMC1_D4/DCMI_D6/LCD_B6
PB9/TIM17_CH1/TIM4_CH4/DFSDM1_DATIN7/I2C1_SDA/SPI2_NSS/I2S2_WS/I2C4_SDA/SDMMC1_CDIR/UART4_TX/FDCAN1_TX/SDMMC2_D5/I2C4_SMBA/SDMMC1_D5/DCMI_D7/LCD_B7
PB10/TIM2_CH3/HRTIM_SCOUT/LPTIM2_IN1/I2C2_SCL/SPI2_SCK/I2S2_CK/DFSDM1_DATIN7/USART3_TX/QUADSPI_BK1_NCS/OTG_HS_ULPI_D3/ETH_MII_RX_ER/LCD_G4
PB11/TIM2_CH4/HRTIM_SCIN/LPTIM2_ETR/I2C2_SDA/DFSDM1_CKIN7/USART3_RX/OTG_HS_ULPI_D4/ETH_MII_TX_EN/ETH_RMII_TX_EN/LCD_G5
PB12/TIM1_BKIN/I2C2_SMBA/SPI2_NSS/I2S2_WS/DFSDM1_DATIN1/USART3_CK/FDCAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMII_TXD0/OTG_HS_ID/TIM1_BKIN_COMP12/UART5_RX
PB13/TIM1_CH1N/LPTIM2_OUT/SPI2_SCK/I2S2_CK/DFSDM1_CKIN1/USART3_CTS/USART3_NSS/FDCAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMII_TXD1/UART5_TX/OTG_HS_VBUS
PB14/TIM1_CH2N/TIM12_CH1/TIM8_CH2N/USART1_TX/SPI2_MISO/I2S2_SDI/DFSDM1_DATIN2/USART3_RTS/USART3_DE/UART4_RTS/UART4_DE/SDMMC2_D0/OTG_HS_DM
PB15/RTC_REFIN/TIM1_CH3N/TIM12_CH2/TIM8_CH3N/USART1_RX/SPI2_MOSI/I2S2_SDO/DFSDM1_CKIN2/UART4_CTS/SDMMC2_D1/OTG_HS_DP
PC0/DFSDM1_CKIN0/DFSDM1_DATIN4/SAI2_FS_B/OTG_HS_ULPI_STP/FMC_SDNWE/LCD_R5/ADC123_INP10
PC1/SAI1_D1/DFSDM1_DATIN0/DFSDM1_CKIN4/SPI2_MOSI/I2S2_SDO/SAI1_SD_A/SAI4_SD_A/SDMMC2_CK/SAI4_D1/ETH_MDC/MDIOS_MDC/ADC123_INN10/ADC123_INP11/RTC_TAMP3/WKUP5
PC2/DFSDM1_CKIN1/SPI2_MISO/I2S2_SDI/DFSDM1_CKOUT/OTG_HS_ULPI_DIR/ETH_MII_TXD2/FMC_SDNE0/ADC123_INN11/ADC123_INP12
PC3/DFSDM1_DATIN1/SPI2_MOSI/I2S2_SDO/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/FMC_SDCKE0/ADC12_INN12/ADC12_INP13
PC4/DFSDM1_CKIN2/I2S1_MCK/SPDIFRX1_IN3/ETH_MII_RXD0/ETH_RMII_RXD0/FMC_SDNE0/ADC12_INP4/OPAMP1_VOUT/COMP1_INM
PC5/SAI1_D3/DFSDM1_DATIN2/SPDIFRX1_IN4/SAI4_D3/ETH_MII_RXD1/ETH_RMII_RXD1/FMC_SDCKE0/COMP1_OUT/ADC12_INN4/ADC12_INP8/OPAMP1_VINM
PC6/HRTIM_CHA1/TIM3_CH1/TIM8_CH1/DFSDM1_CKIN3/I2S2_MCK/USART6_TX/SDMMC1_D0DIR/FMC_NWAIT/SDMMC2_D6/SDMMC1_D6/DCMI_D0/LCD_HSYNC/SWPMI_IO
PC7/TRGIO/HRTIM_CHA2/TIM3_CH2/TIM8_CH2/DFSDM1_DATIN3/I2S3_MCK/USART6_RX/SDMMC1_D123DIR/FMC_NE1/SDMMC2_D7/SWPMI_TX/SDMMC1_D7/DCMI_D1/LCD_G6
PC8/HRTIM_CHB1/TIM3_CH3/TIM8_CH3/USART6_CK/UART5_RTS/UART5_DE/FMC_NE2/FMC_NCE/SWPMI_RX/SDMMC1_D0/DCMI_D2
PC9/MCO2/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/UART5_CTS/QUADSPI_BK1_IO0/LCD_G3/SWPMI_SUSPEND/SDMMC1_D1/DCMI_D3/LCD_B2
PC10/HRTIM_EEV1/DFSDM1_CKIN5/SPI3_SCK/I2S3_CK/USART3_TX/UART4_TX/QUADSPI_BK1_IO1/SDMMC1_D2/DCMI_D8/LCD_R2
PC11/HRTIM_FLT2/DFSDM1_DATIN5/SPI3_MISO/I2S3_SDI/USART3_RX/UART4_RX/QUADSPI_BK2_NCS/SDMMC1_D3/DCMI_D4
PC12/HRTIM_EEV2/SPI3_MOSI/I2S3_SDO/USART3_CK/UART5_TX/SDMMC1_CK/DCMI_D9
PC13/RTC_TAMP1/RTC_TS/WKUP2

STM32H750VBT6

SWD & USART1



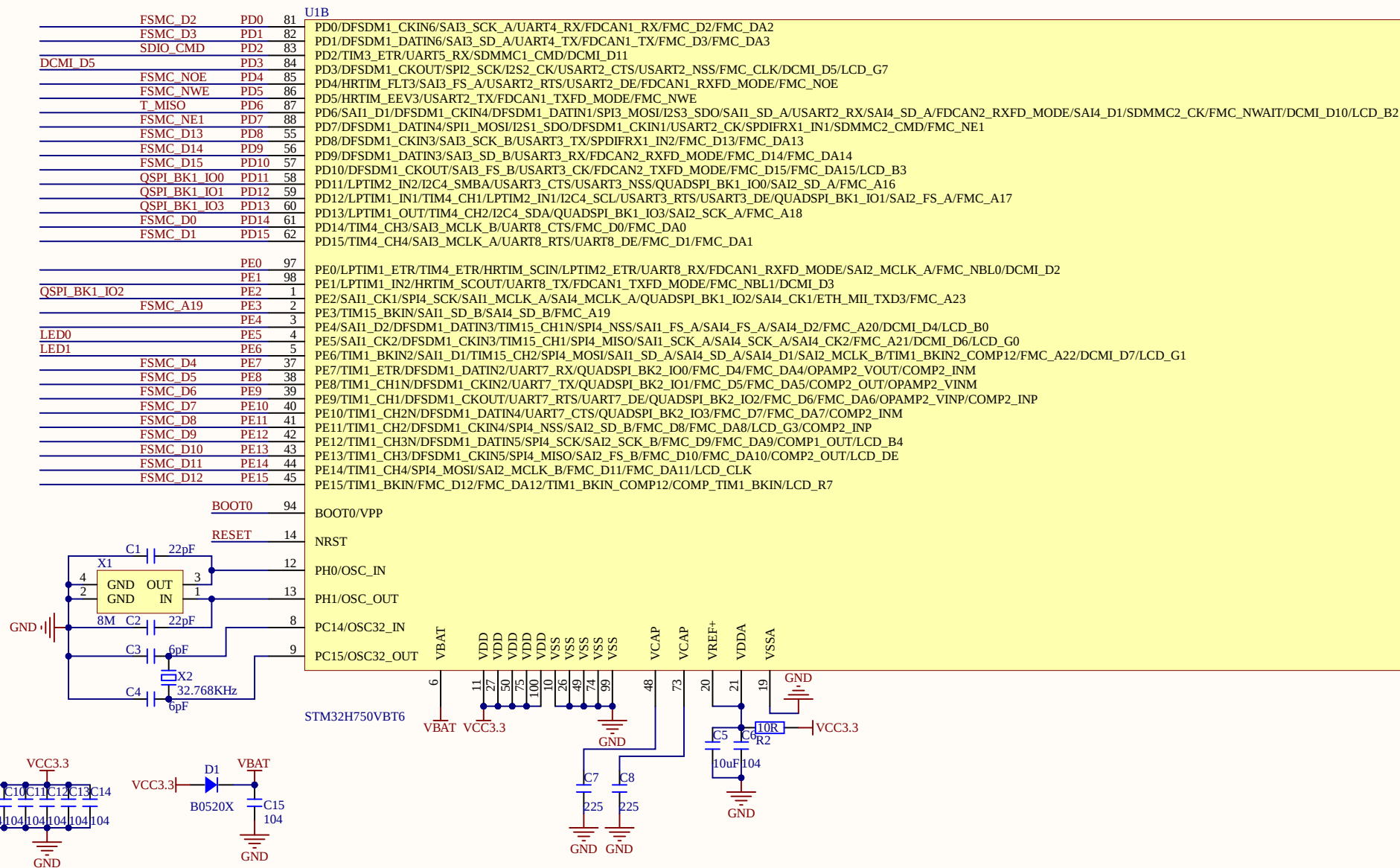
BOOT



Title: M100Z-M7.PrjPcb	
Author: ALIENTEK	Size: A4
Date: 2023/6/26	File: M100Z-M7_CPU_ABC.SchDoc
Revision: V1.1	Version:



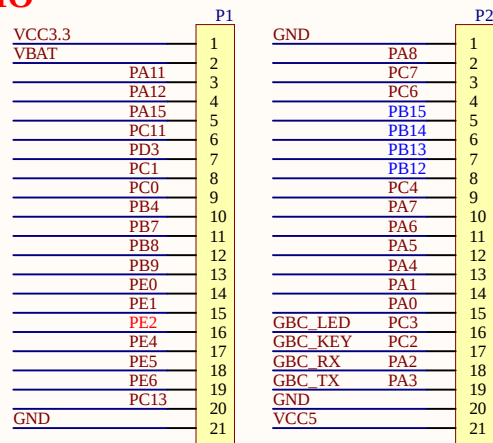
MCU DE



Title: M100Z-M7.PrjPcb	
Author: ALJENTEK	Size: A4
Date: 2023/6/26	File: M100Z-M7_CPU_DE.SchDoc
Revision: V1.1	Version:



IO



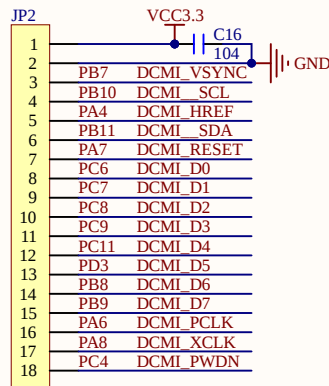
蓝色部分IO口
M7和M3/4存在差异！
无法做到完全兼容！

要兼容，请用其他的IO口

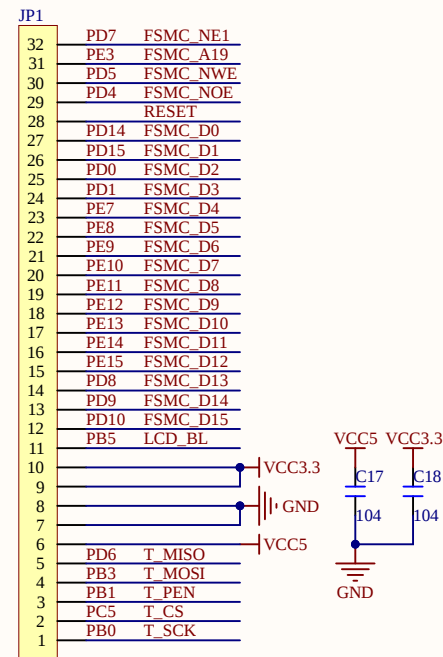
不考虑兼容，则所有IO都可以正常使用

PE2在M7上用作QSPI数据线，因此在M7上一般也不作IO使用。但是在M3/4上，则可以当普通IO用

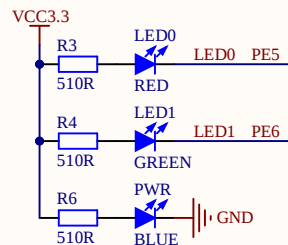
CAMERA



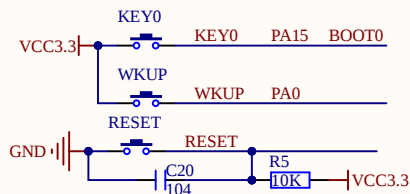
TFTLCD



LED

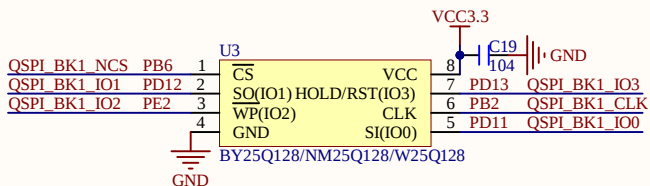


KEY

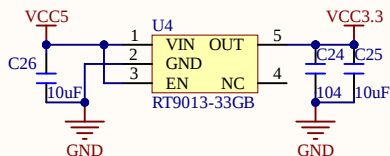
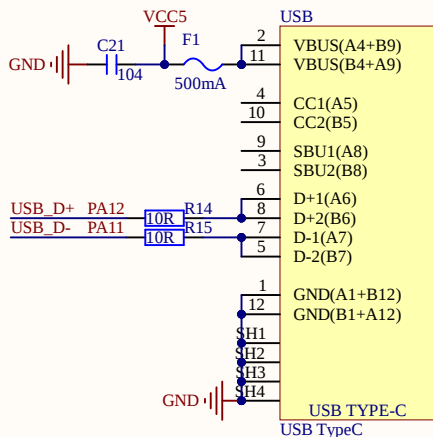


进入默认BootLoader模式方法：
先按住KEY0（BOOT0=1）并保持不放
然后再按板子的复位键，再松开复位键
此时，MCU将进入Bootloader模式

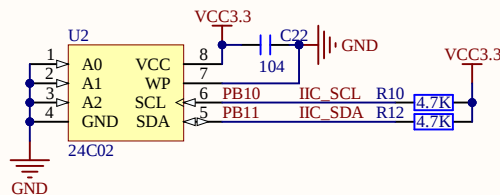
FLASH



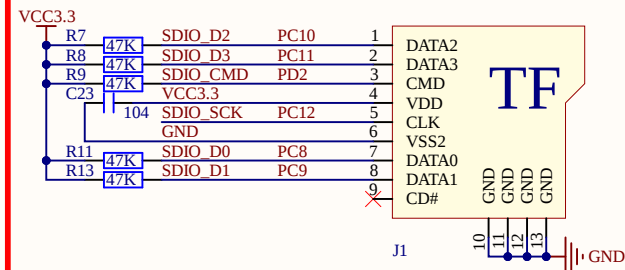
USB & POWER



EEPROM



TF CARD



Title: M100Z-M7.PrjPcb
Author: ALIENTEK
Date: 2023/6/26
Revision: V1.1
Size: A4
File: M100Z-M7_DEVICE.SchDoc
Version:



