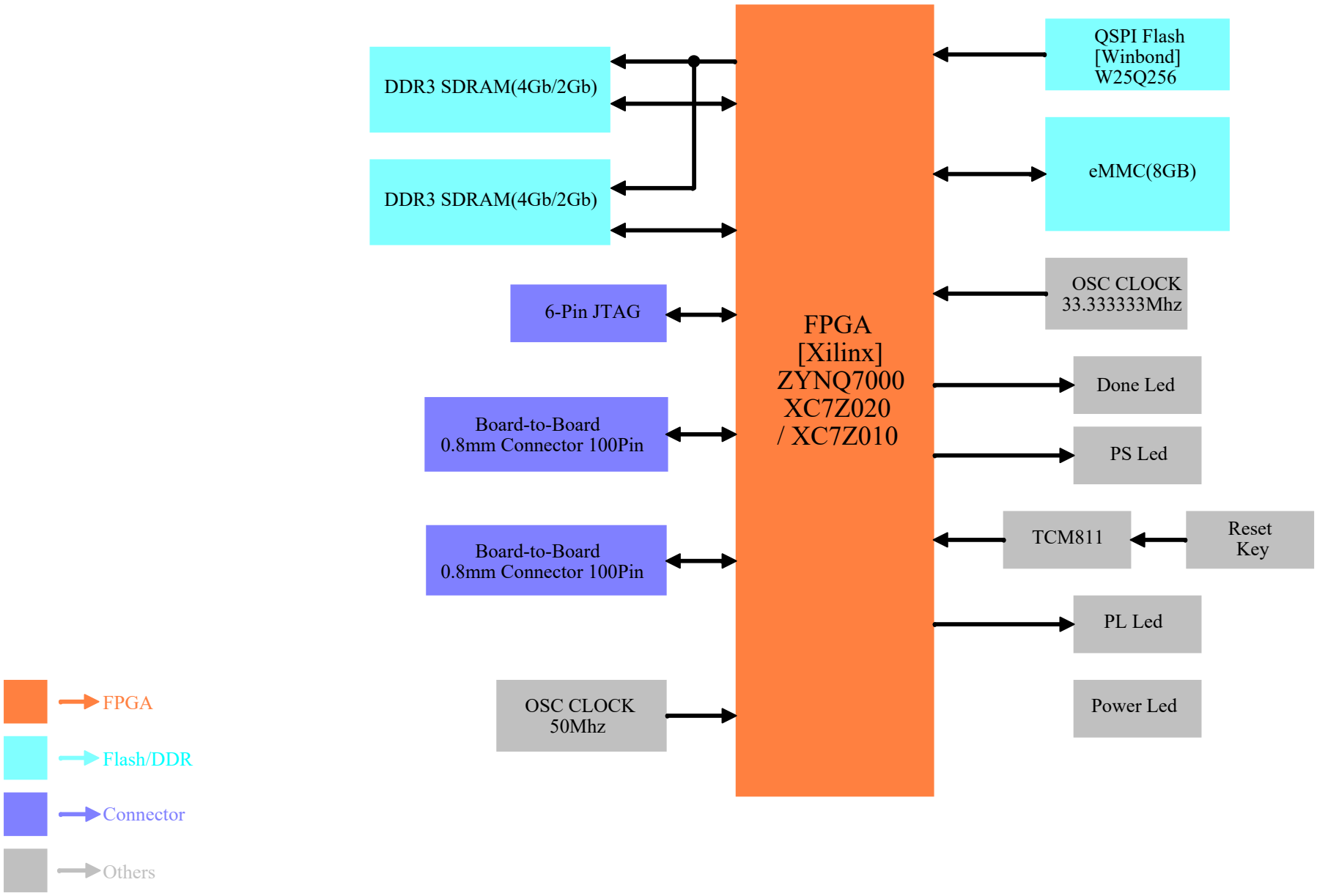
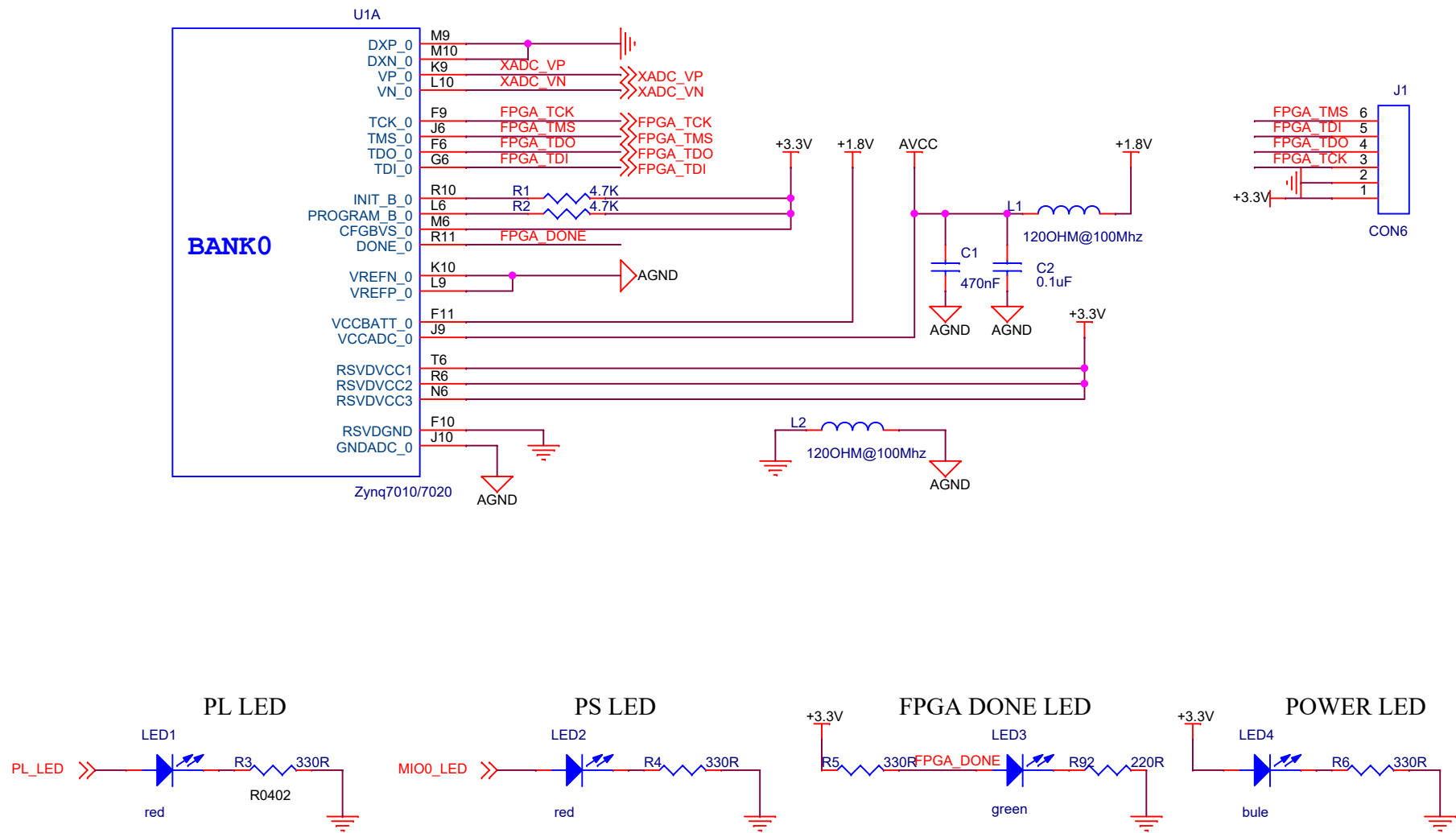
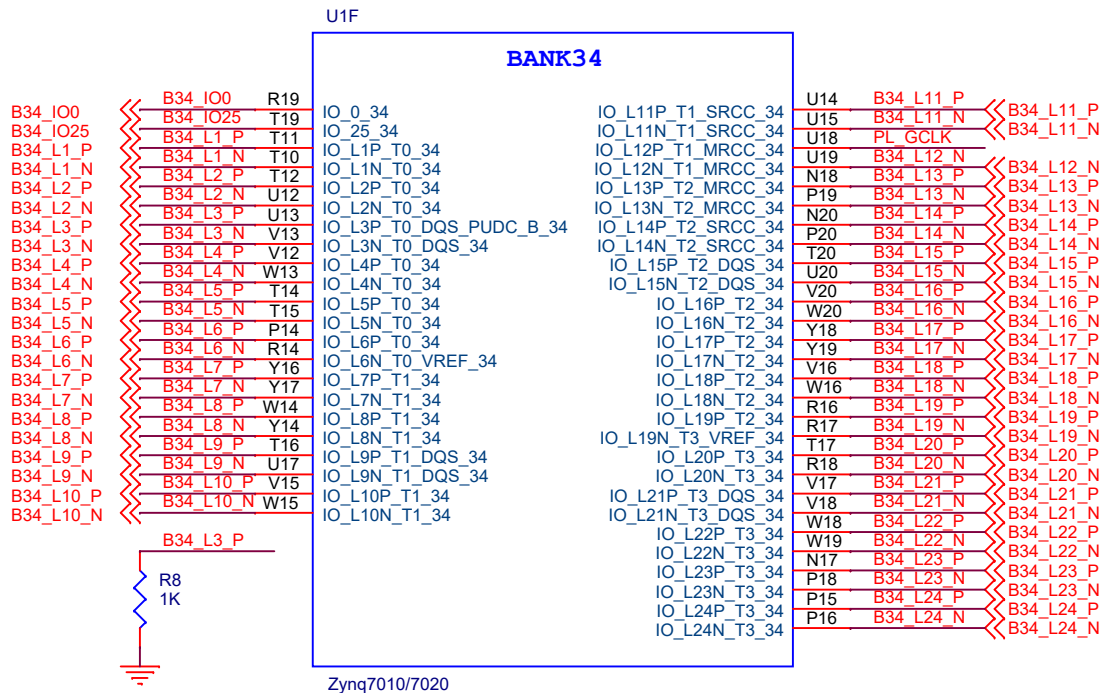


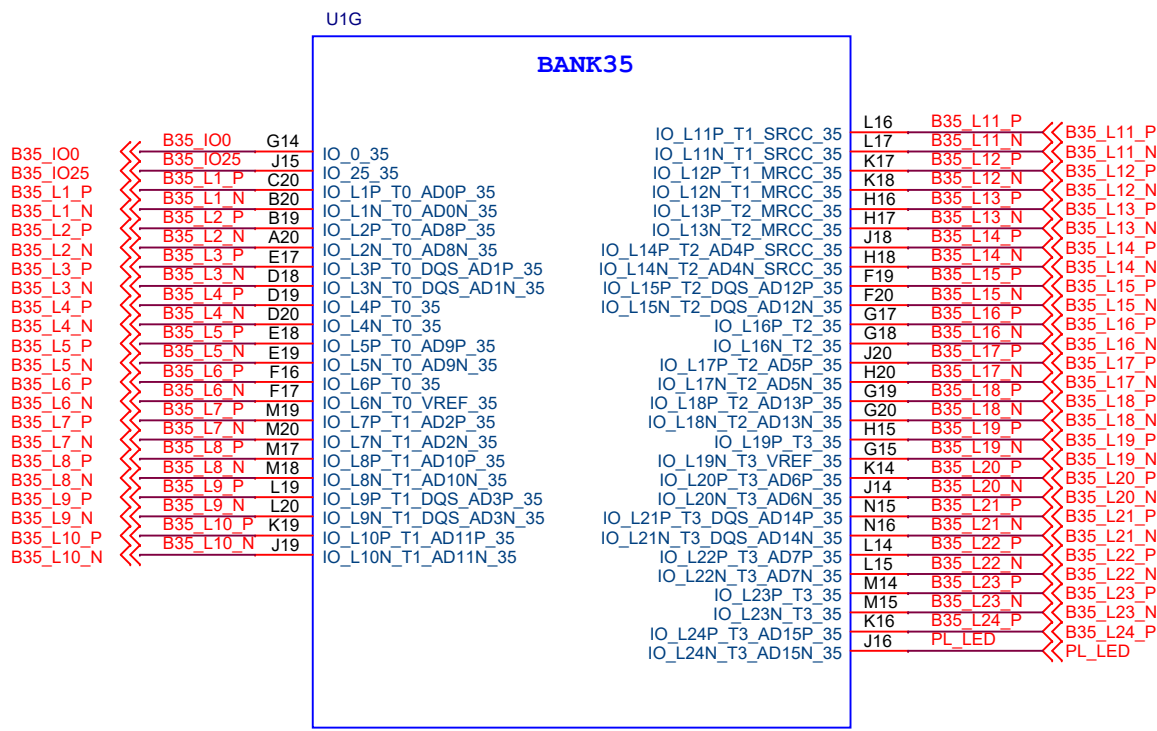




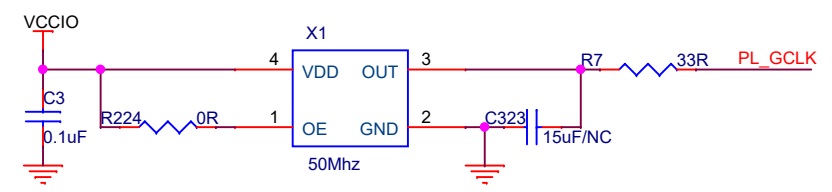
Design Name		
ZYNQ-CORE		
Size	Page Name	Rev
A4	02_FPGA Bank0	1.0
Date:	Tuesday, April 23, 2024	Sheet 2 of 12

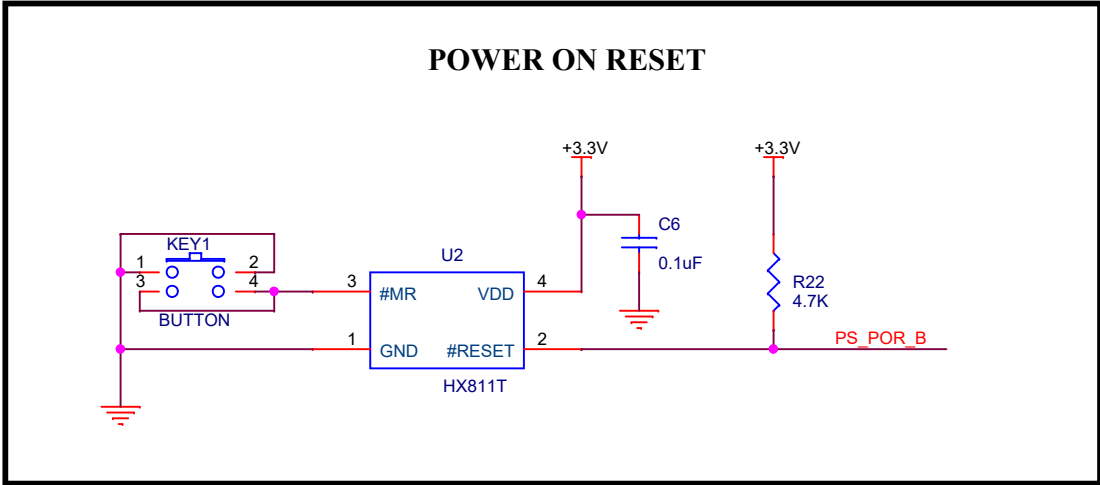
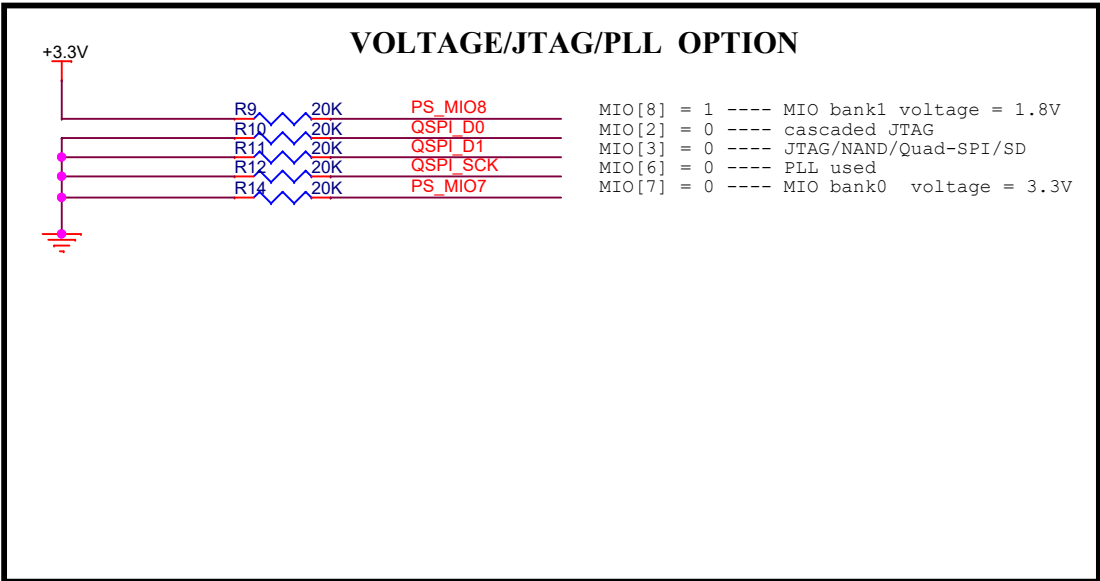
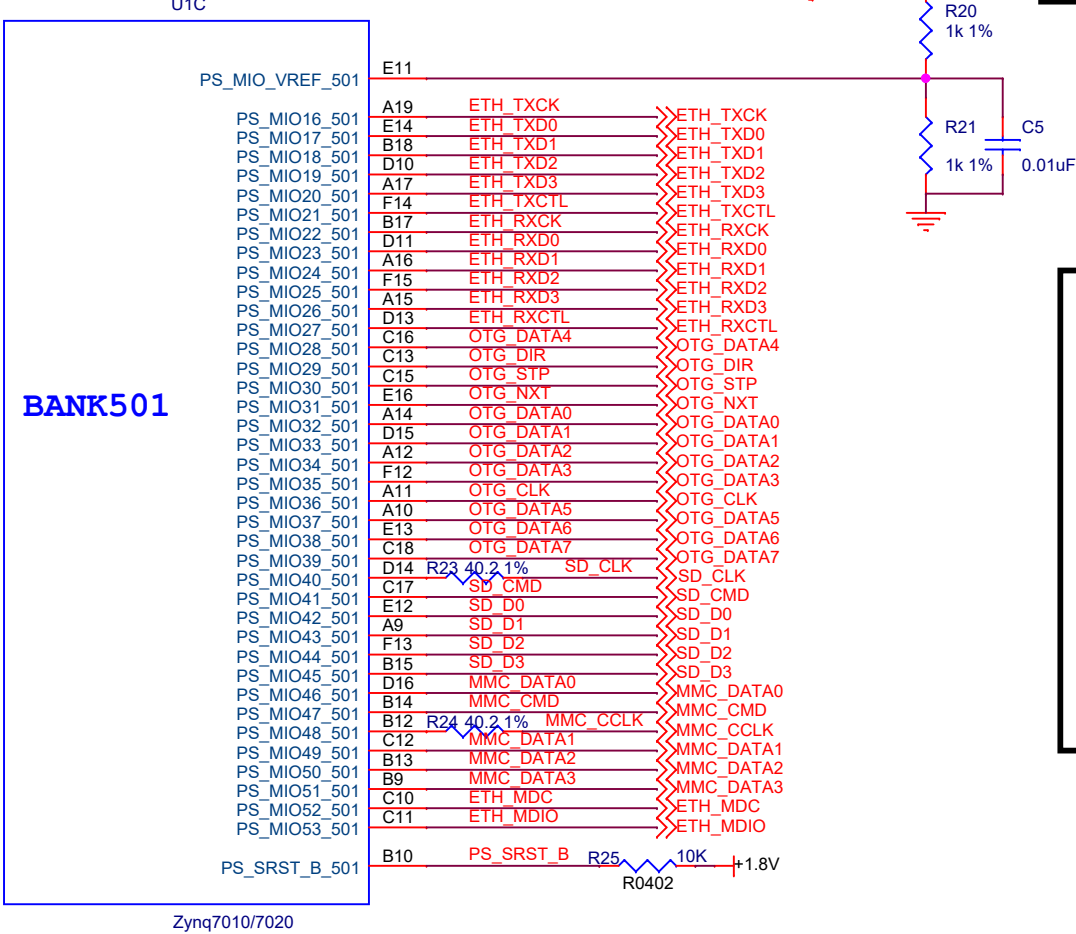
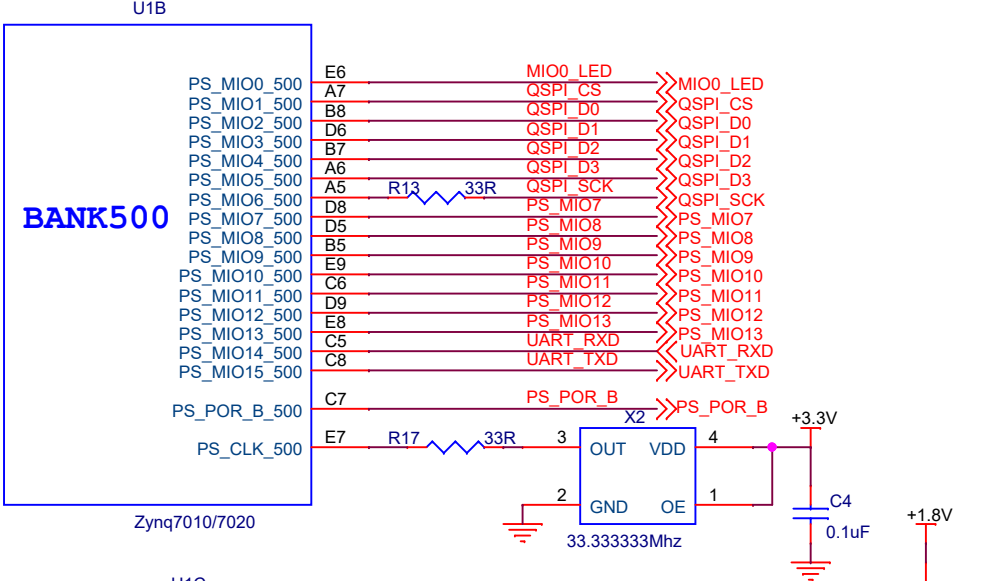


Zynq7010/7020

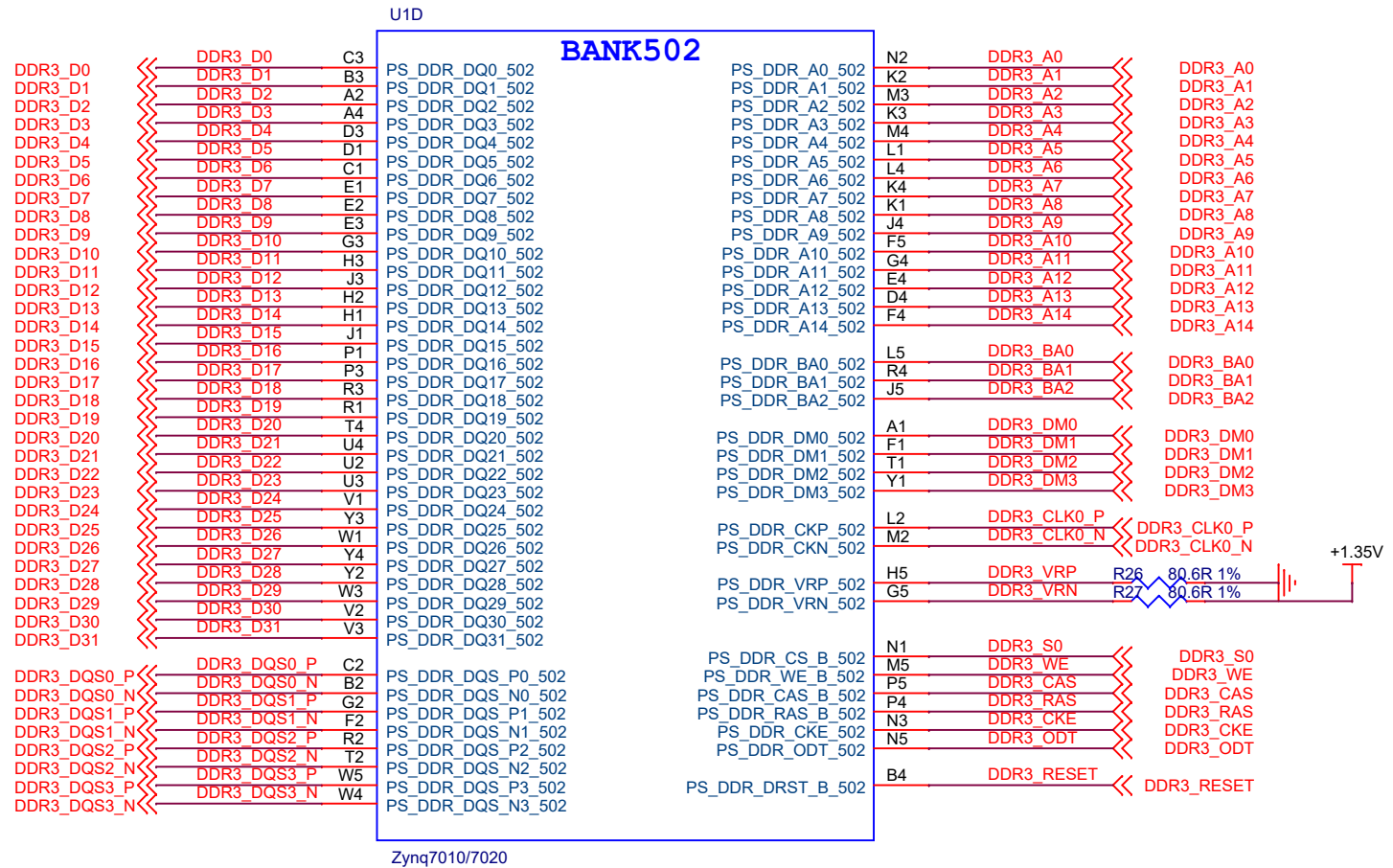


Zynq7010/7020





Design Name			ZYNQ-CORE	
Size	Page Name	Rev		
A4	04_FPGA Bank500,Bank501	1.0		
Date:	Tuesday, April 23, 2024	Sheet	4	of 12

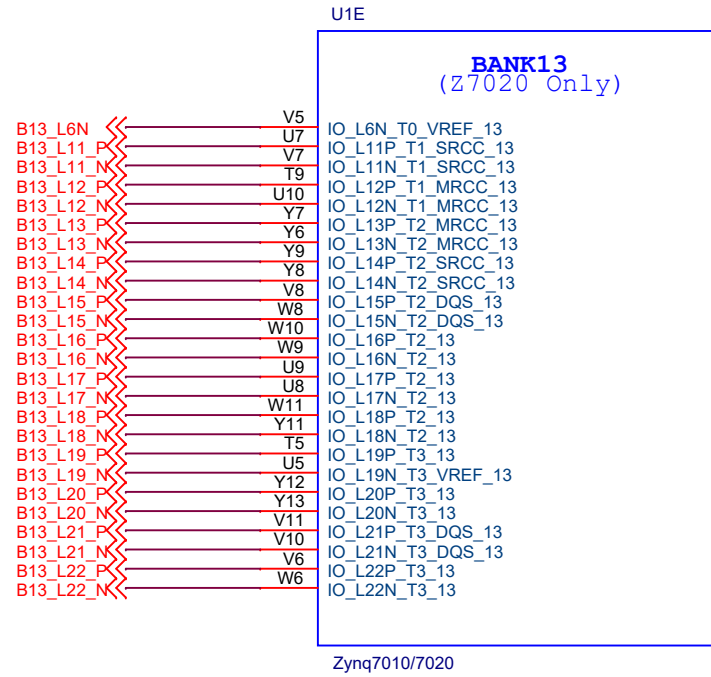


D

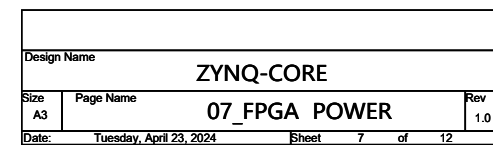
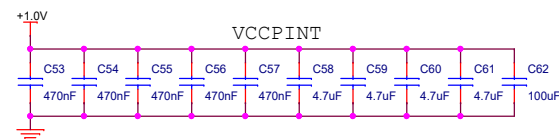
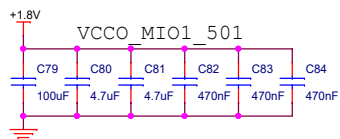
C

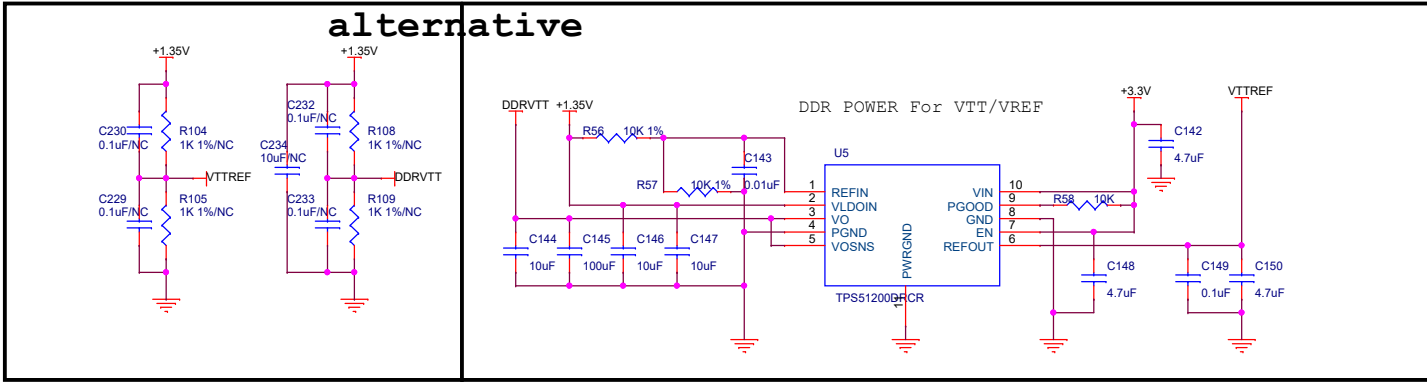
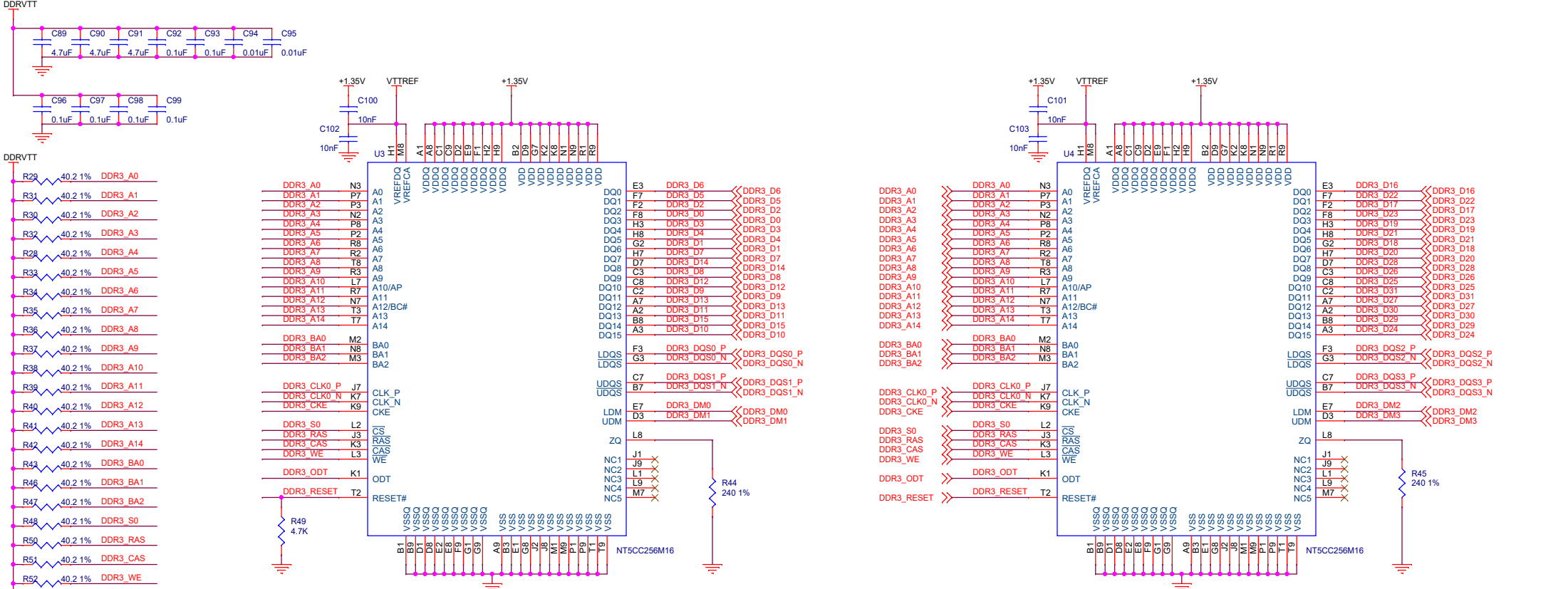
B

A

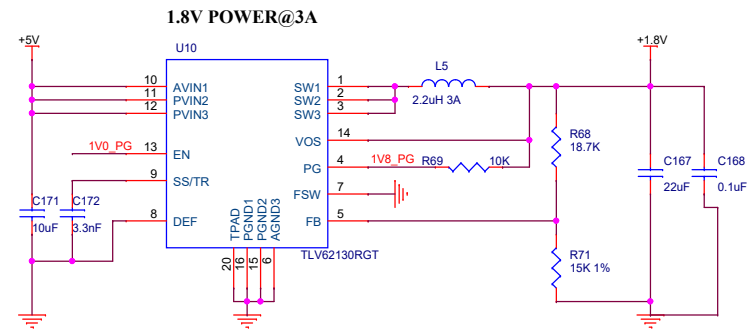
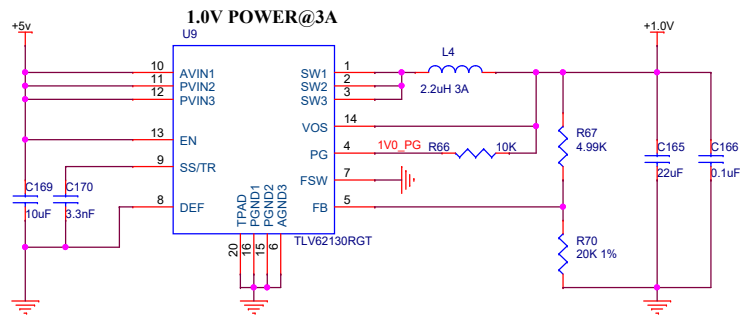
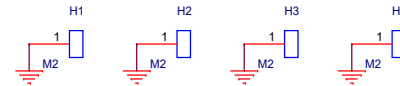
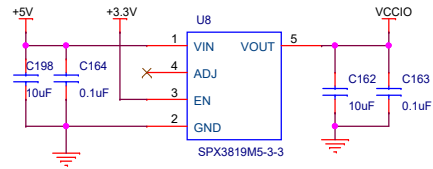


Design Name			
ZYNQ-CORE			
Size	Page Name		Rev
A4	06_Bank_13		1.0
Date:	Tuesday, April 23, 2024	Sheet	6 of 12

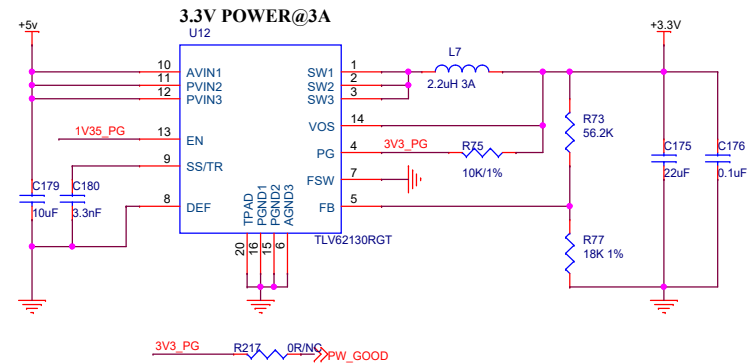
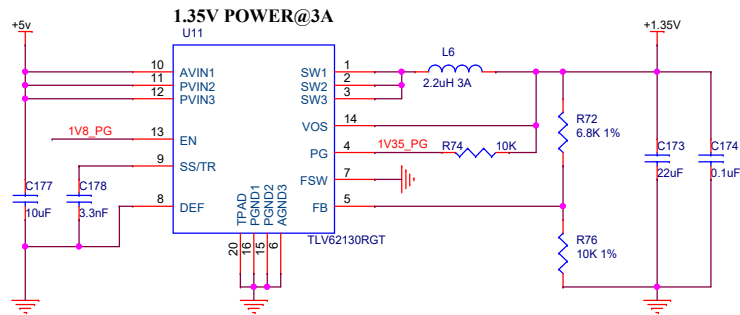




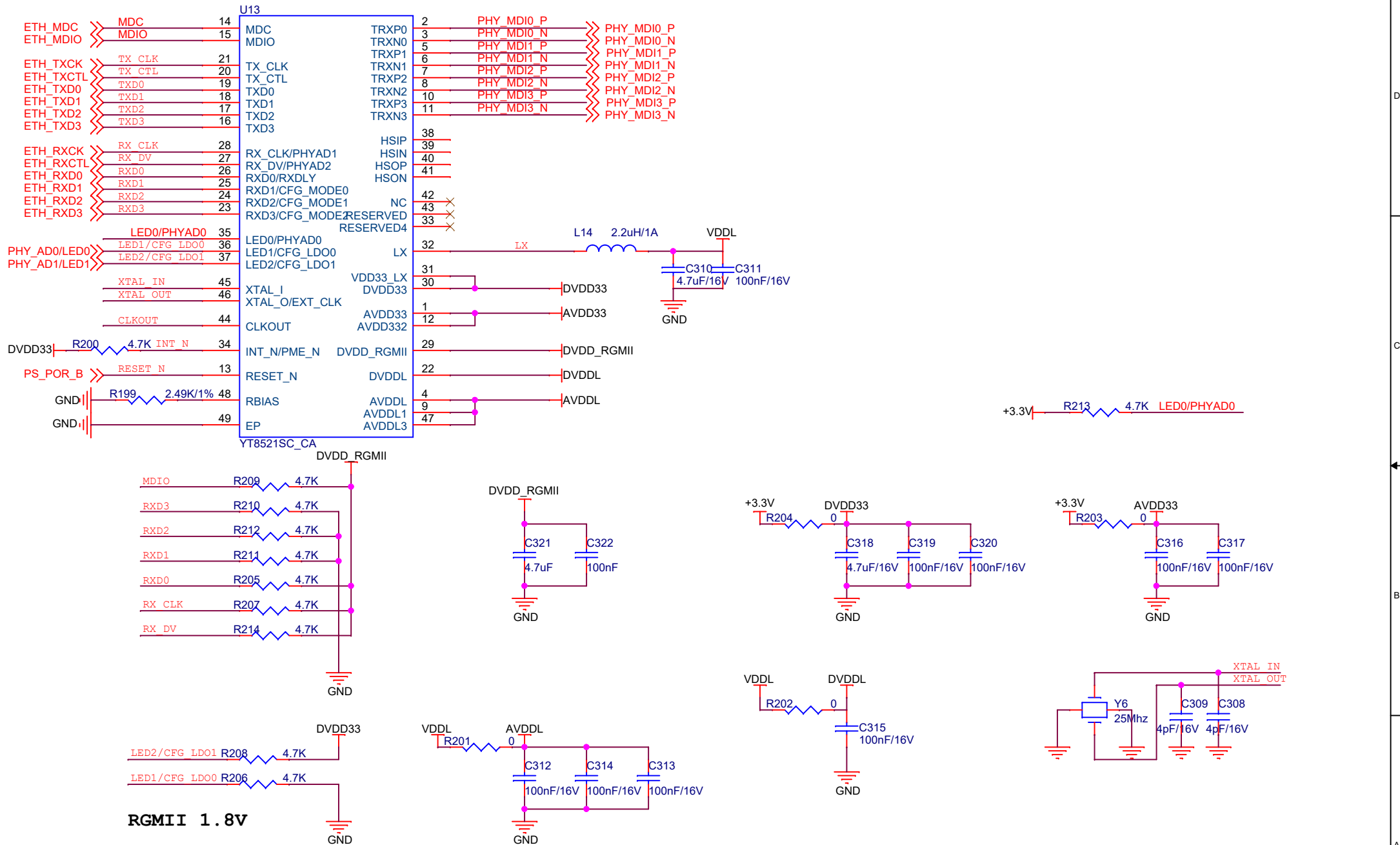
Power On Sequence:
1.0V -> 1.8V -> 1.35V -> 3.3V -> VCCIO



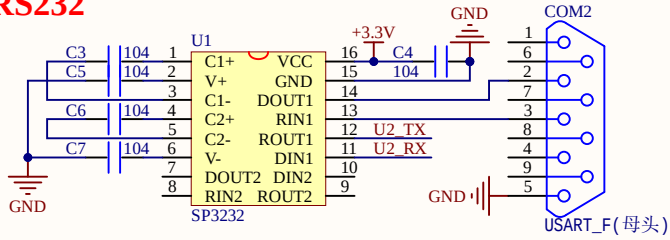
$$V_{out} = 0.8 \cdot R1/R2 + 0.8$$



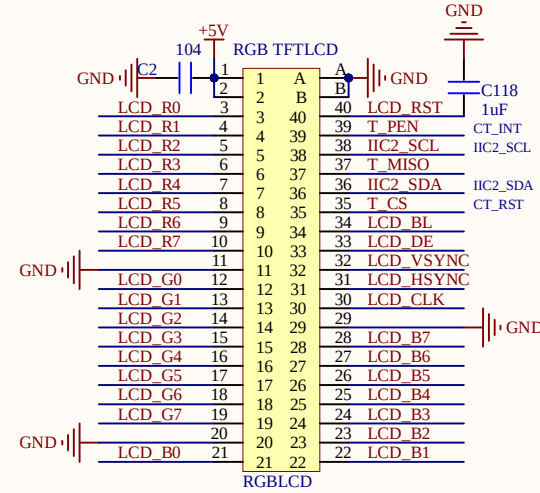
Design Name			
ZYNQ-CORE			
Size	Page Name	Rev	
A3	10_POWER	1.0	
Date:	Wednesday, June 19, 2024	Sheet	10 of 12



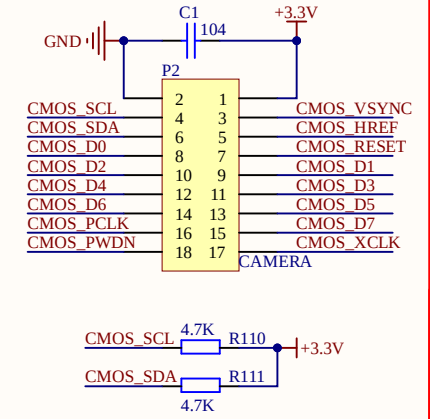
RS232



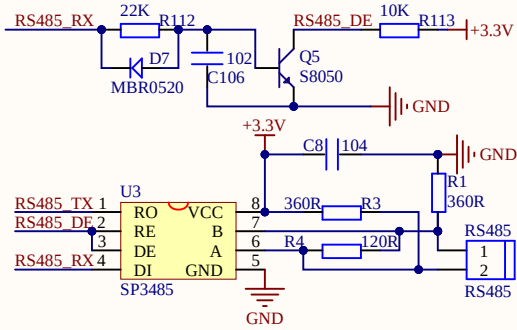
RGB LCD



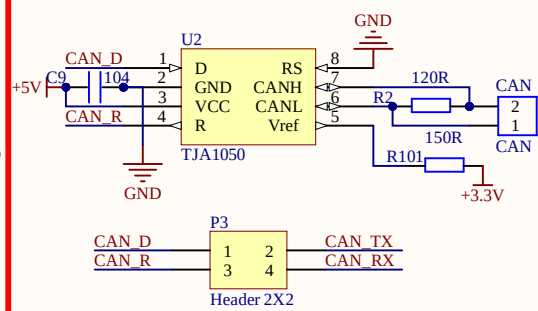
OLED&CAMERA



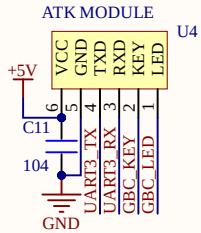
RS485



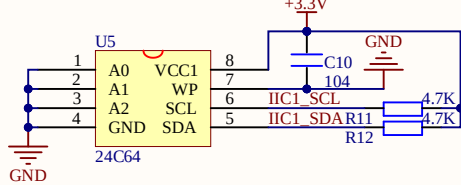
CAN



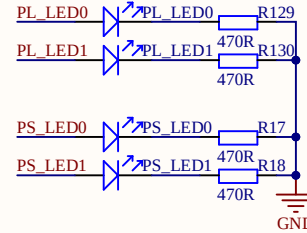
ATK MODULE



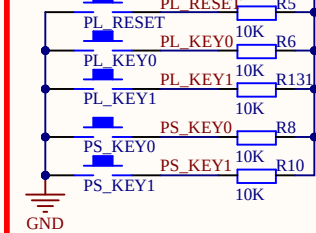
EEPROM



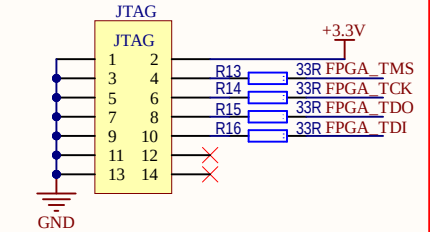
LED



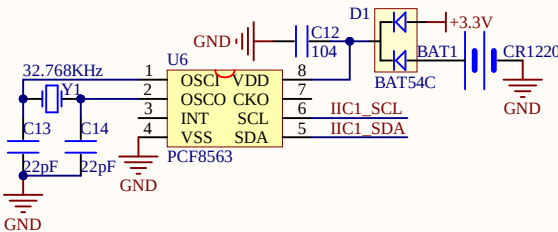
KEY



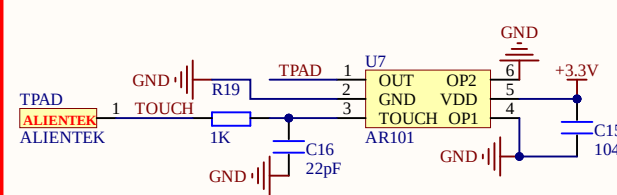
JTAG



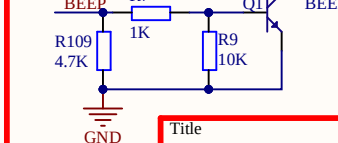
RTC



TOUCH KEY

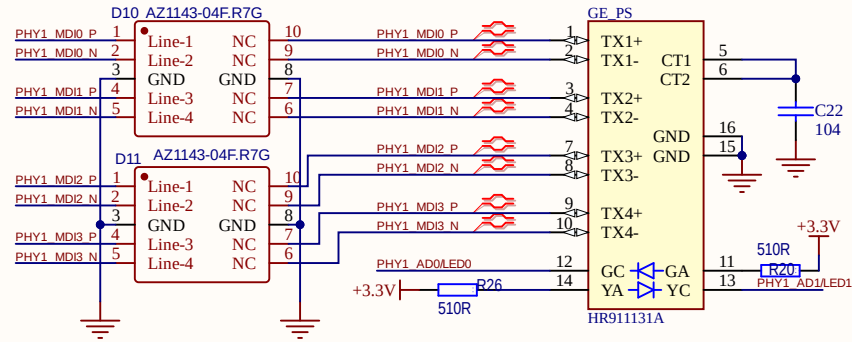


BEEP

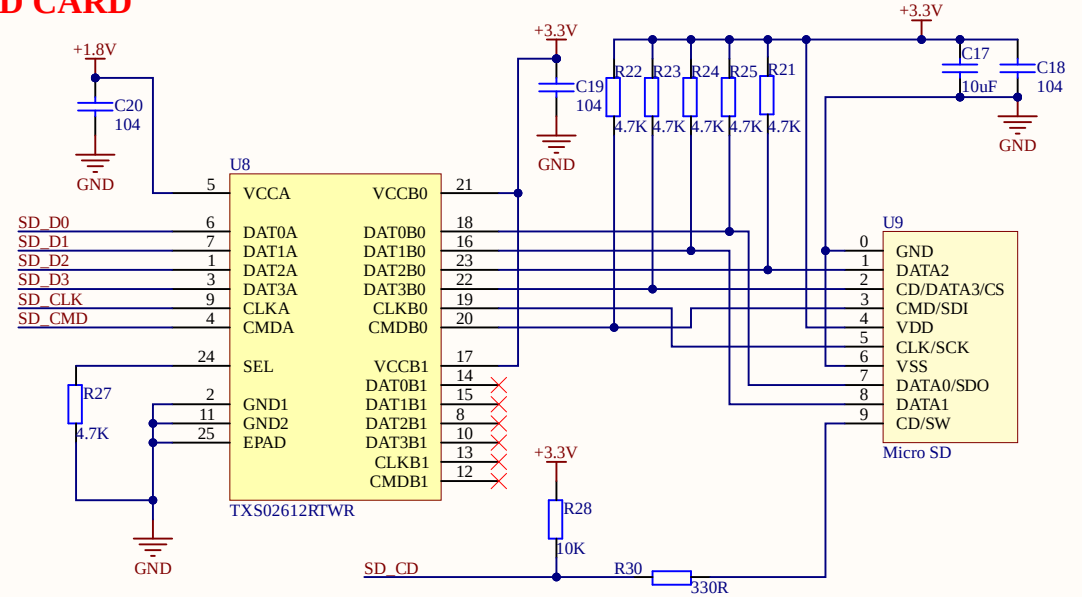


Title		
Size	Number	Revision
A4		
Date:	9/18/2024	Sheet of
File:	E:\领航者\DEVICE.SchDoc	Drawn By:

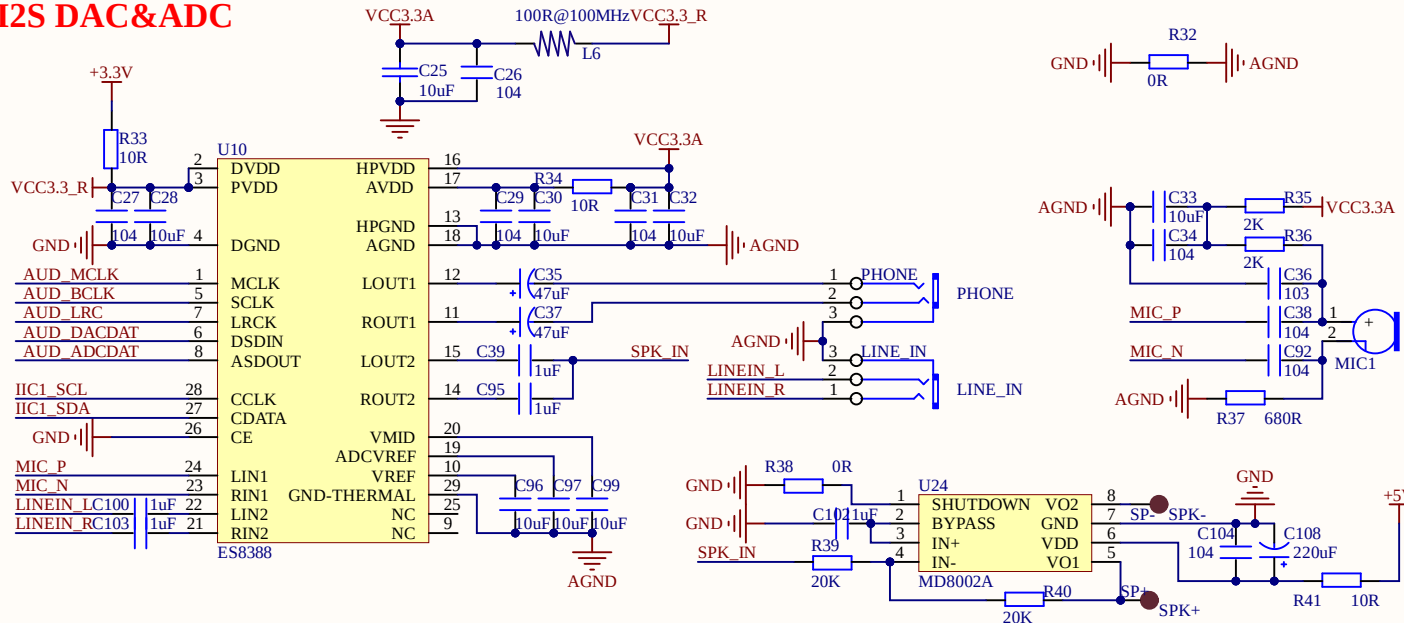
ETHERNET(PS)



SD CARD



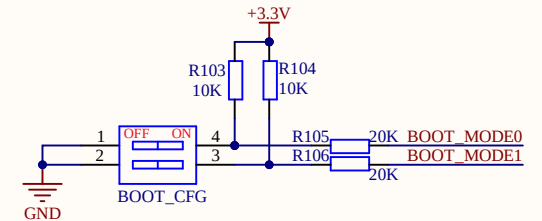
I2S DAC&ADC



SPK+和SPK-连接开发板自带的喇叭，在开发板底部。

BOOT OPTION

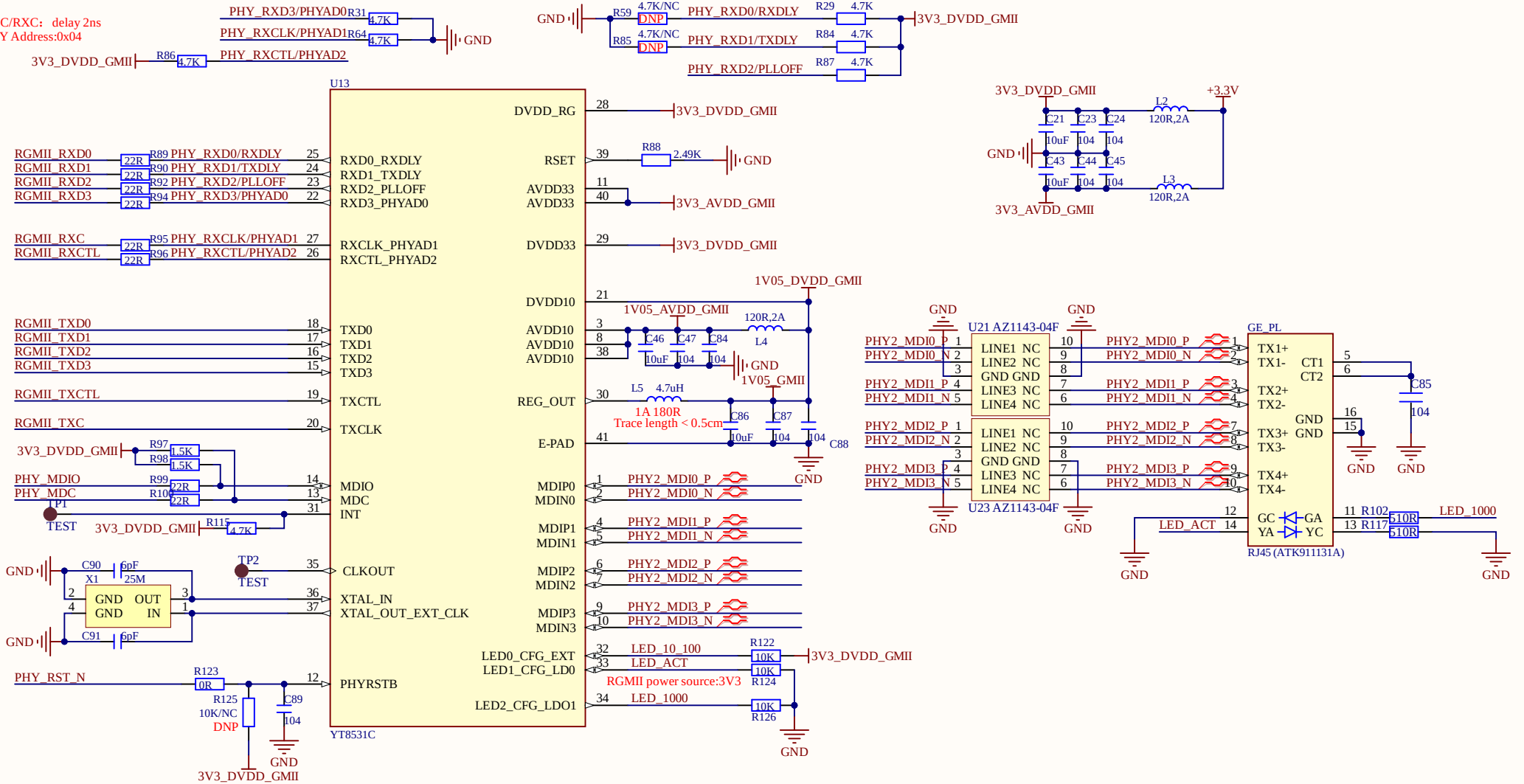
BOOT_CFG	MIO 4	MIO 5
JTAG	ON	ON
NAND	OFF	ON
QSPI	ON	OFF
SD Card	OFF	OFF



Title

Size	Number	Revision
A4		
Date:	9/18/2024	Sheet of
File:	E:\领航者\ETH&SD&AUDIO.SchDoc	Drawn By:

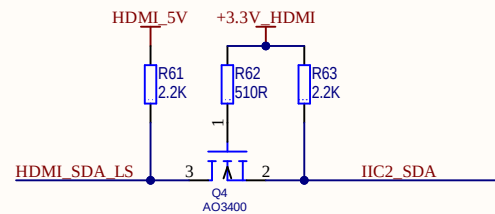
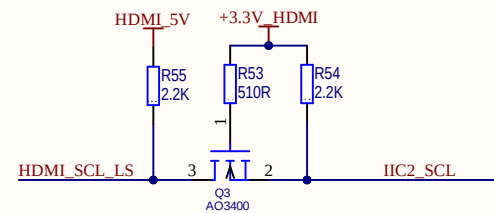
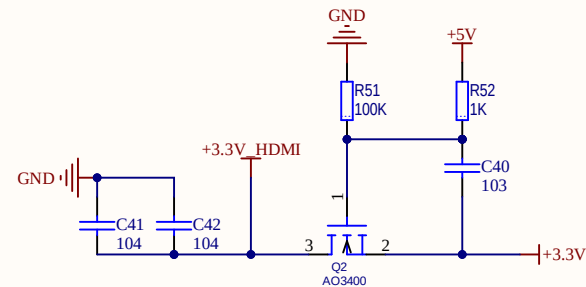
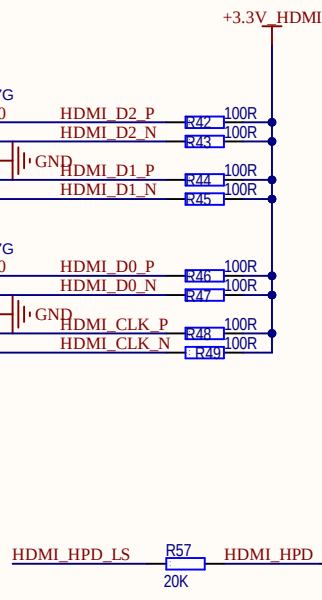
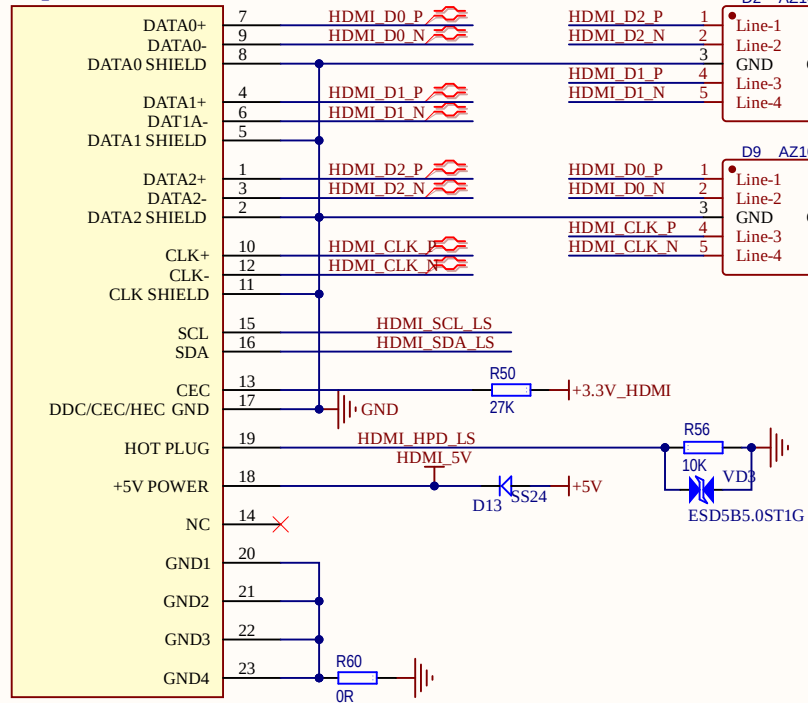
TXC/RXC: delay 2ns
PHY Address:0x04



Title:	ATK-DF7010P_7020P.PrjPCB		Cannot open file G:\AD标准库\字体模板\AD通用模板标题栏\正点LOGO竖.png. File does not exist.
Author:	ALIENTEK	Size: A4	
Date:	2024/9/18	File: ETH.SchDoc	
Revision:		Version: V1.6	

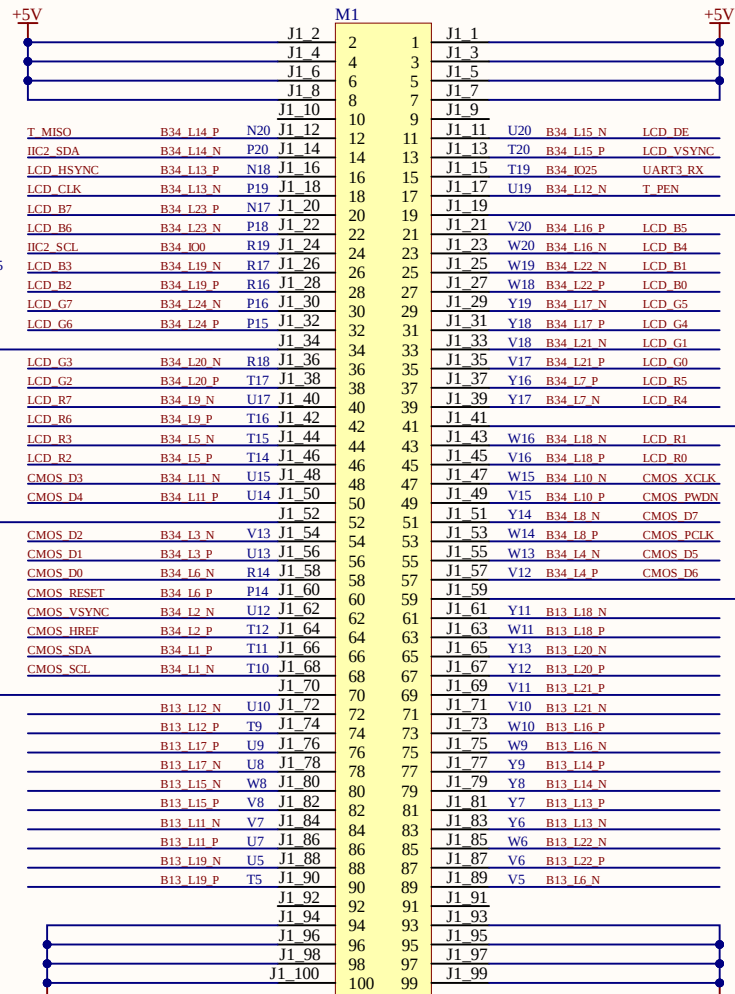
HDMI

HDMI
HDMI_A

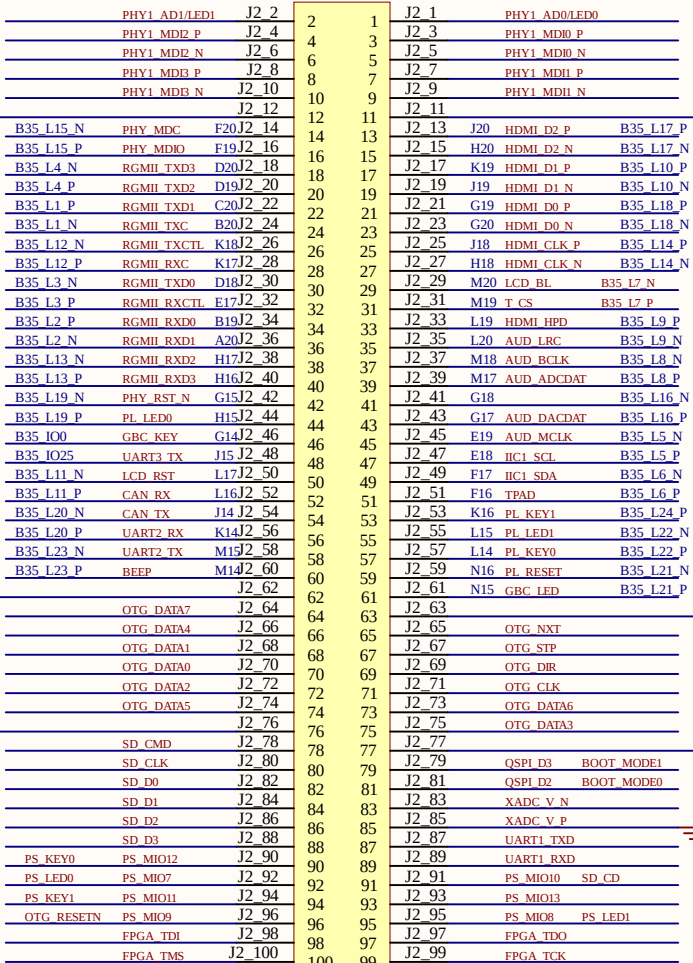


Title		
Size	Number	Revision
A4		
Date:	9/18/2024	Sheet of
File:	E:\领航者\HDMI.SchDoc	Drawn By:

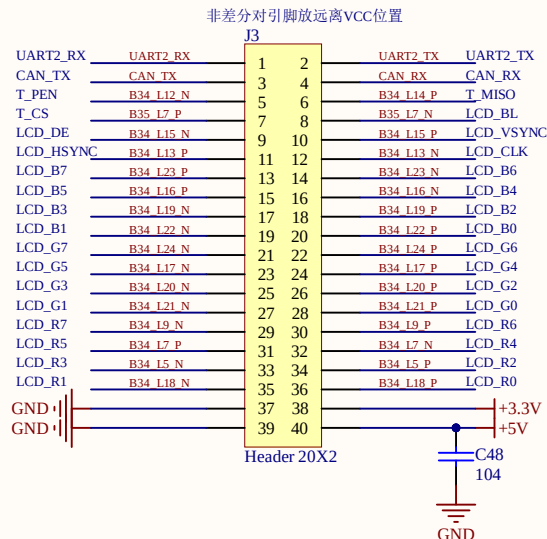
CORE_IO



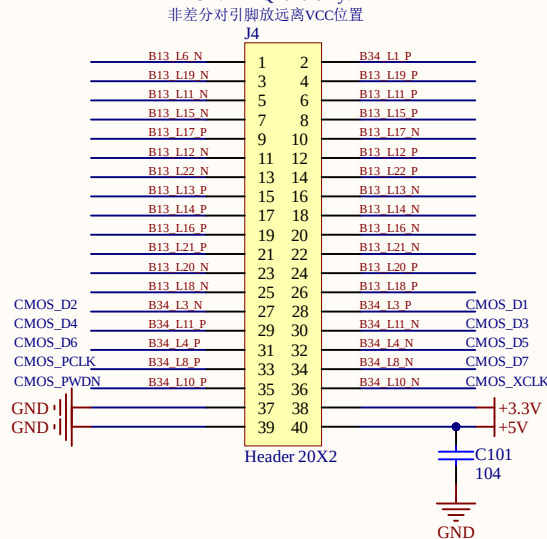
ZYNQ7010&7020



EXT_IO

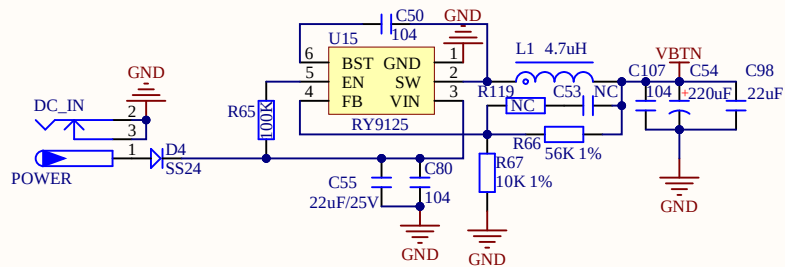


BANK13 (ZYNQ7020 Only)

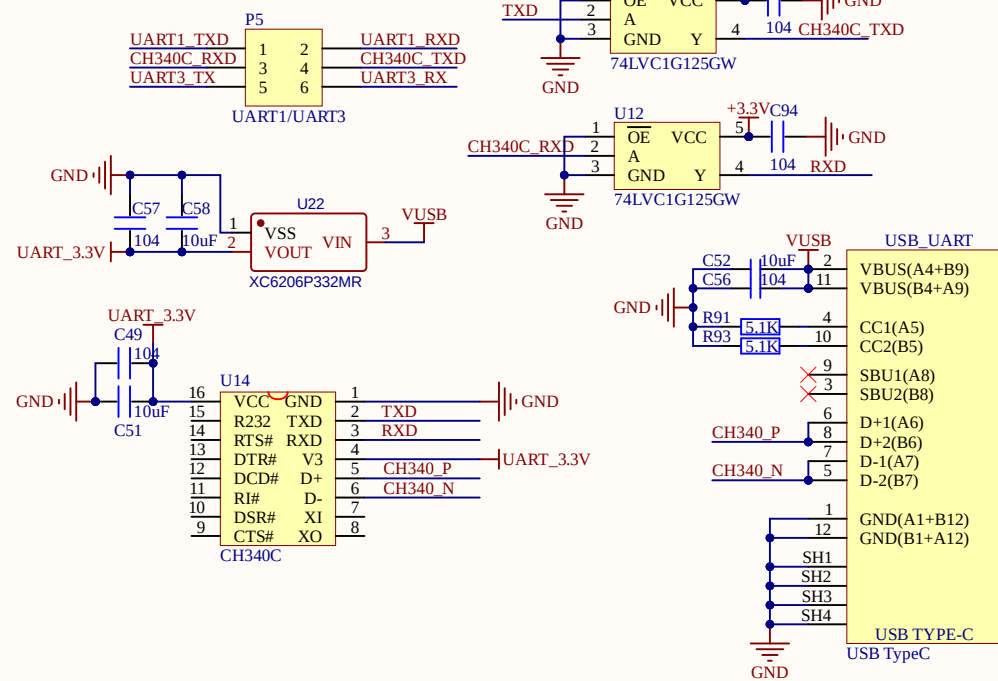


Title		
Size	Number	Revision
A4		
Date:	9/18/2024	Sheet of
File:	E:\领航者\IO.SchDoc	Drawn By:

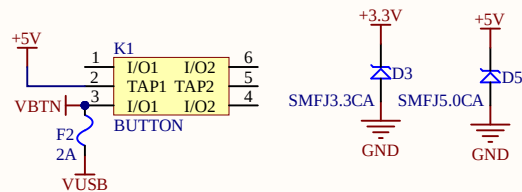
DC-DC POWER IN



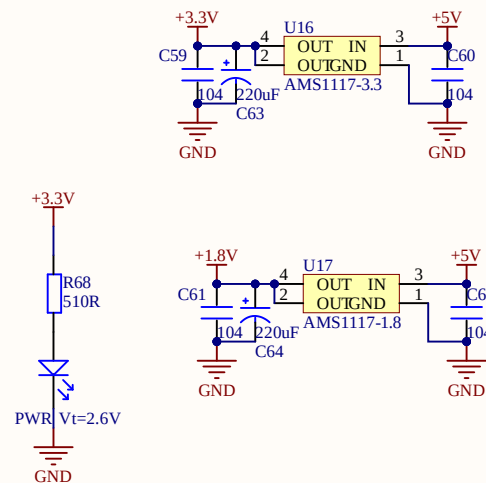
USB UART&USB POWER



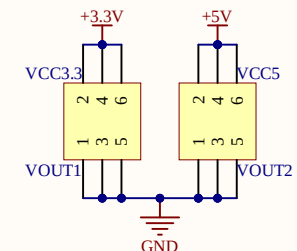
POWER SWITCH



1.8V/3.3V POWER

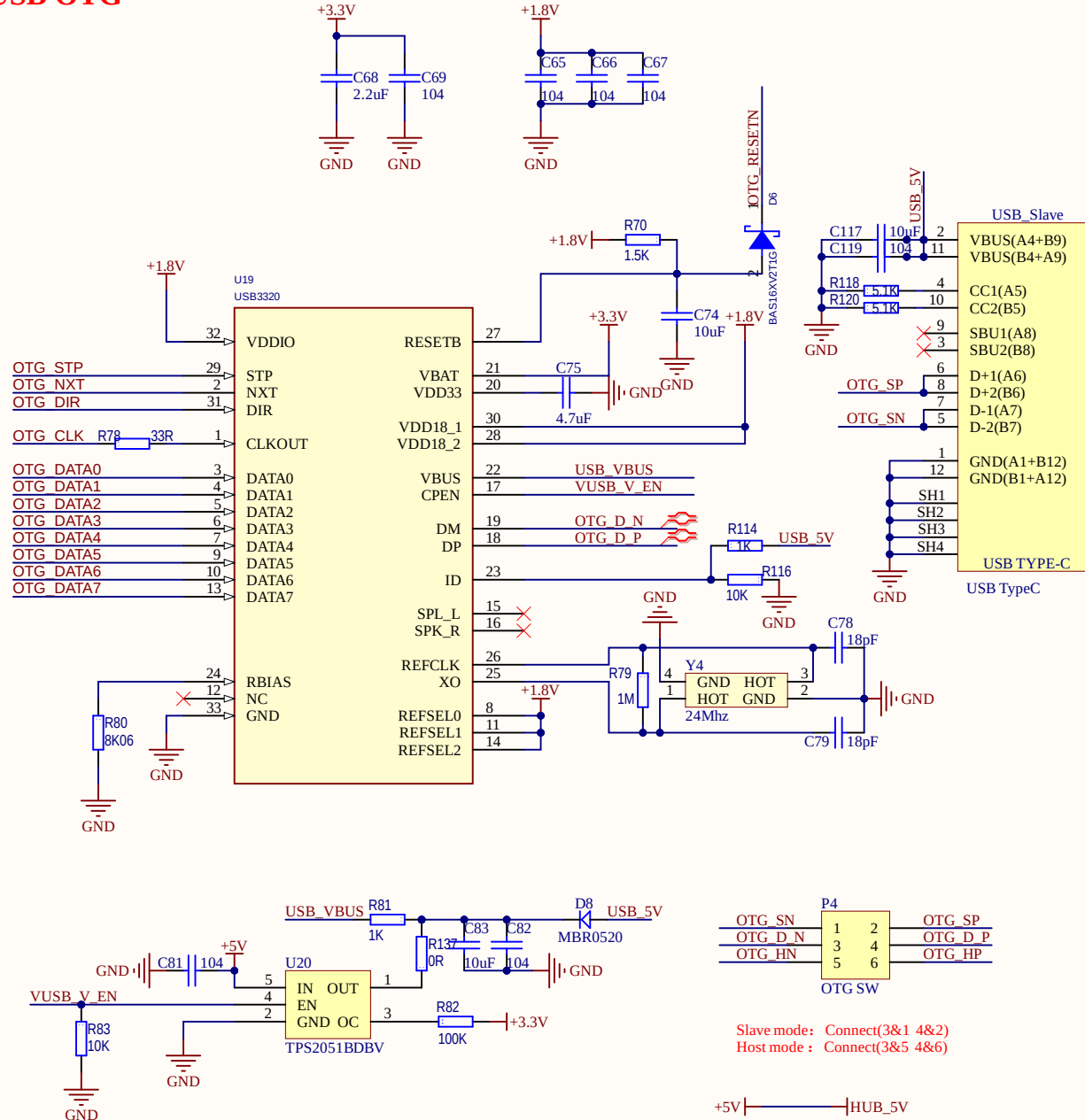


ON BOARD POWER SOURCE

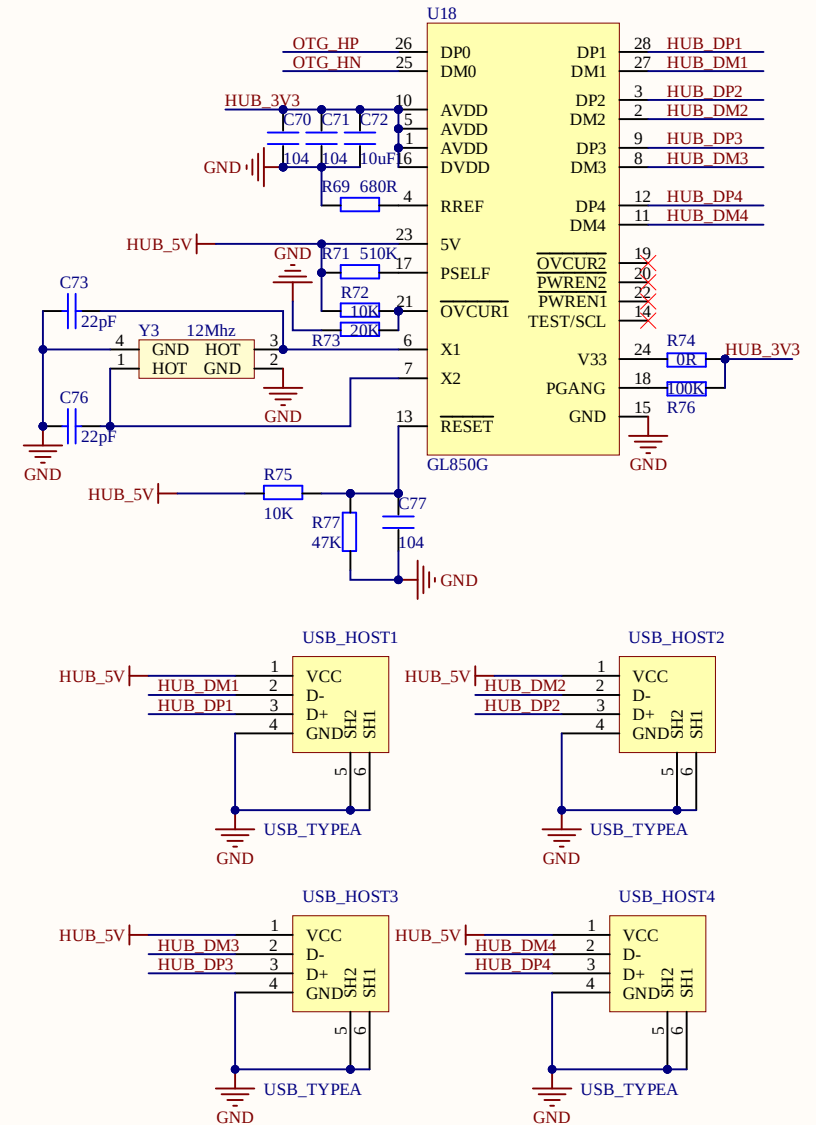


Title		
Size	Number	Revision
A4		
Date:	9/18/2024	Sheet of
File:	E:\领航者\POWER&UART.SchDoc	Drawn By:

USB OTG



USB HUB



Title	USB OTG
-------	---------

Size A4	Number V1.0	Revision
Date:	9/18/2024	Sheet of
File:	E:\领航者\..USB.SchDoc	Drawn By: