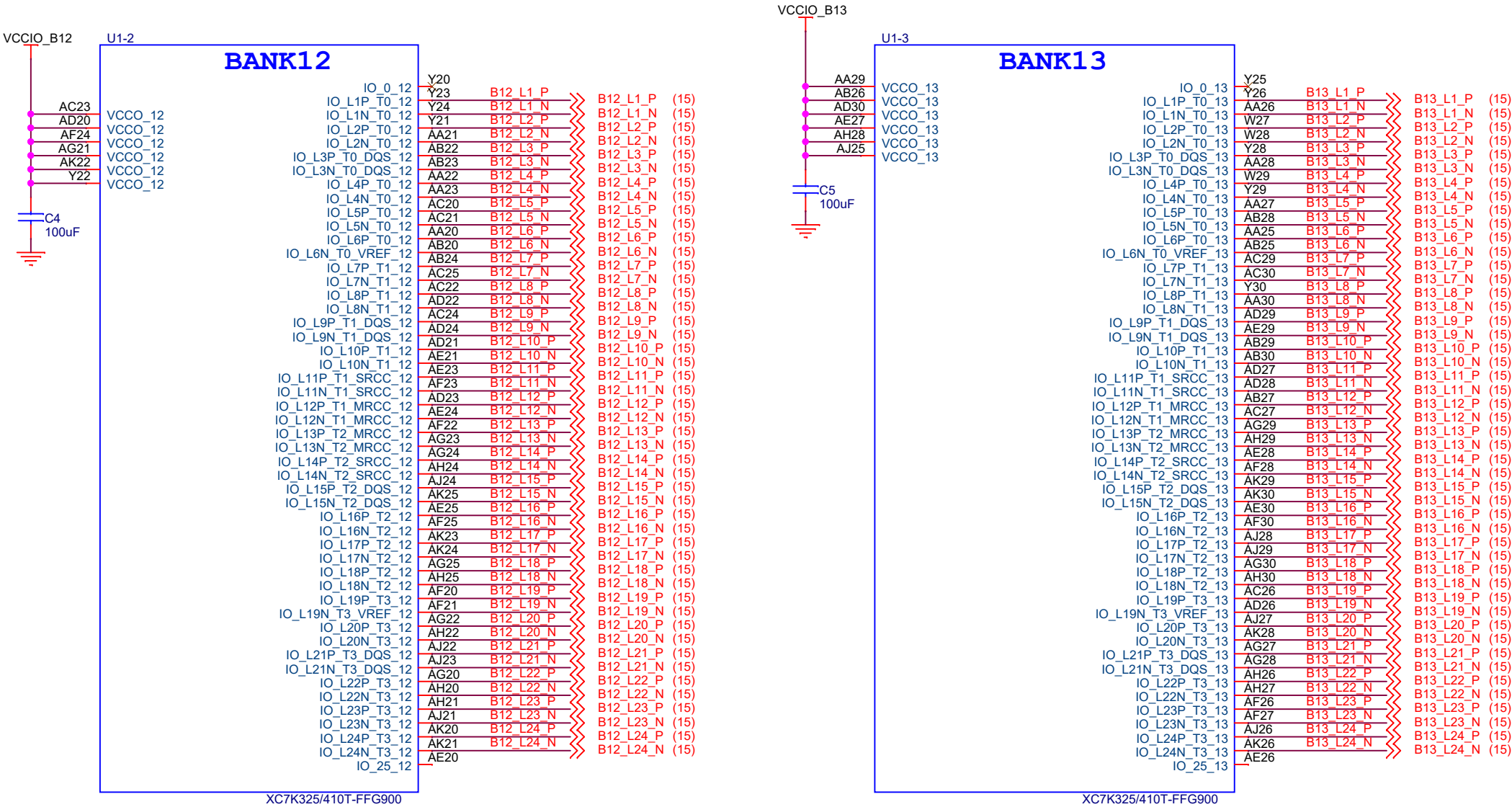
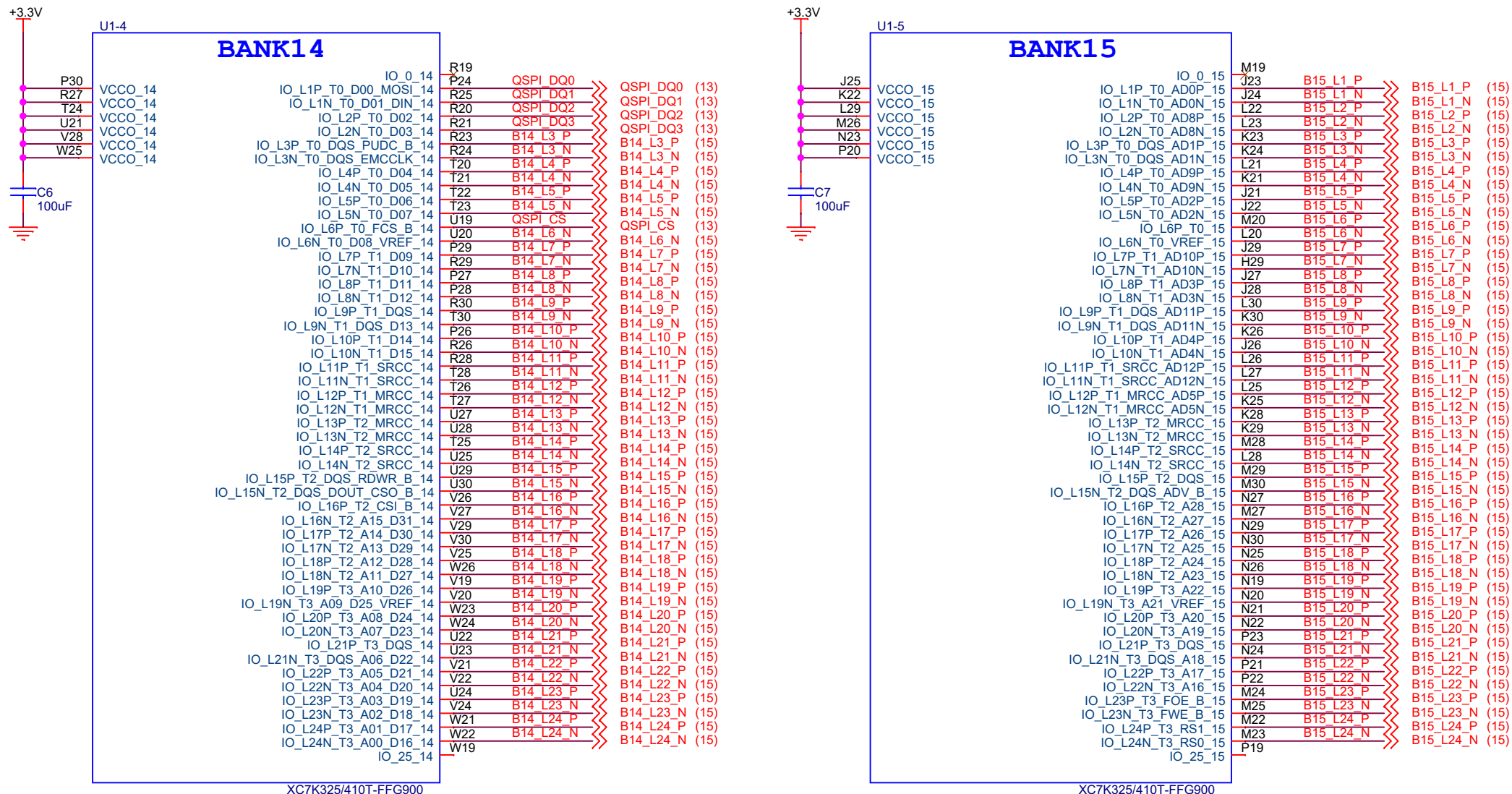


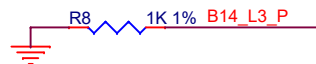
BANK12和BANK13为HR (High Range) BANK，表示支持wider range of I/O standards，最高能够支持到3.3v的电压。



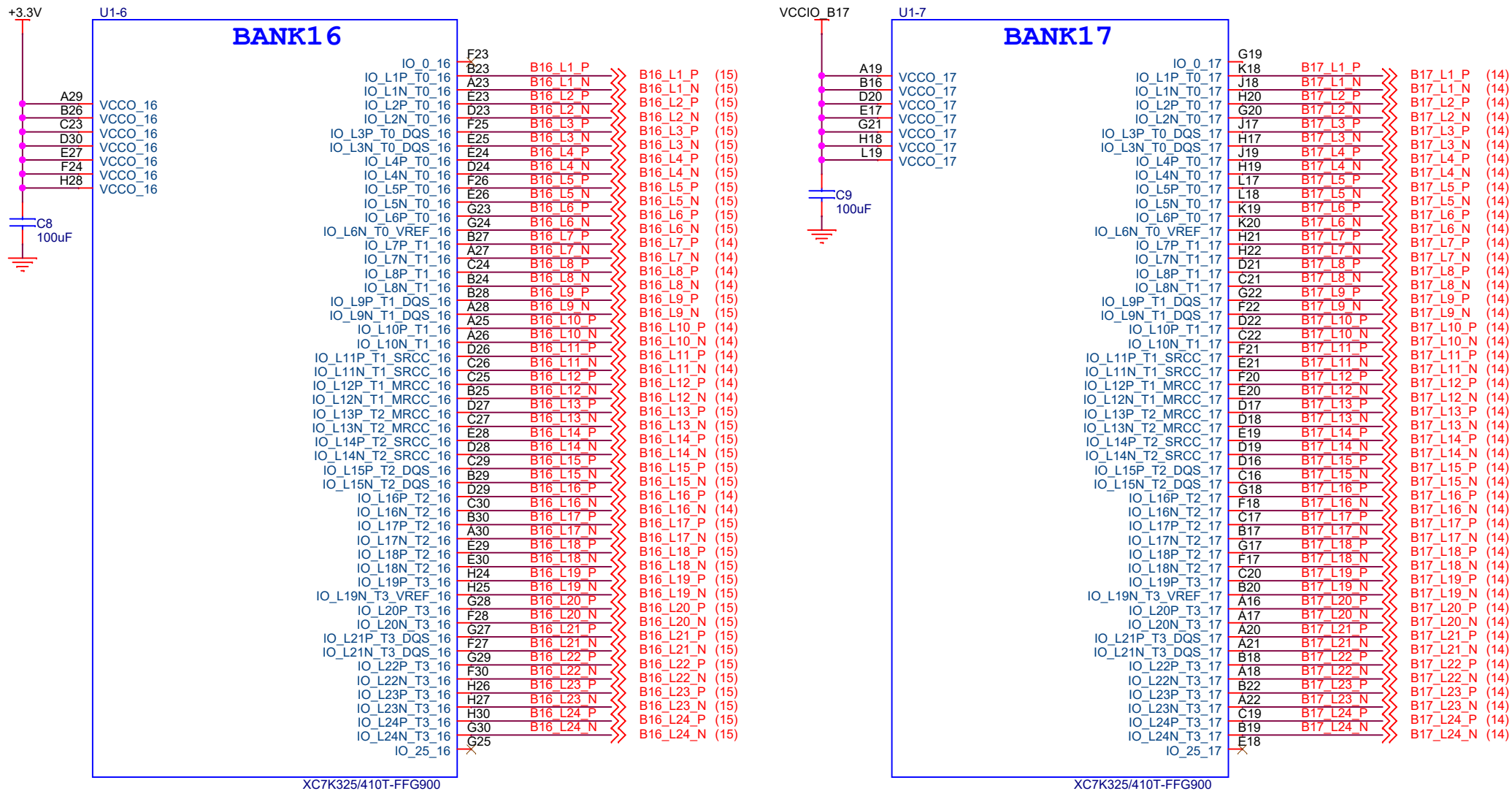
BANK14和BANK15为HR(High Range) BANK, 表示支持wider range of I/O standards, 最高能够支持到3.3V的电压。



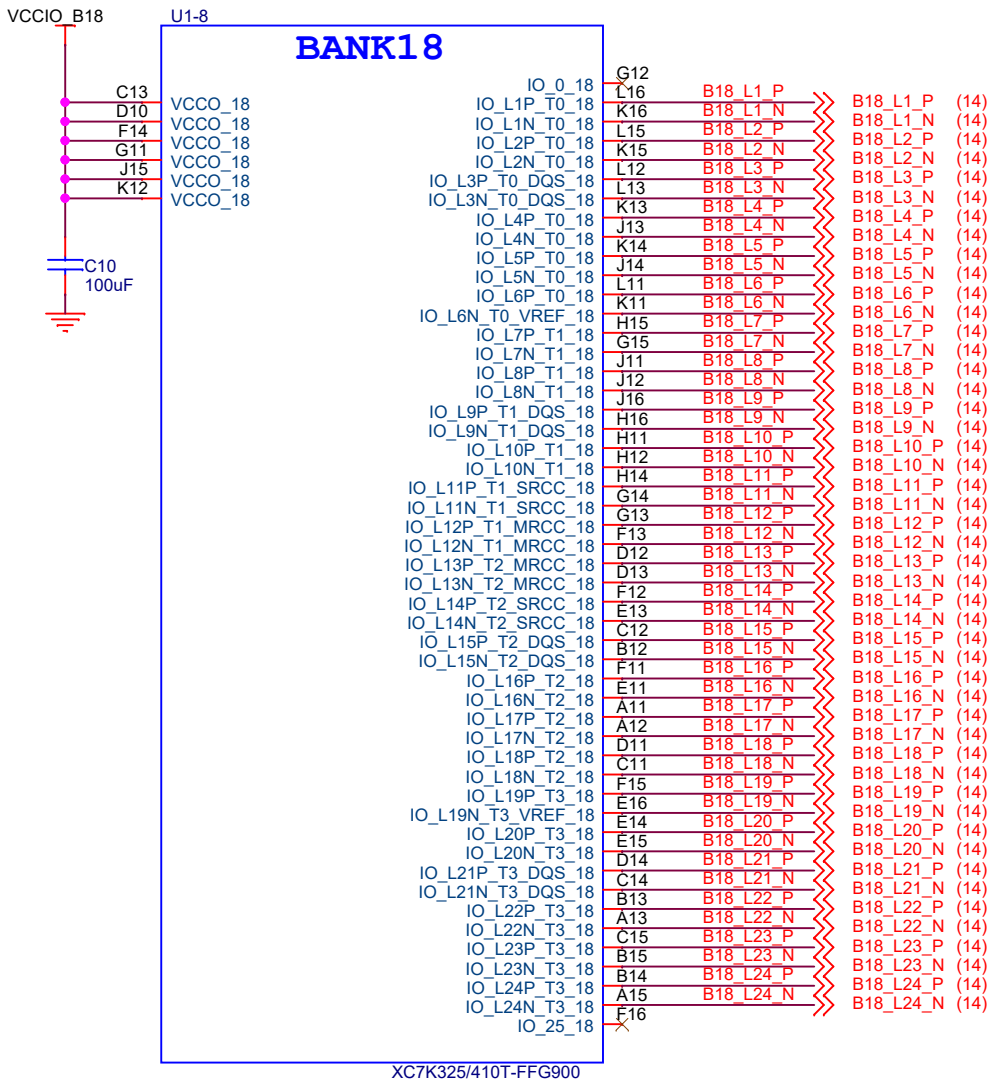
PUDC_B=0: Enable internal pull-up resistance



BANK16和BANK17为HR(High Range) BANK，表示支持wider range of I/O standards，最高能够支持到3.3V的电压。



BANK18为HR(High Range) BANK，表示支持wider range of I/O standards，最高能够支持到3.3v的电压。

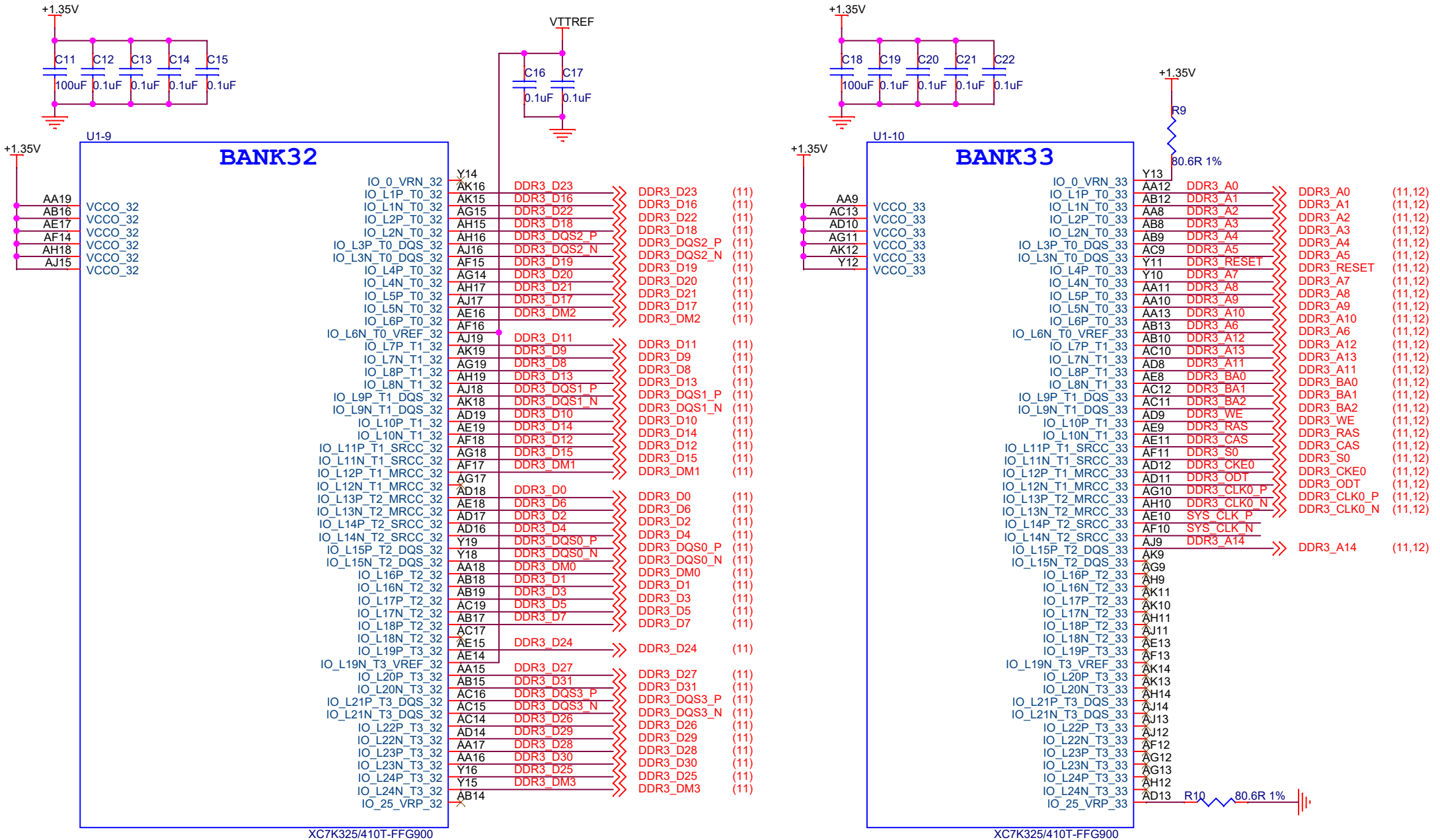


XC7K325/410T-FFG900

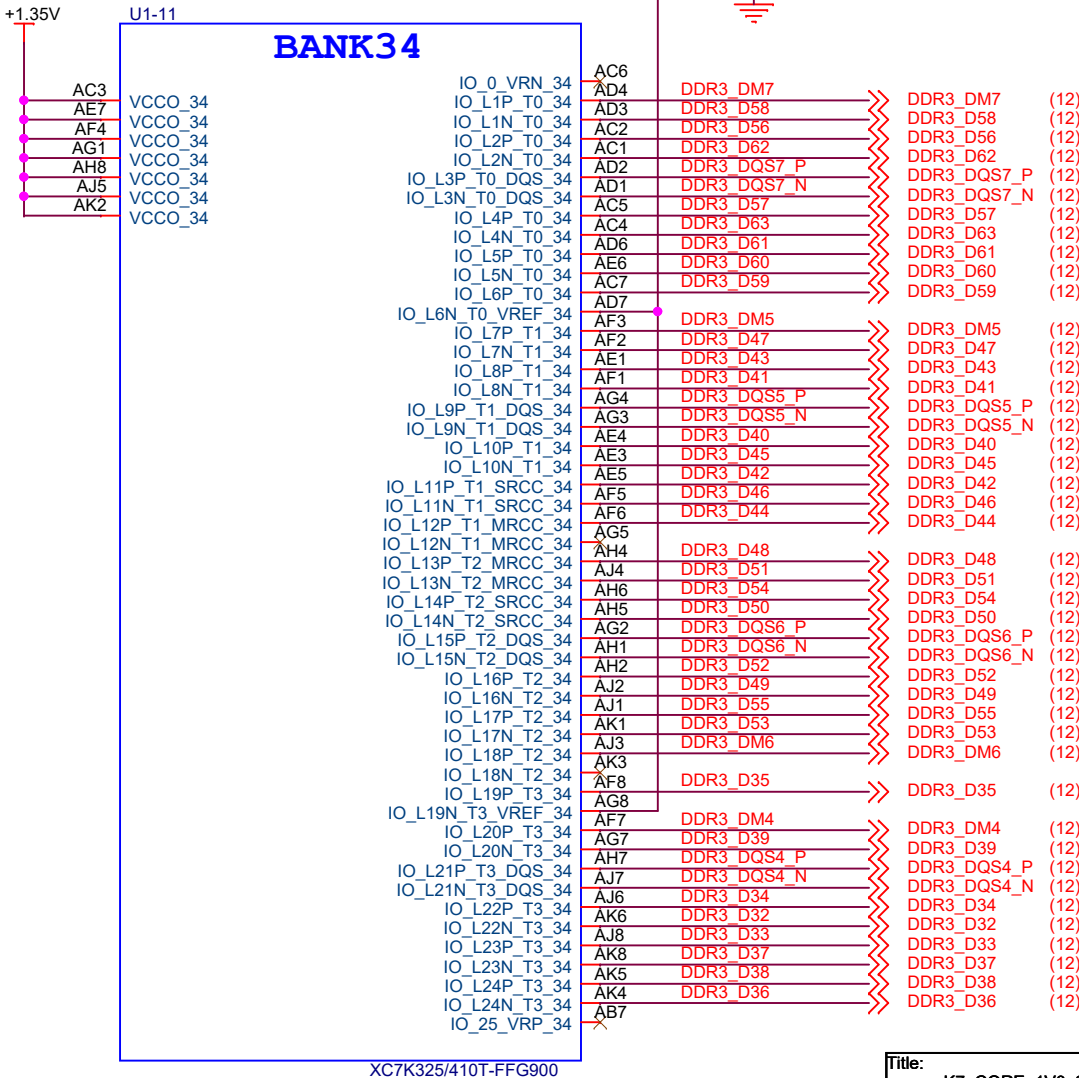
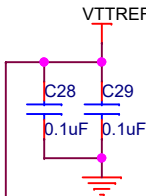
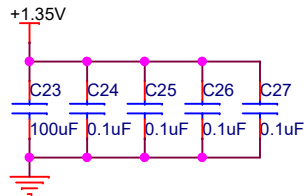
Title:		K7 CORE 1V0 20240723 USER	
Author:	ALIENTEK	Date:	Wednesday, June 19, 2024
Version:	V1.0	File:	05 BANK18
Size:	A4	Sheet	5 of 17



BANK32和BANK33为HP(High Performance) BANK, 应用于高性能也就是速度比较高的场景, 由于速率比较高, Bank电压最高也只能到1.8v。



BANK34为HP(High Performance) BANK，应用于高性能也就是速度比较高的场景，由于速率比较高，Bank电压最高也只能到1.8V。



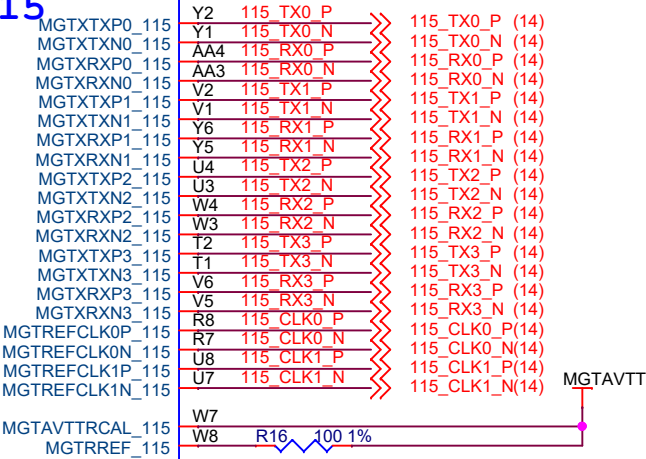
Title: K7 CORE 1V0 20240723 USER	
Author: ALIENTEK	Date: Tuesday, July 23, 2024
Version: V1.0	File: 07 BANK34
Size: A4	Sheet 7 of 17



MGT (Multi-gigabit transceiver), 即高速串行收发器模块

U1-12

BANK115

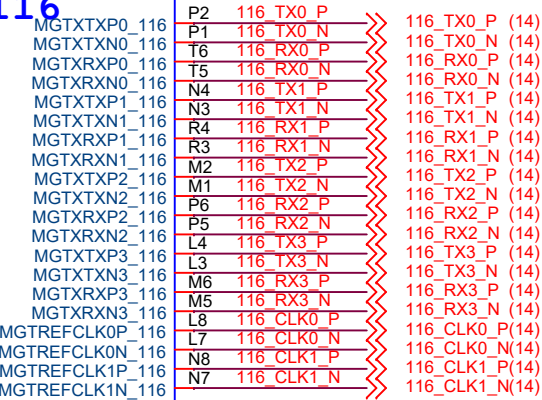


XC7K325/410T-FFG900

Trace length from the resistor pins to the
FPGA pins MGTRREF and MGTAVTTRCAL
Connection must be equal in length

U1-13

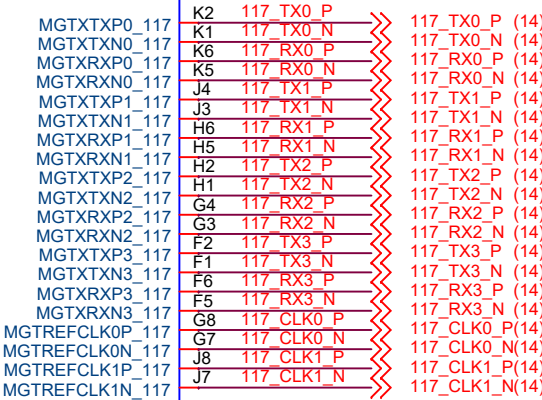
BANK116



XC7K325/410T-FFG900

U1-14

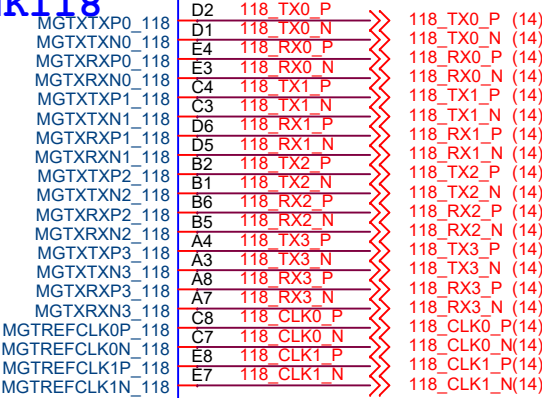
BANK117



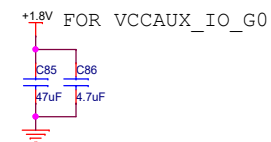
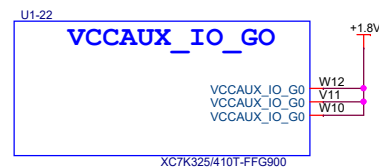
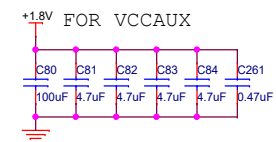
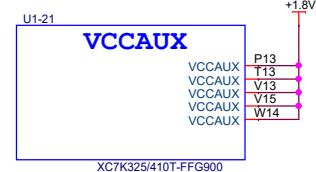
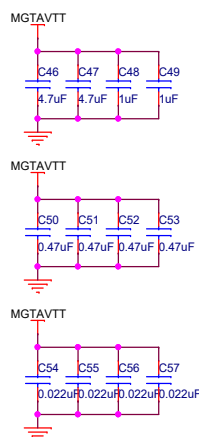
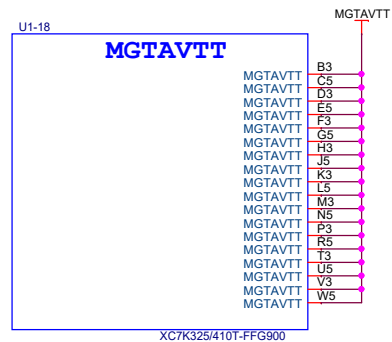
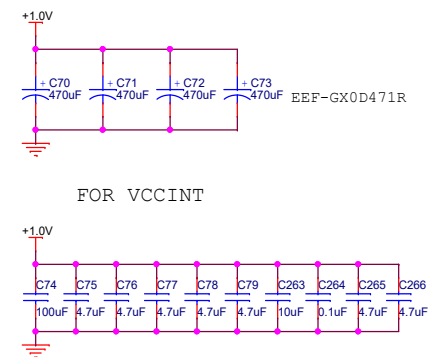
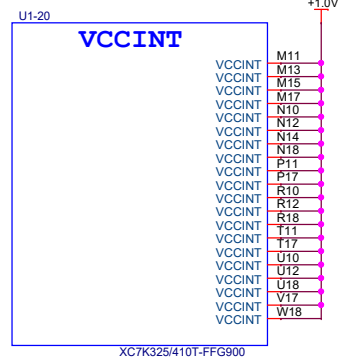
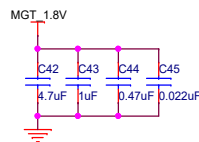
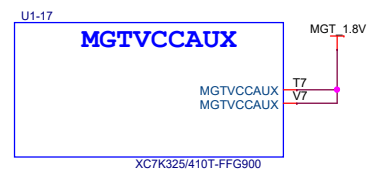
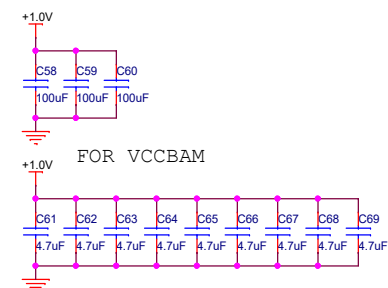
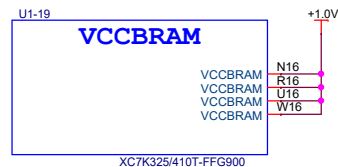
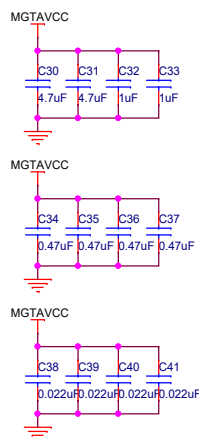
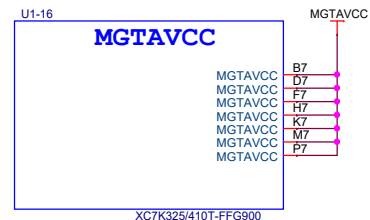
XC7K325/410T-FFG900

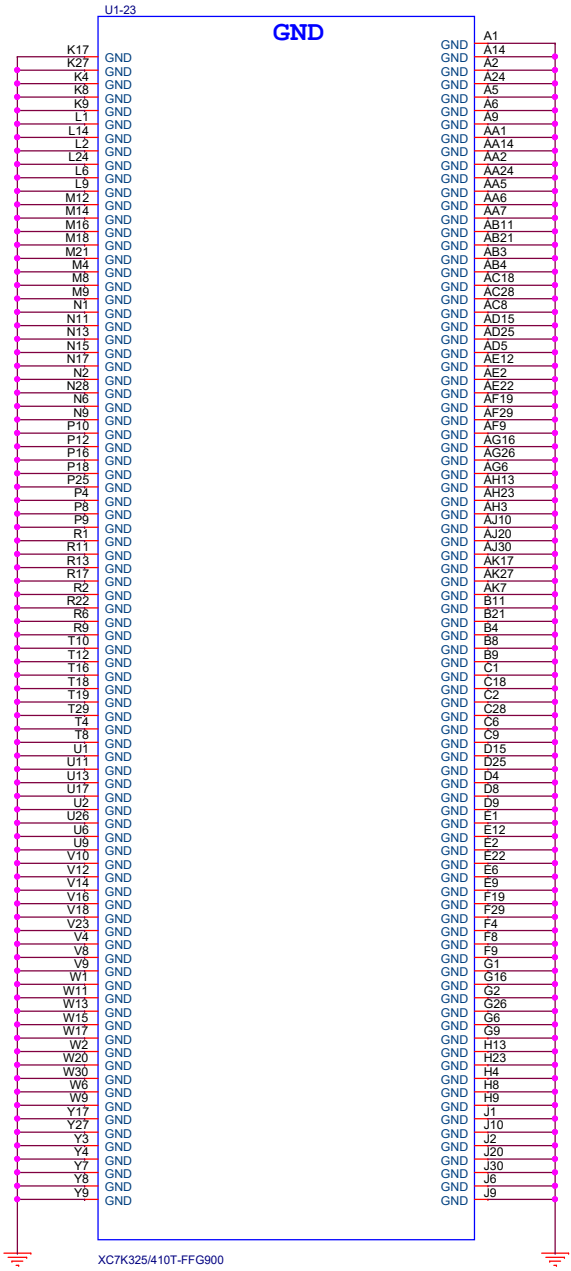
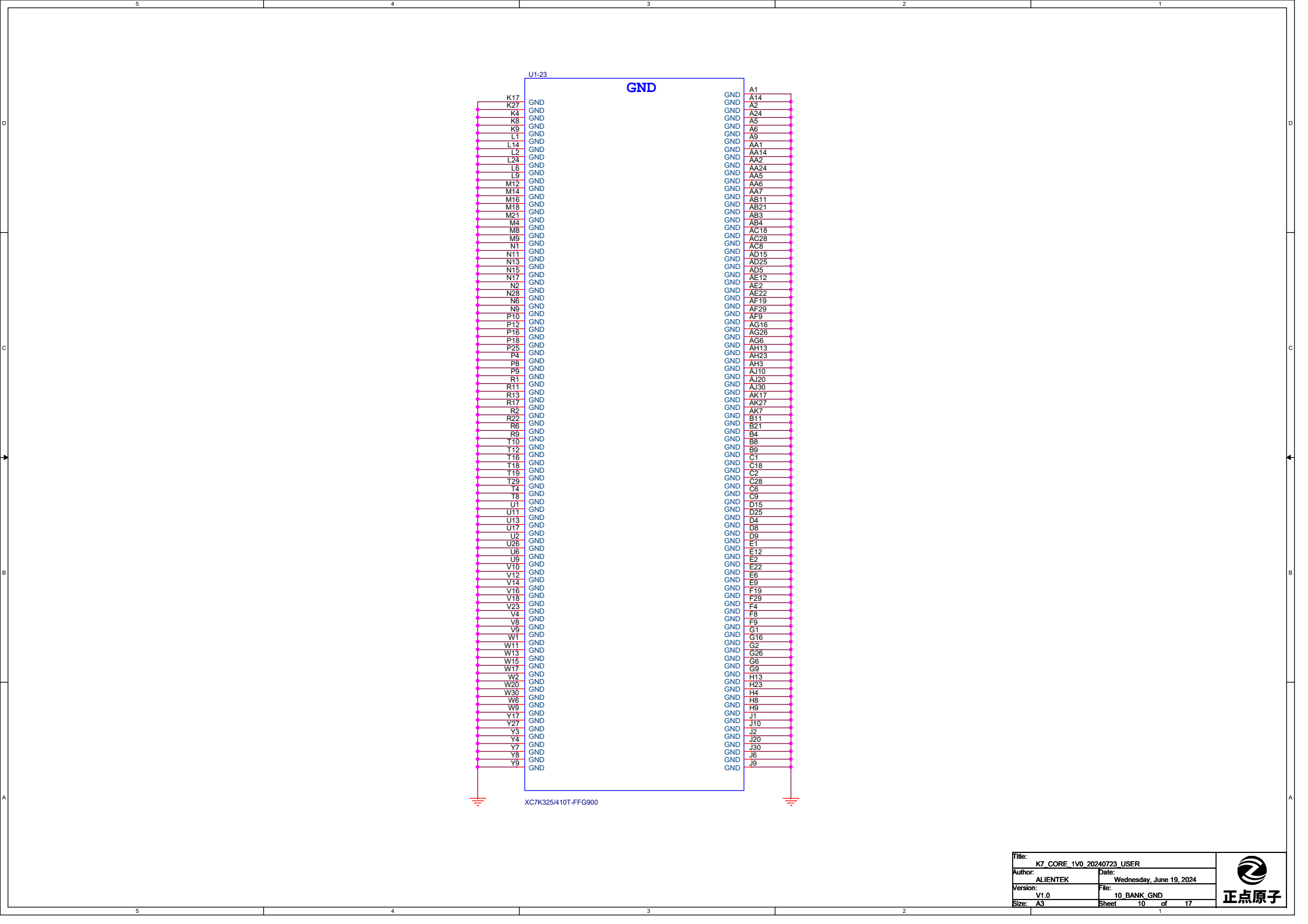
U1-15

BANK118



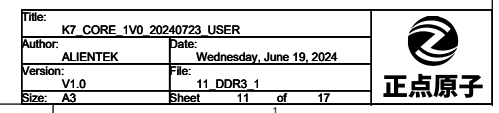
XC7K325/410T-FFG900

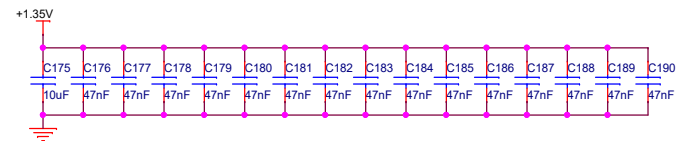
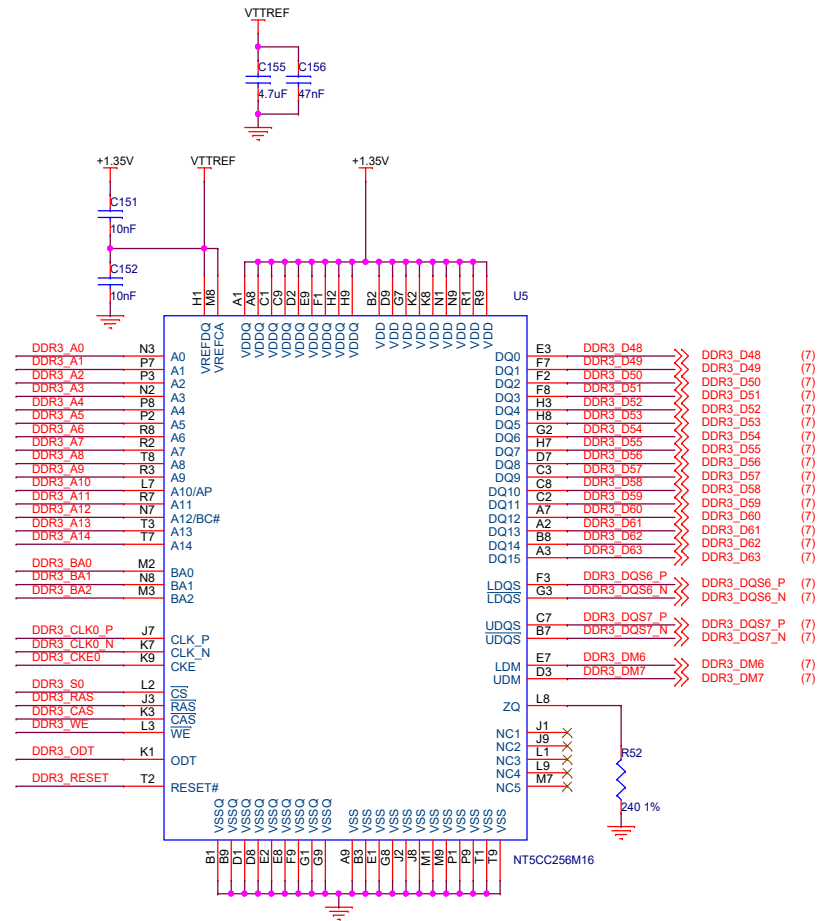
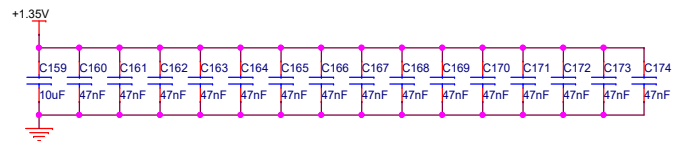
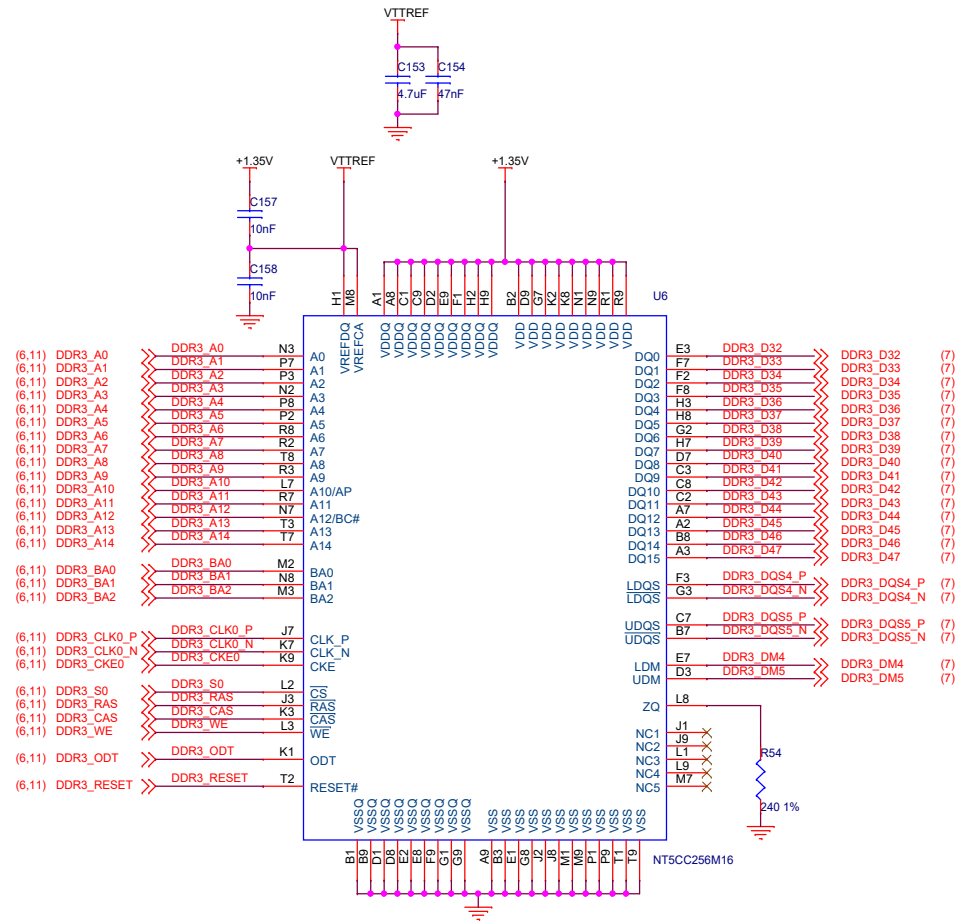




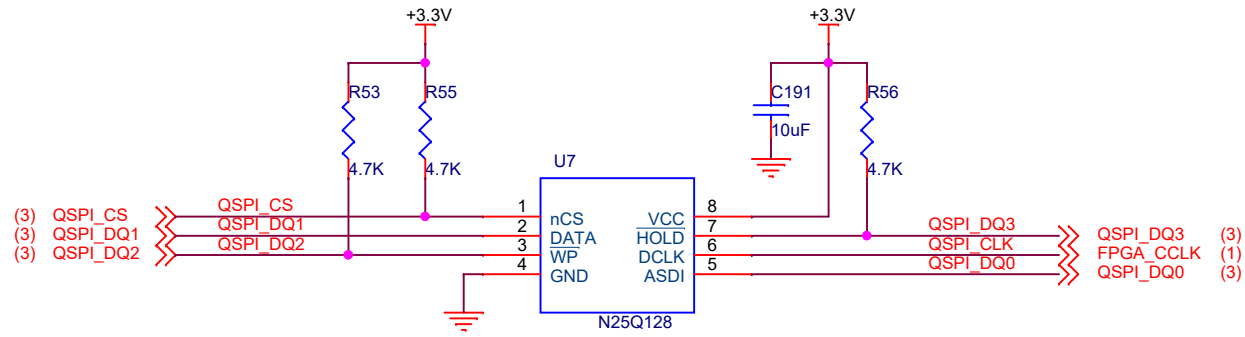
Title: K7 CORE 1V0 20240723 USER	
Author: ALIENTEK	Date: Wednesday, June 19, 2024
Version: V1.0	File: 10 BANK GND
Size: A3	Sheet 10 of 17



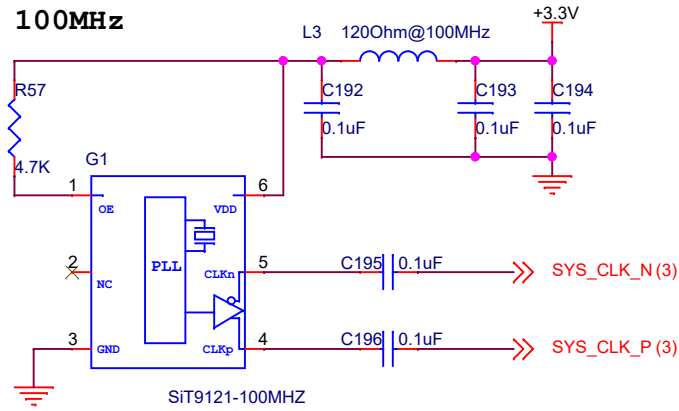




FPGA CONFIG SPI



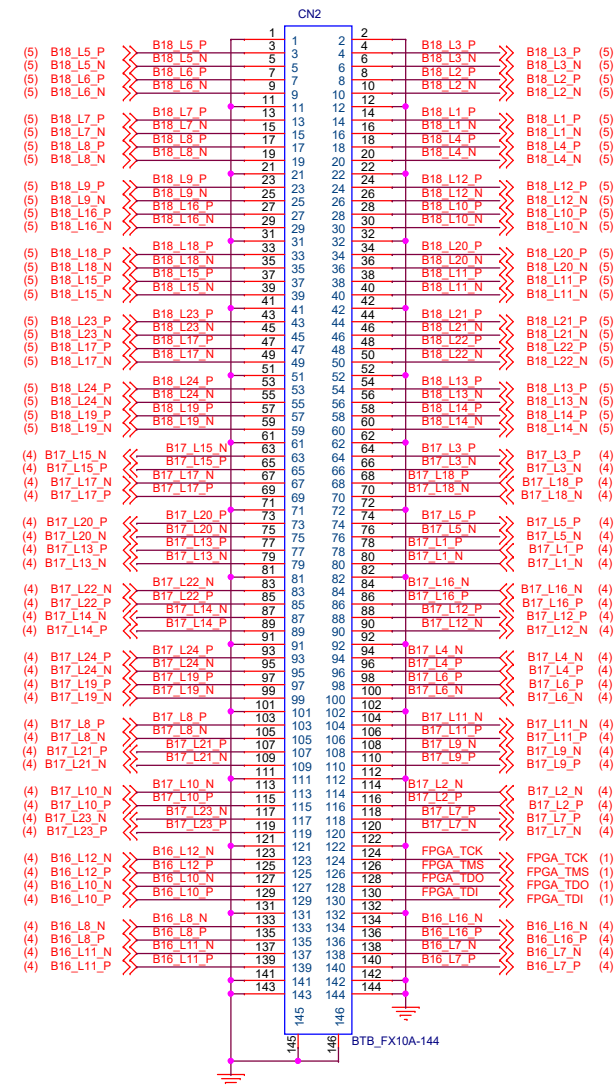
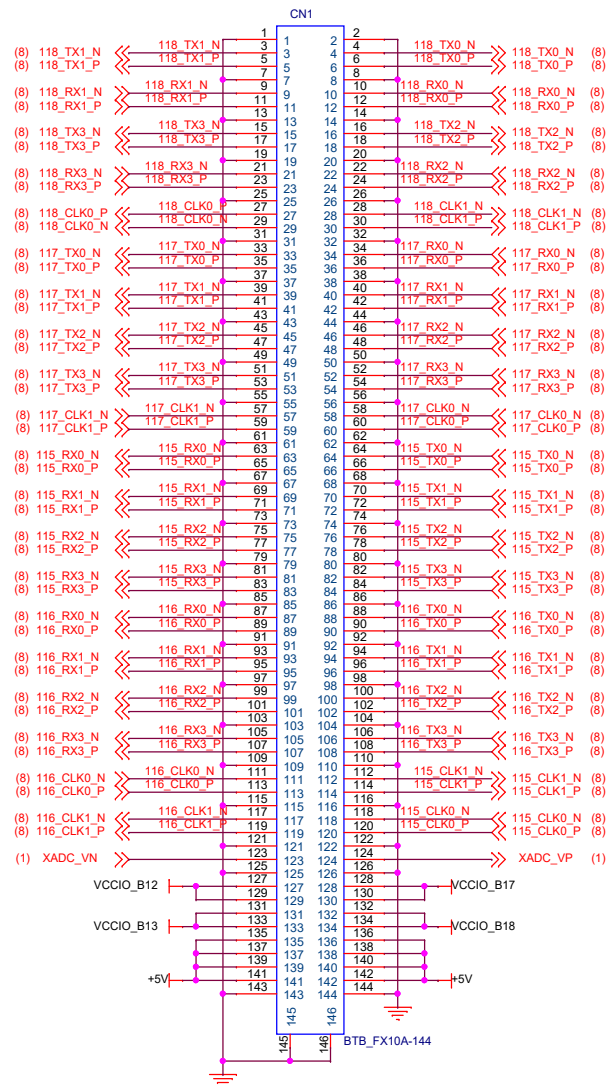
SYSTEM CLOCK

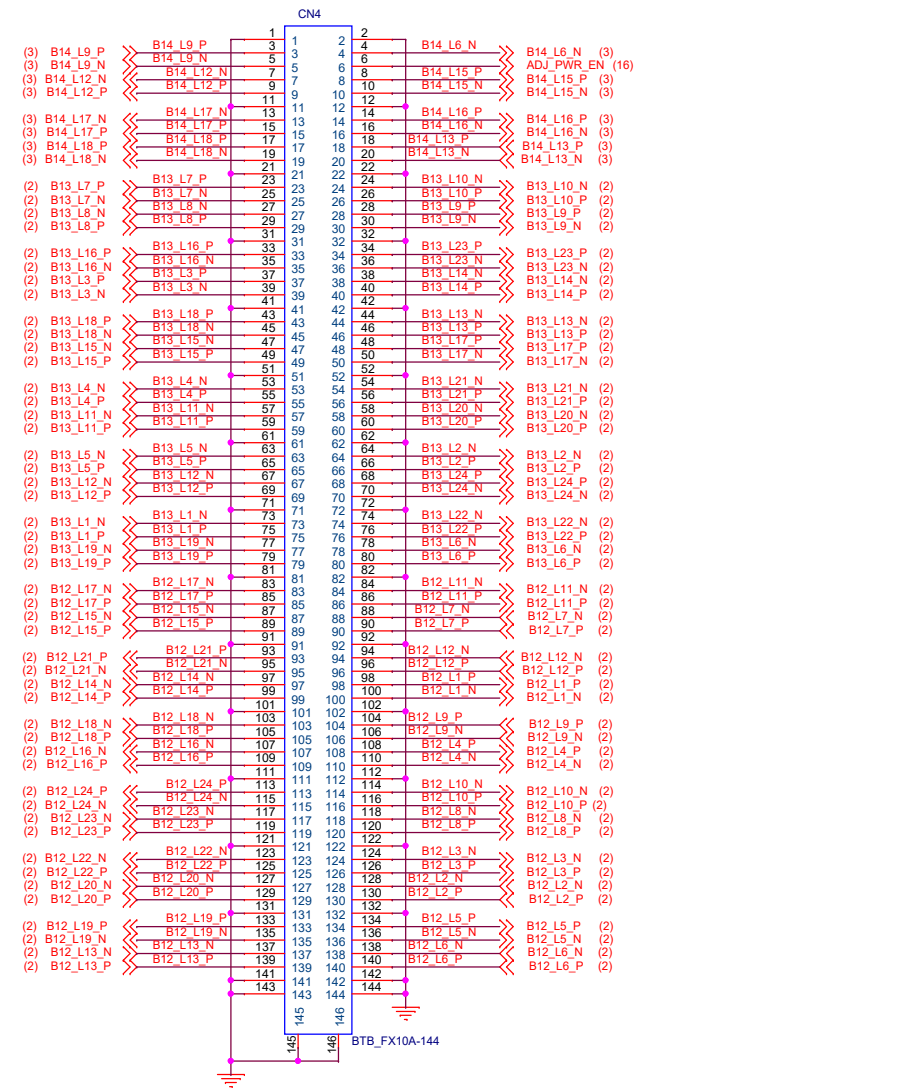
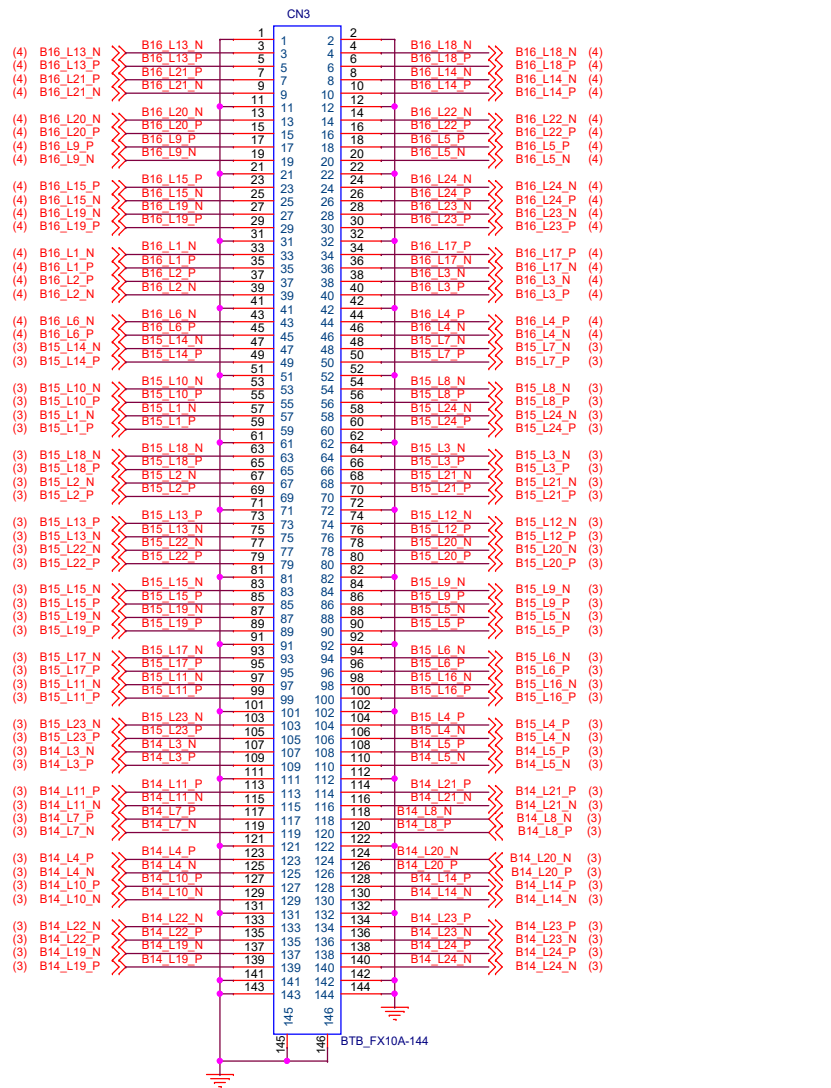



Title: K7 CORE 1V0 20240723 USER	
Author: ALIENTEK	Date: Monday, November 20, 2023
Version: V1.0	File: 13 FLASH CLOCK
Size: A4	Sheet 13 of 17



正点原子





Title: K7 CORE 1V0 20240723 USER		 正点原子
Author: ALIENTEK	Date: Tuesday, July 23, 2024	
Version: V1.0	File: 16 PMIC 1	
Size: A4	Sheet 16 of 17	

+1.0V → MGTAVCC → MGTAVTT
MGT_1.8V

