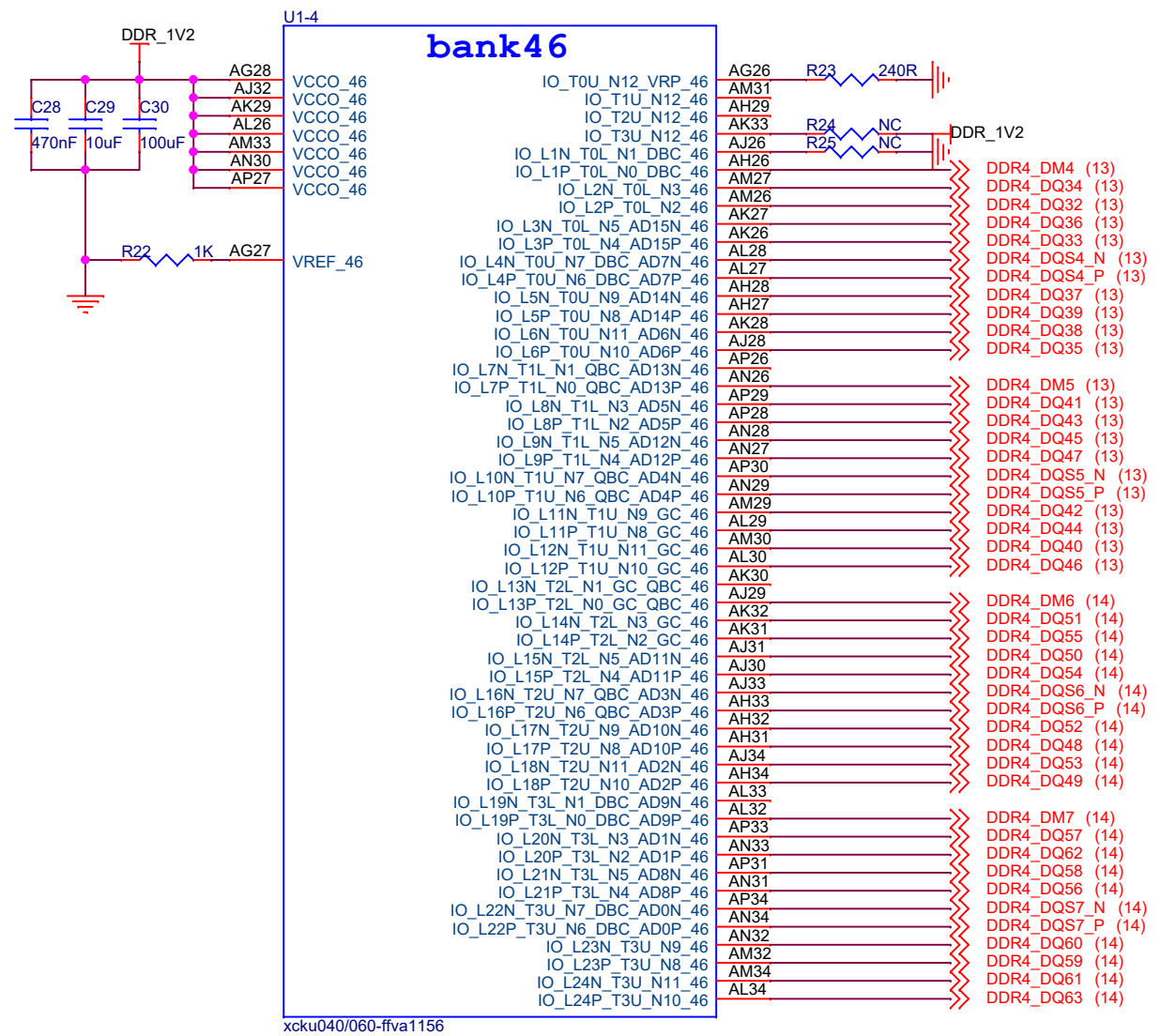


BANK46为HP(High Performance) BANK，应用于高性能也就是速度比较高的场景，由于速率比较高，Bank电压最高也只能到1.8v。

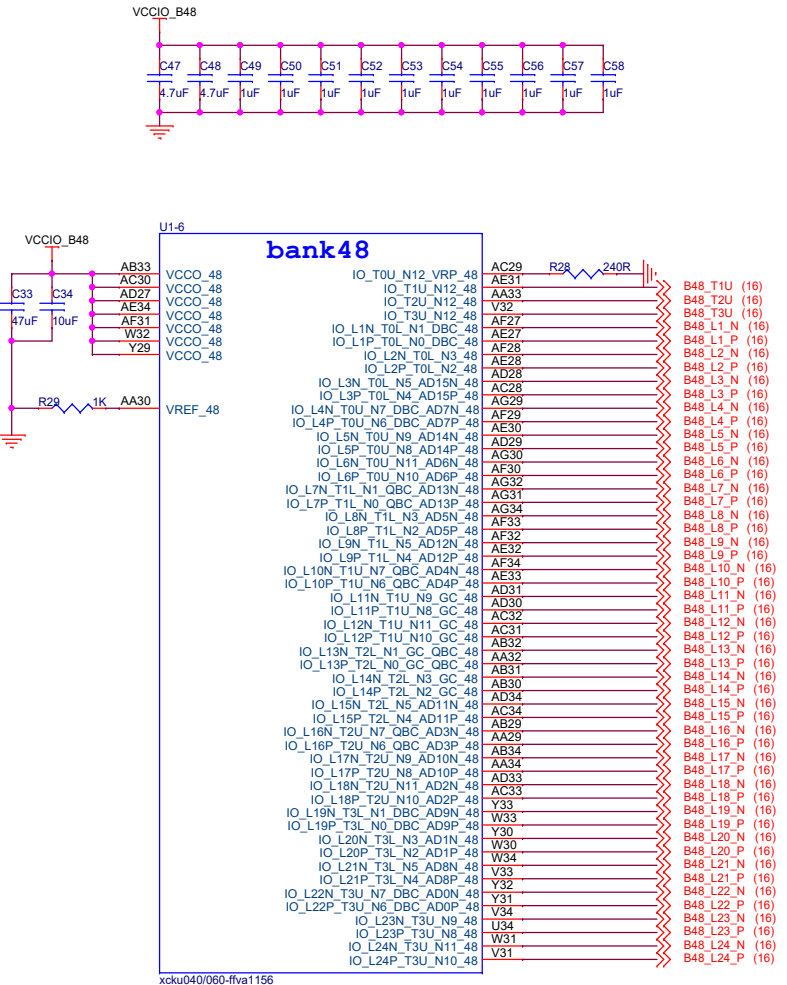
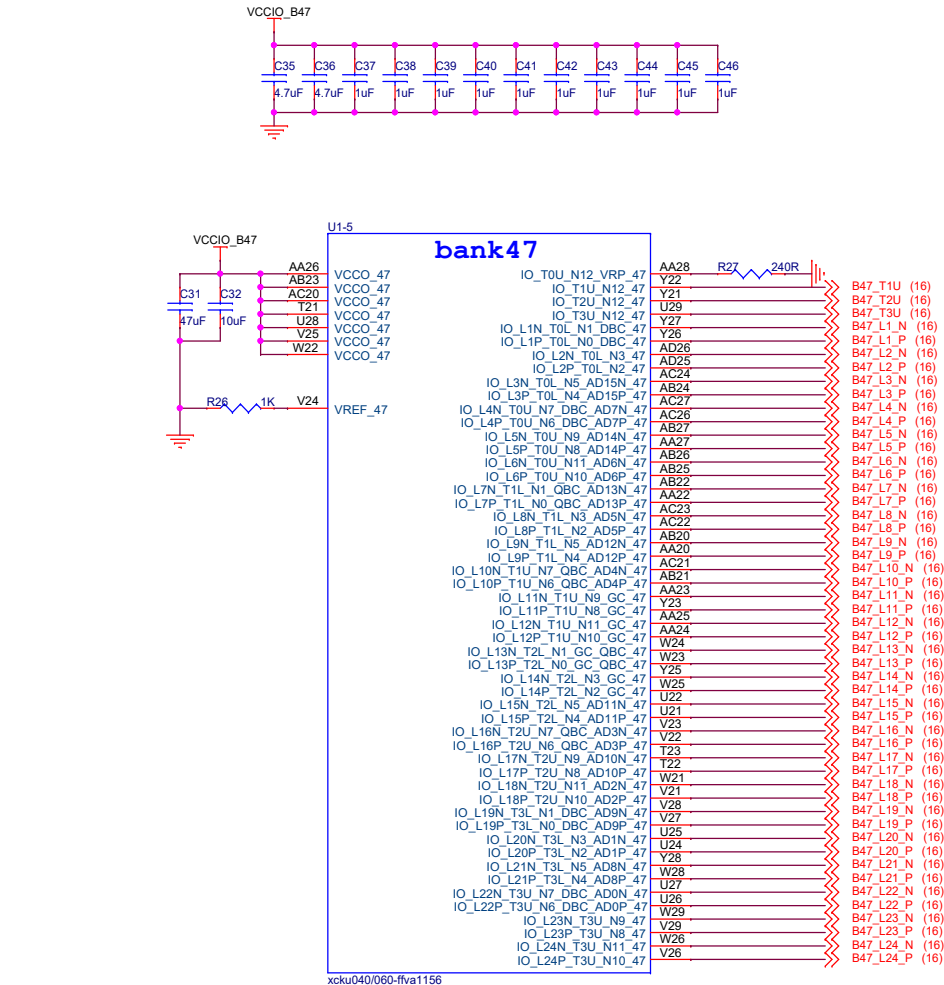


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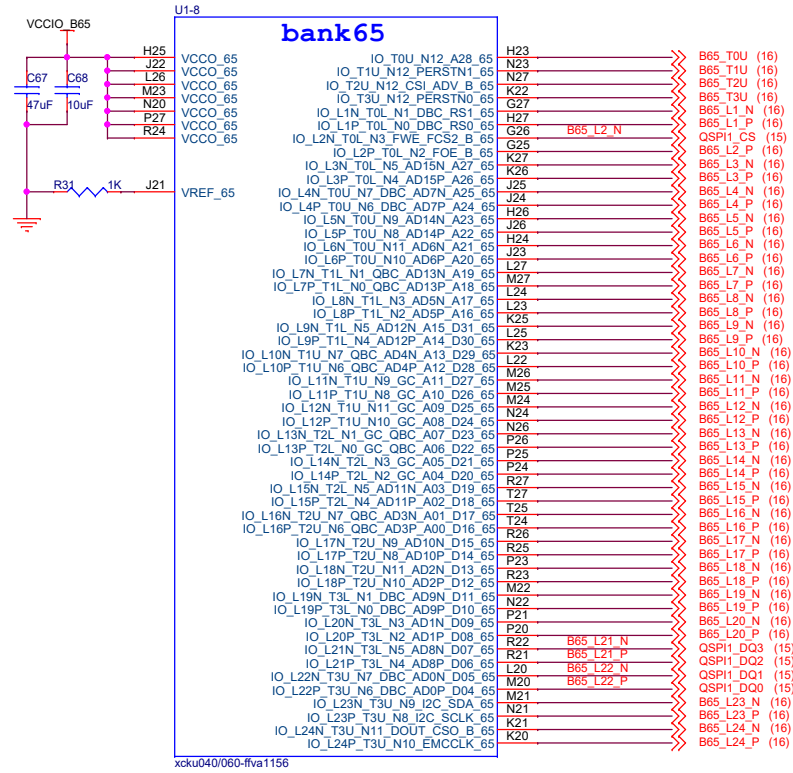
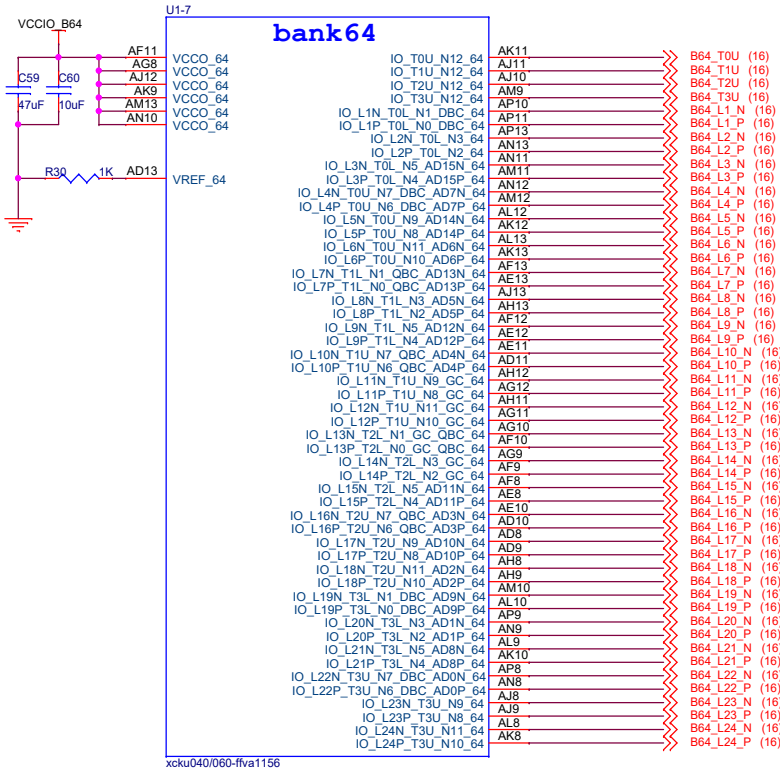
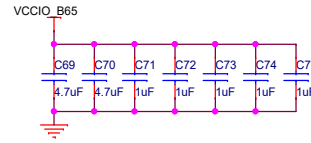
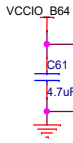
Title:		KU040 CORE 1V0 240809 USER	
Author:	ALIENTEK	Date:	Friday, August 09, 2024
Version:	V1.0	File:	03 BANK46
Size:	A4	Sheet	3 of 19



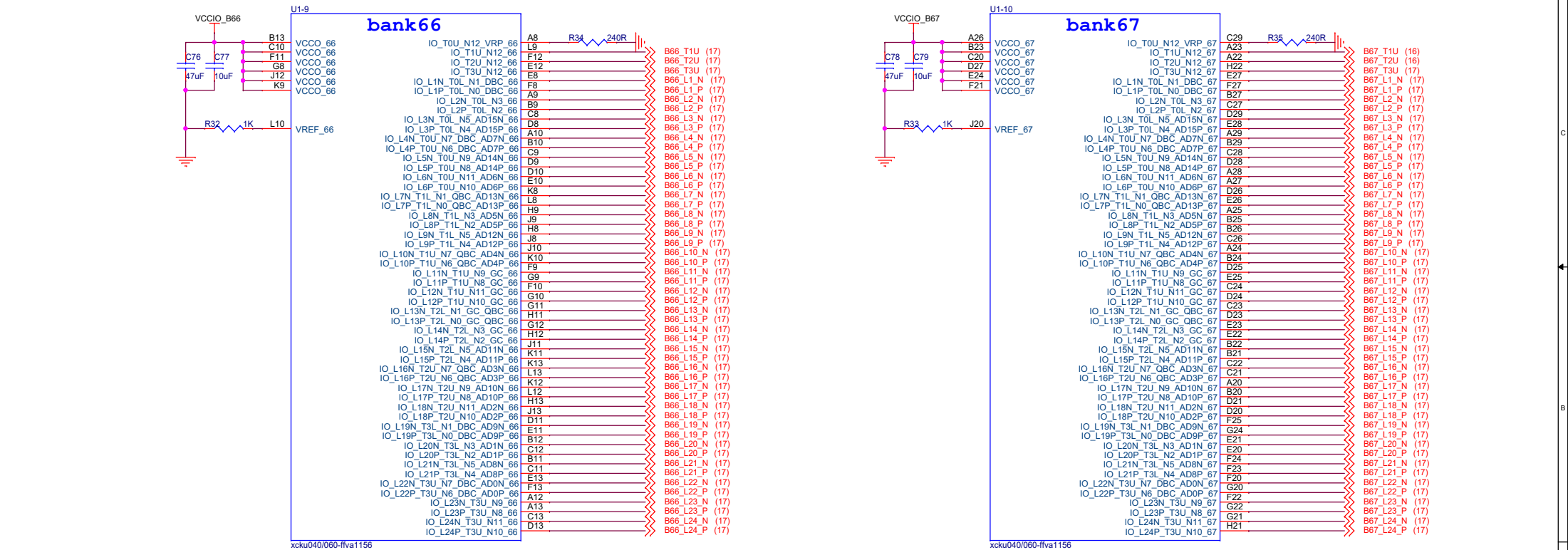
BANK47和BANK48为HP(High Performance) BANK，应用于高性能也就是速度比较高的场景，由于速率比较高，Bank电压最高也只能到1.8V。



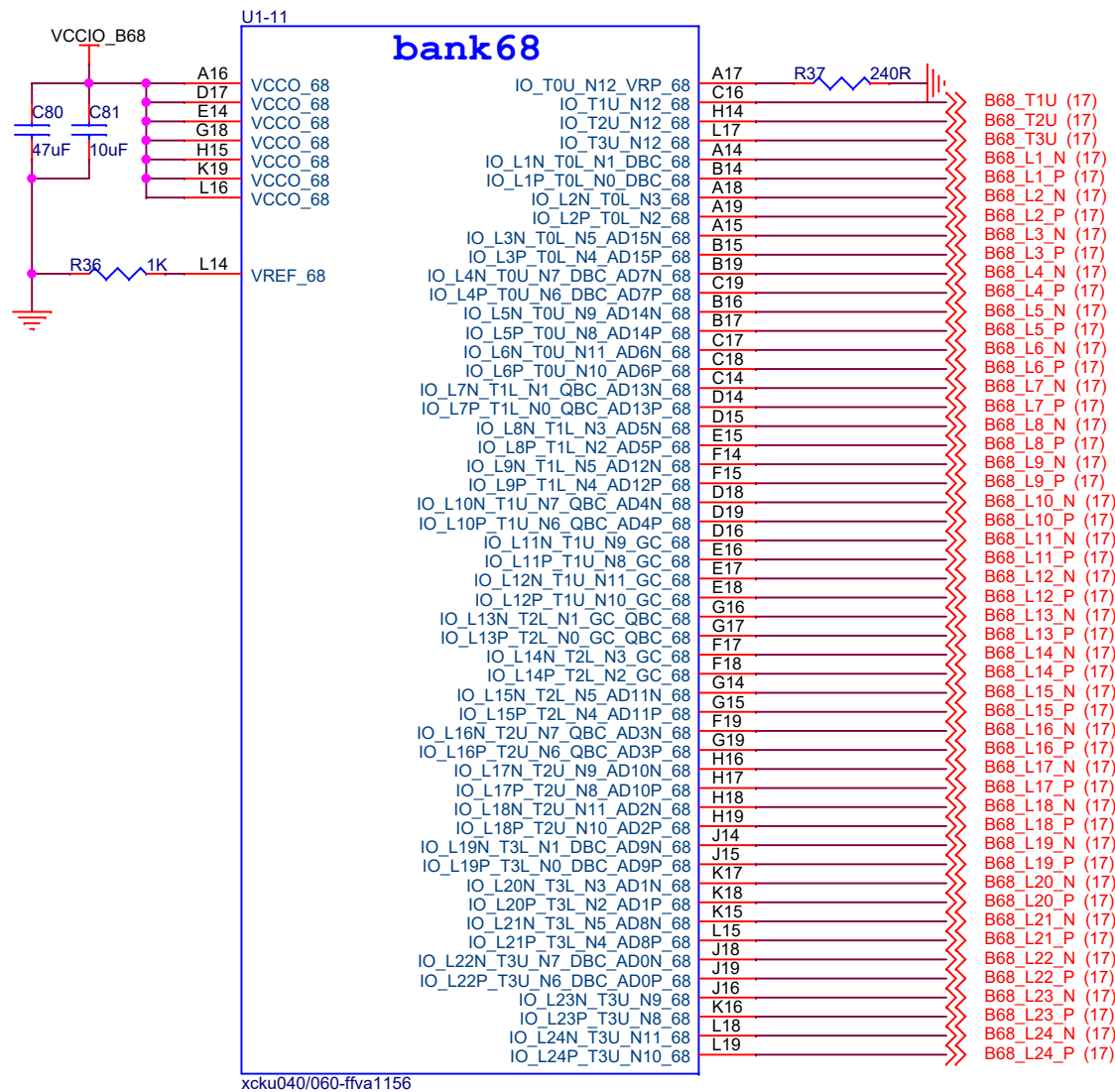
BANK64和BANK65为HR(High Range) BANK，表示支持wider range of I/O standards，最高能够支持到3.3V的电压。



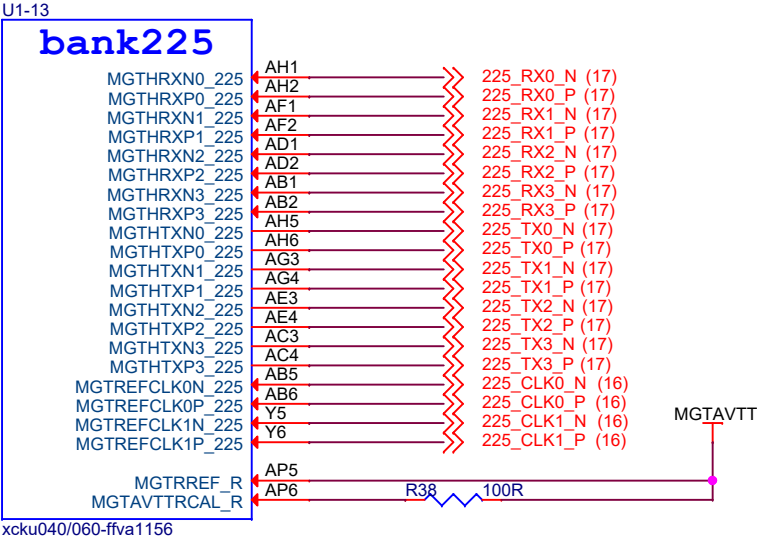
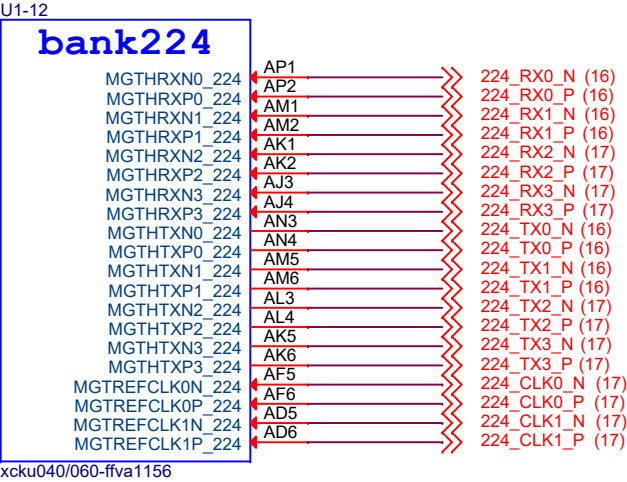
BANK66和BANK67为HP(High Performance) BANK，应用于高性能也就是速度比较高的场景，由于速率比较高，Bank电压最高也只能到1.8V。



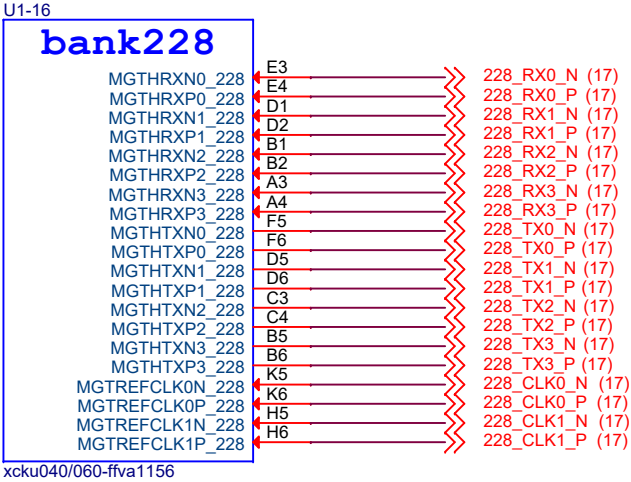
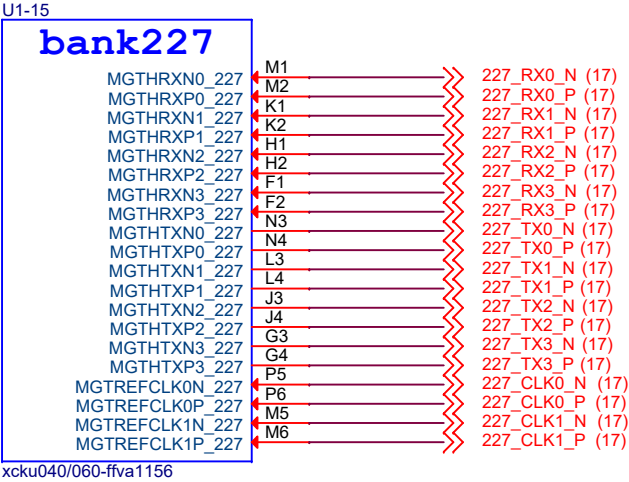
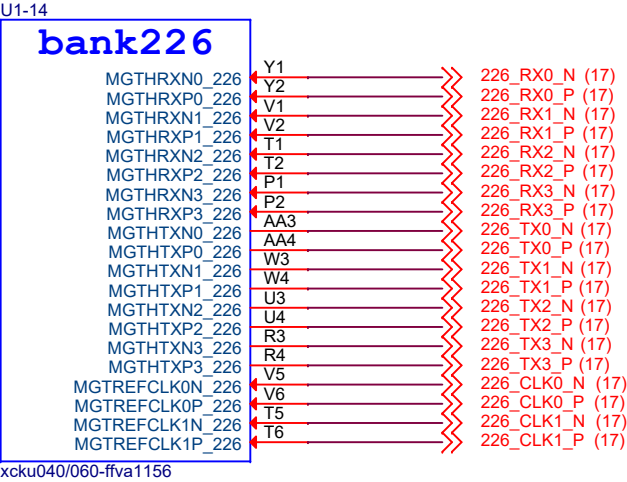
BANK68为HP(High Performance) BANK，应用于高性能也就是速度比较高的场景，由于速率比较高，Bank电压最高也只能到1.8V。



MGT (Multi-gigabit transceiver) ，即高速串行收发器模块



Trace length from the resistor pins to the FPGA pins MGTRREF and MGTAVTTRCAL Connection must be equal in length



U1-18
MGTAVCC_L
MGTAVCC_L_E31
MGTAVCC_L_F30
MGTAVCC_L_H29
MGTAVCC_L_J31
MGTAVCC_L_N31
MGTAVCC_L_P29

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U1-19
MGTAVCC_R
MGTAVCC_R_AC6
MGTAVCC_R_AE6
MGTAVCC_R_AG6
MGTAVCC_R_AJ6
MGTAVCC_R_AL6
MGTAVCC_R_AN6
MGTAVCC_R_C6
MGTAVCC_R_E6
MGTAVCC_R_G6
MGTAVCC_R_J6
MGTAVCC_R_L6
MGTAVCC_R_N6
MGTAVCC_R_U6
MGTAVCC_R_W6

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U1-20
MGTAVTT_L
MGTAVTT_L_C32
MGTAVTT_L_D33
MGTAVTT_L_G32
MGTAVTT_L_H33
MGTAVTT_L_K33
MGTAVTT_L_L32
MGTAVTT_L_P32
MGTAVTT_L_R32

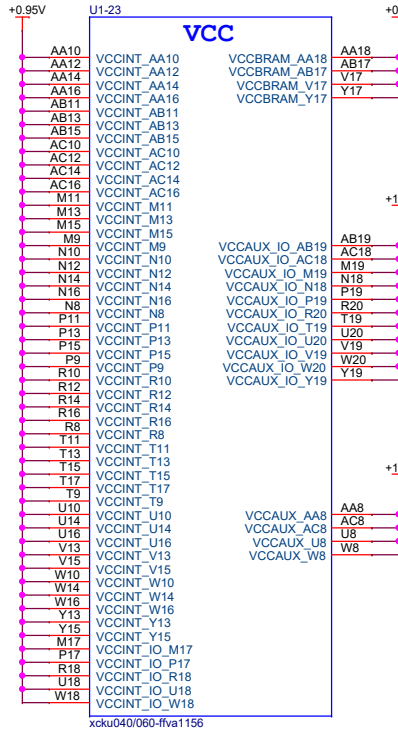
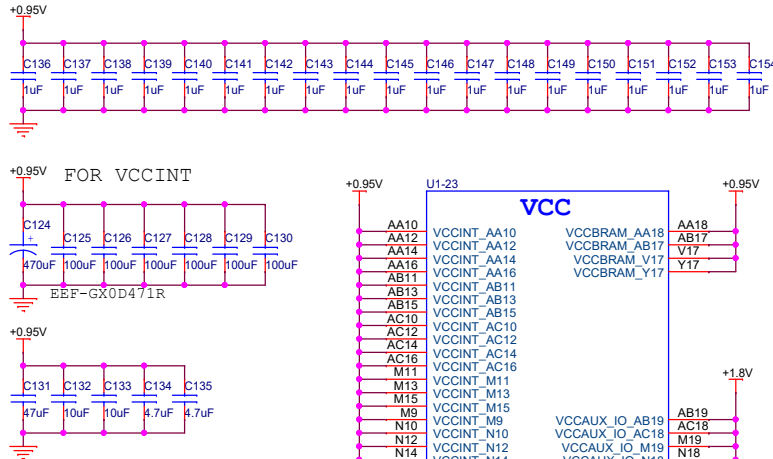
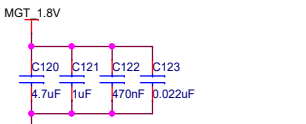
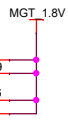
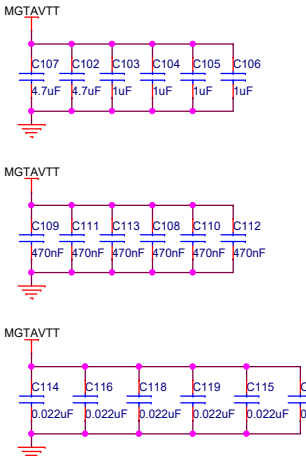
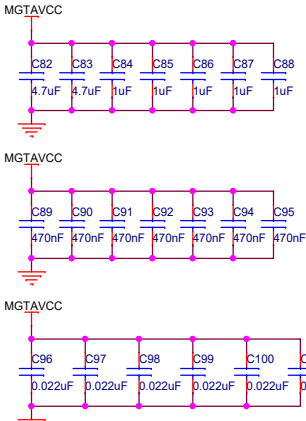
xcku040/060-fvva1156

U1-21
MGTAVTT_R
MGTAVTT_R_AC2
MGTAVTT_R_AD3
MGTAVTT_R_AG2
MGTAVTT_R_AH3
MGTAVTT_R_AL2
MGTAVTT_R_AM3
MGTAVTT_R_C2
MGTAVTT_R_D3
MGTAVTT_R_G2
MGTAVTT_R_H3
MGTAVTT_R_L2
MGTAVTT_R_M3
MGTAVTT_R_R2
MGTAVTT_R_T3
MGTAVTT_R_W2
MGTAVTT_R_Y3

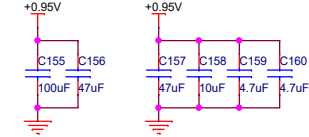
xcku040/060-fvva1156

U1-22
MGTVCCAUX
MGTVCCAUX_L_K29
MGTVCCAUX_L_M29
MGTVCCAUX_R_AA6
MGTVCCAUX_R_R6

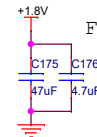
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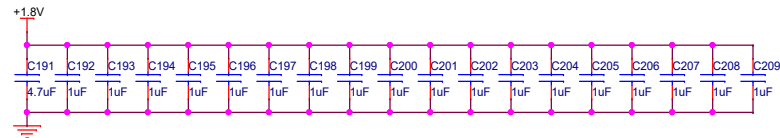
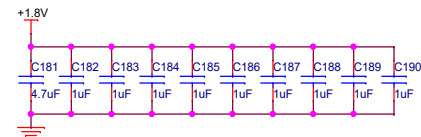
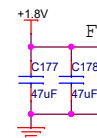
FOR VCCBRAM

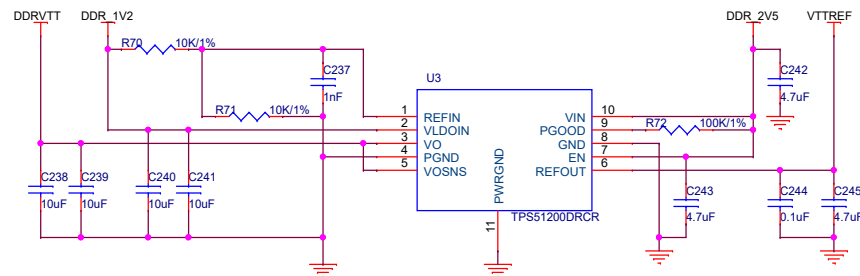
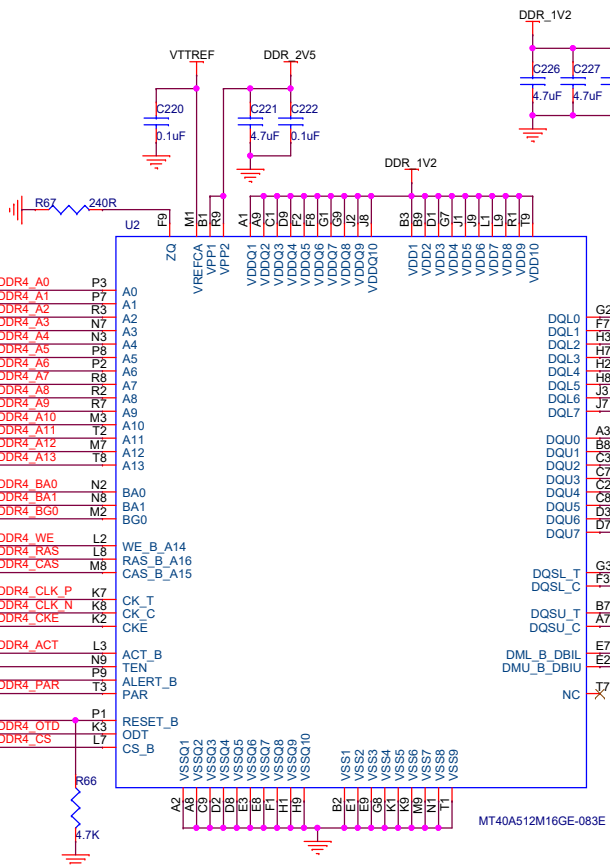
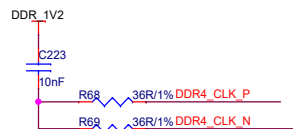
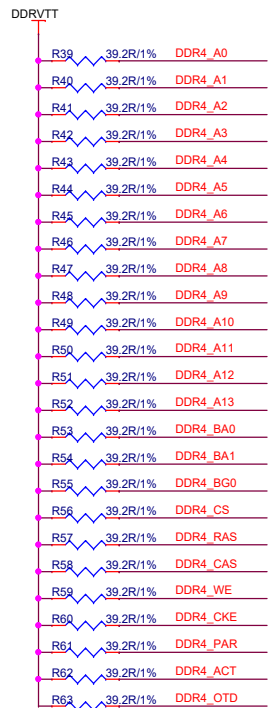
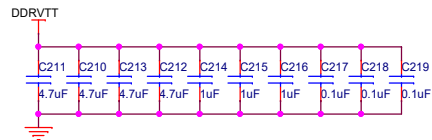


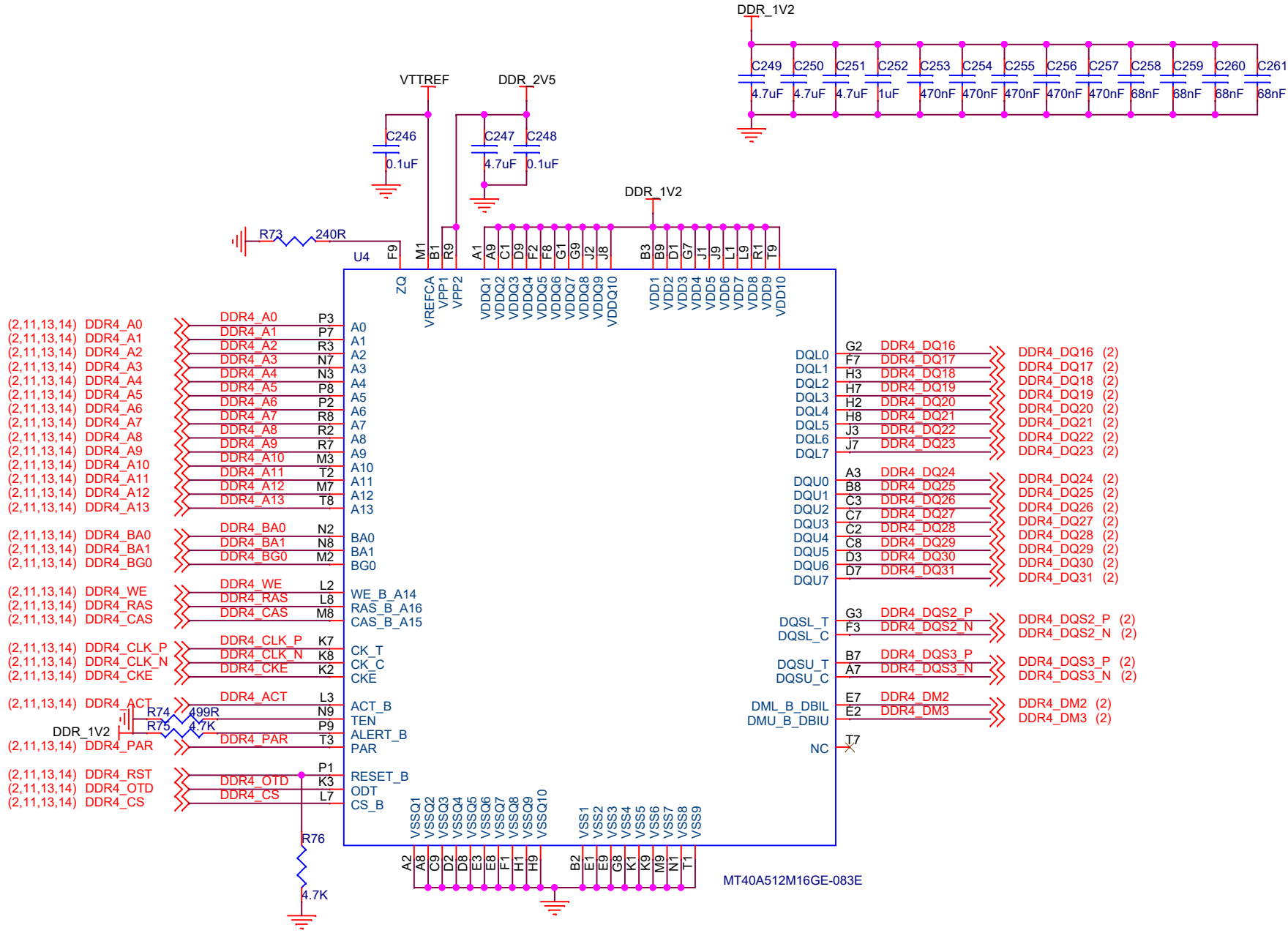
FOR VCCAUX_IO



FOR VCCAUX

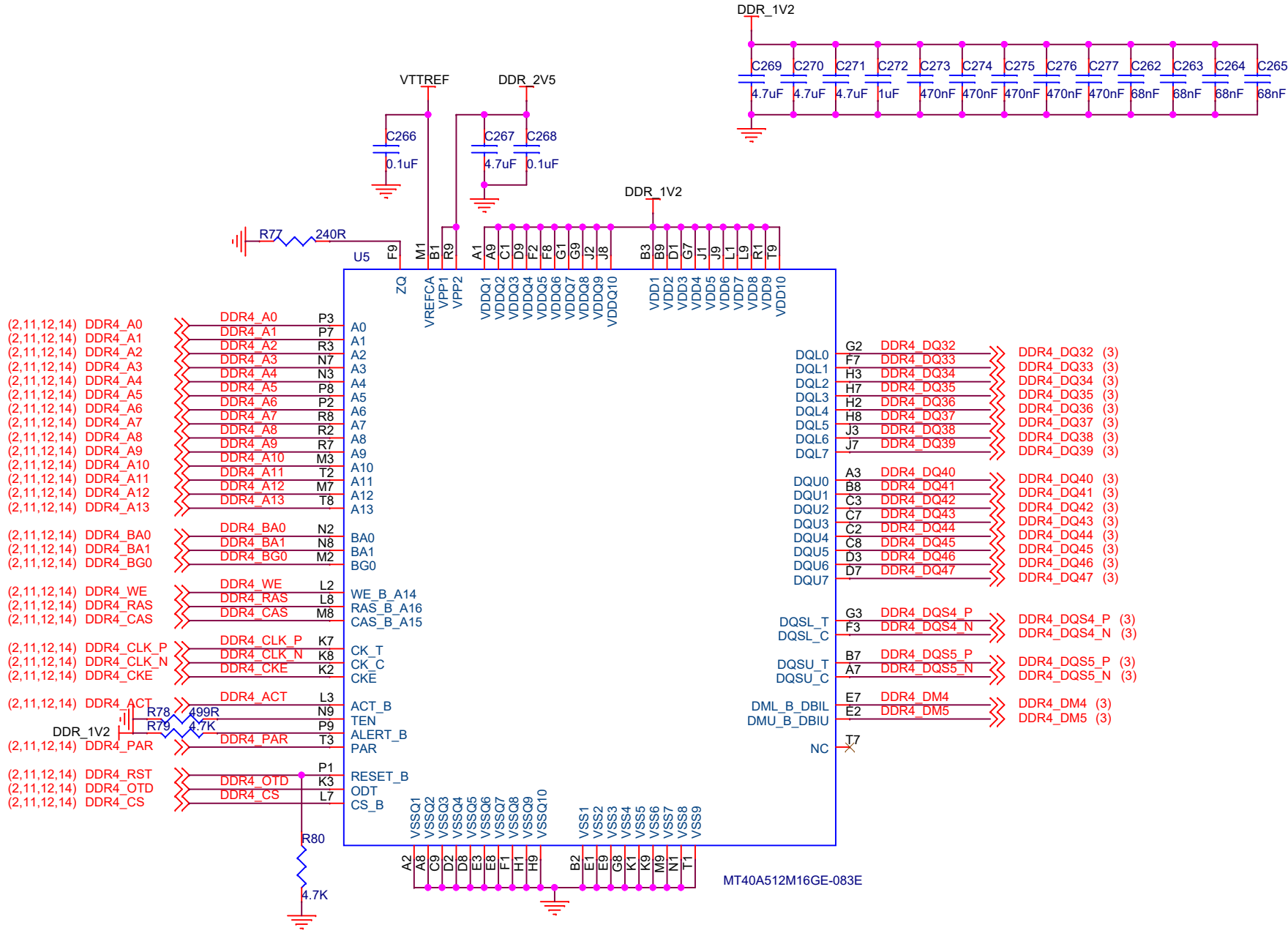


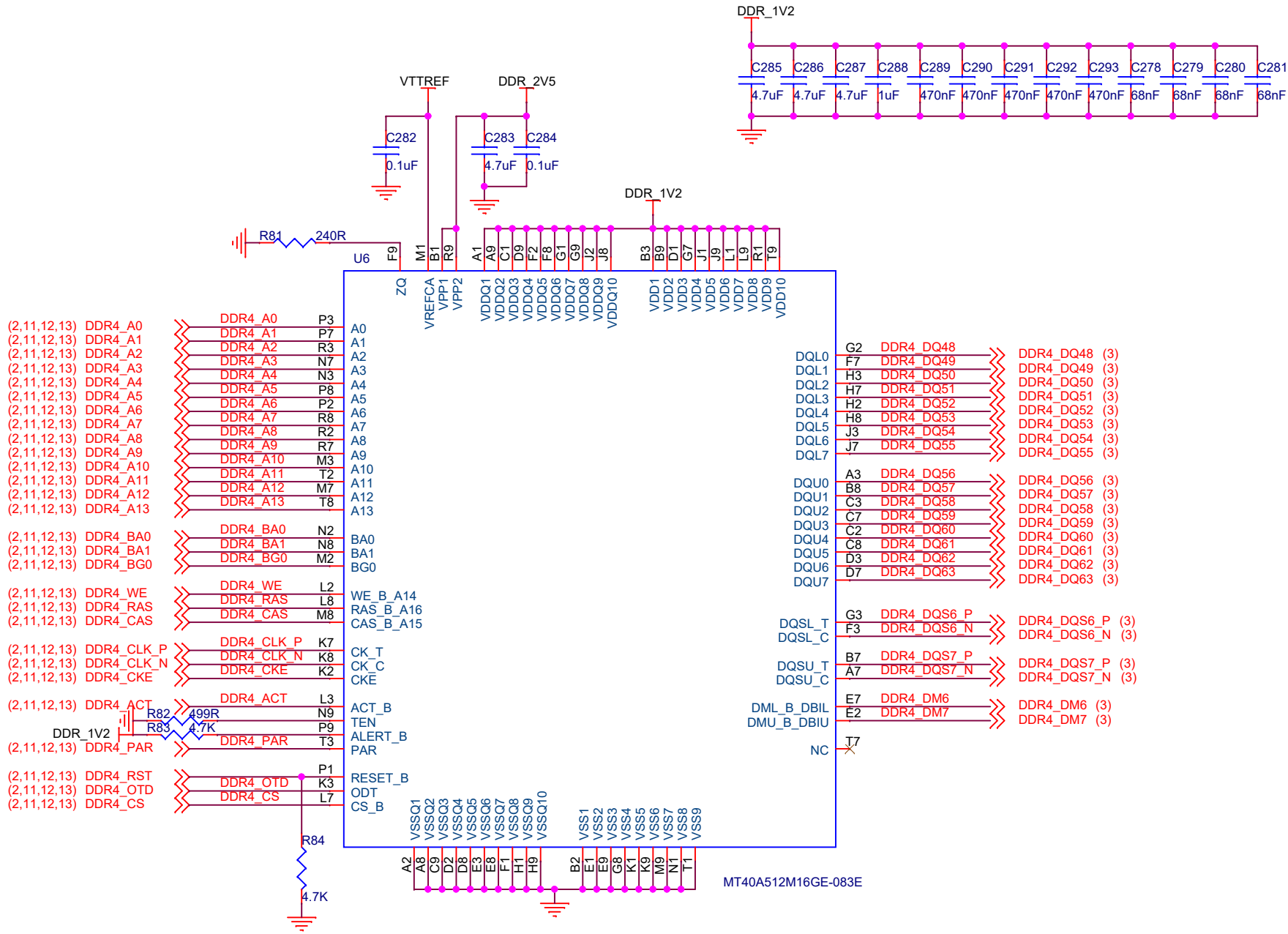


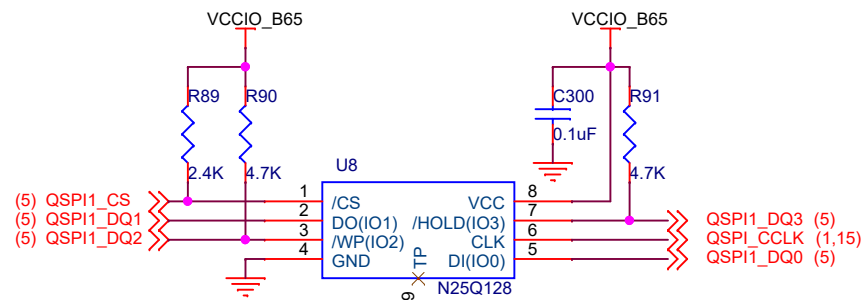


Title:		KU040 CORE 1V0 240809 USER	
Author:	ALIENTEK	Date:	Thursday, July 25, 2024
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Size:	A4	Sheet	12 of 19









正点原子





+12V → +0.95V → +1.8V MGTAVCC →
 DDR_1V2 +3.3V MGTAVTT → MGT_1.8V DDR_2V5

